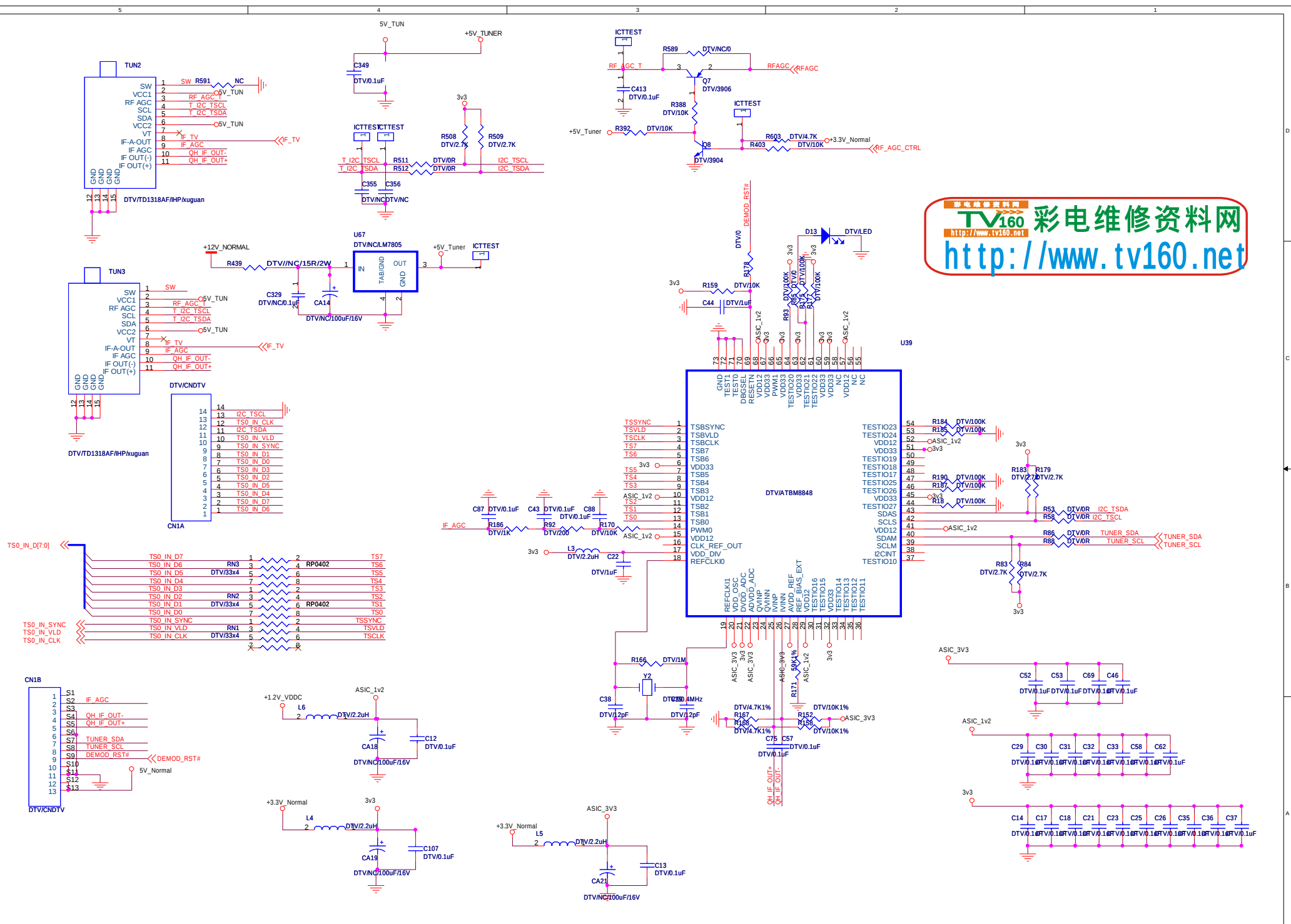
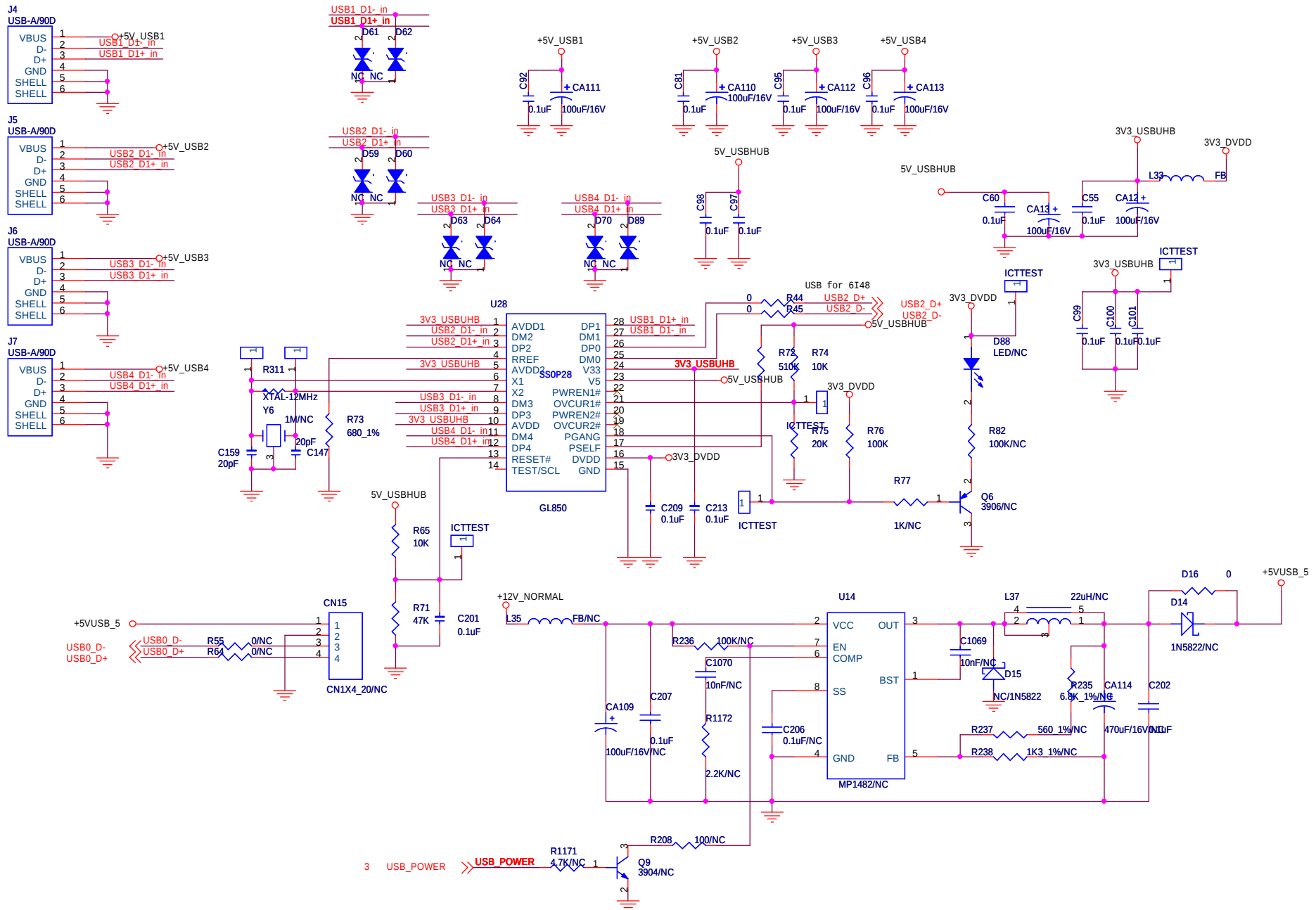


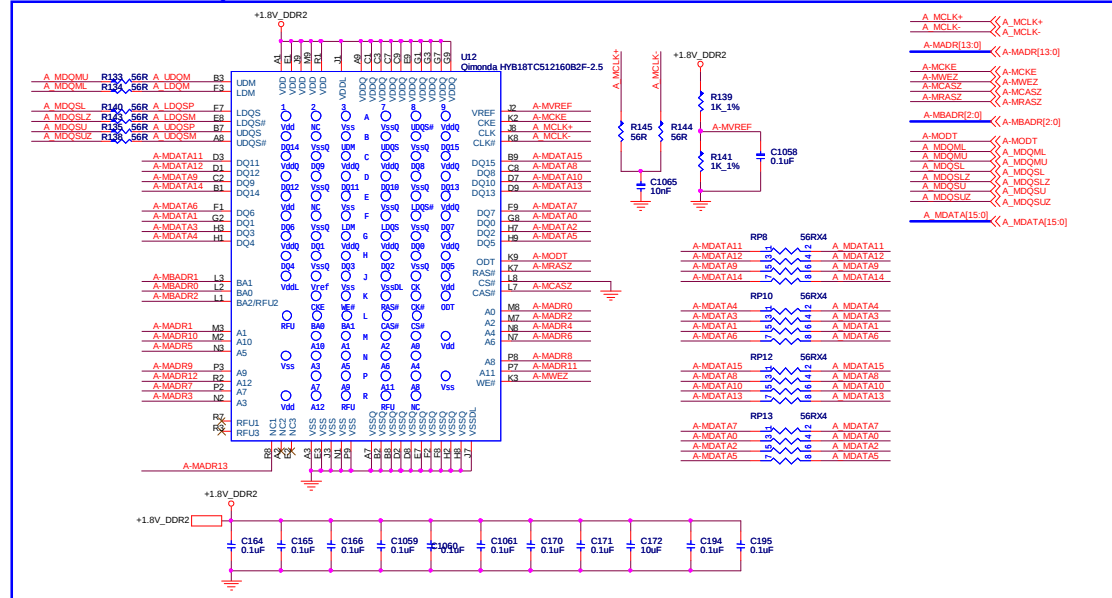


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TV160 彩电维修资料网
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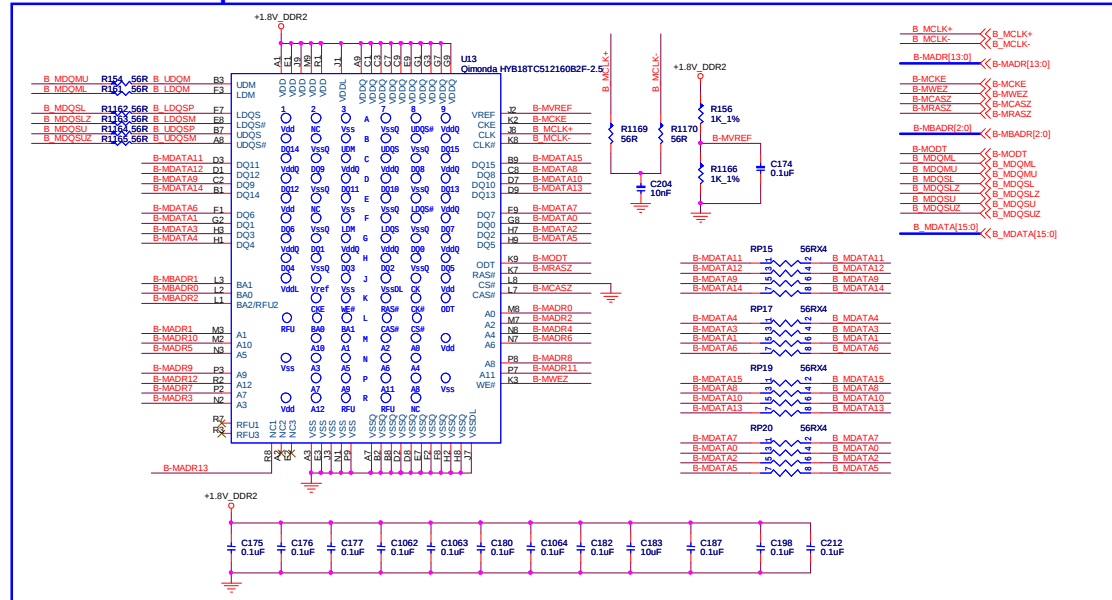




1st DDR2 Chip



2nd DDR2 Chip

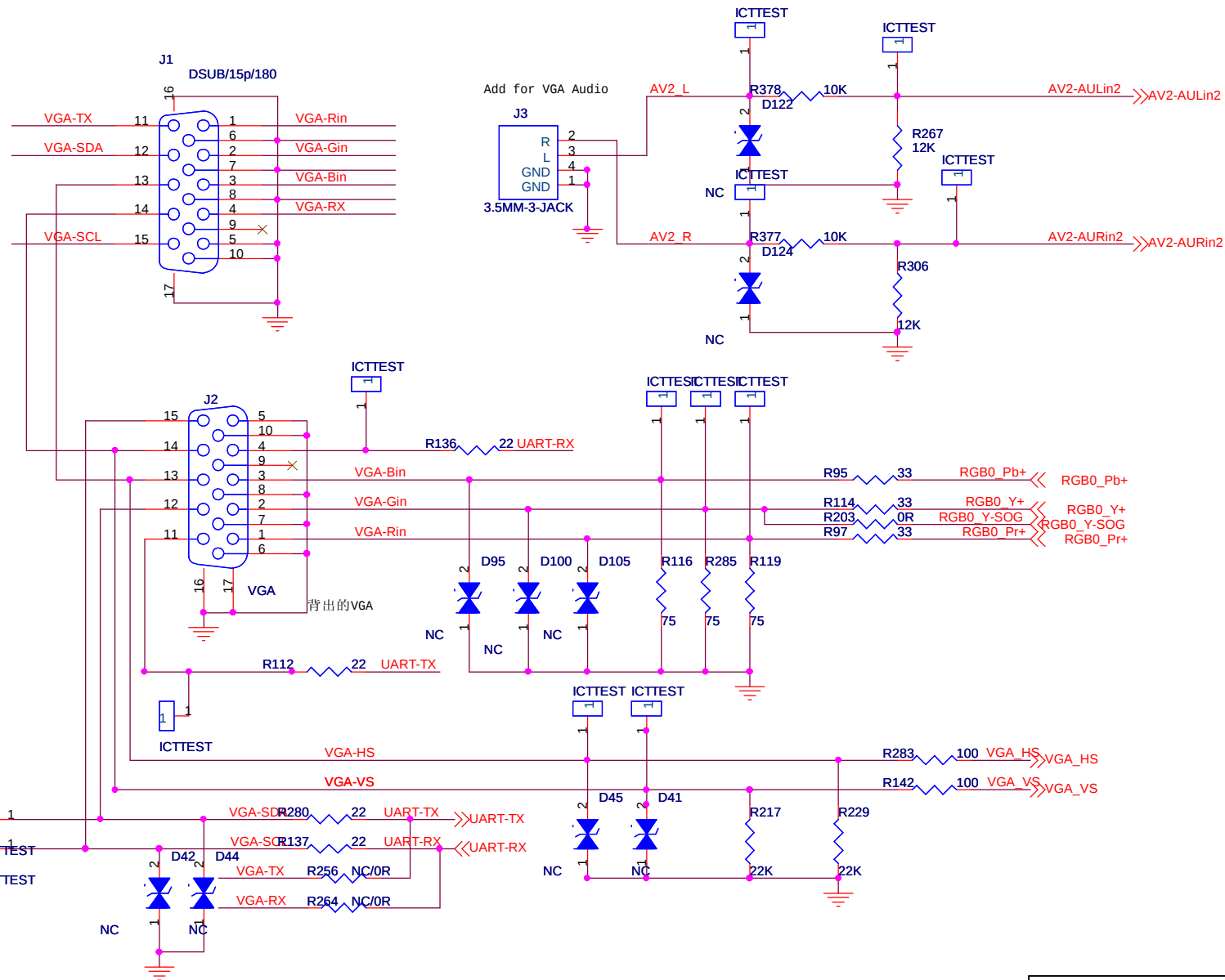


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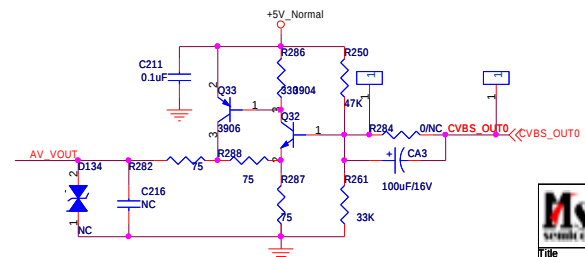
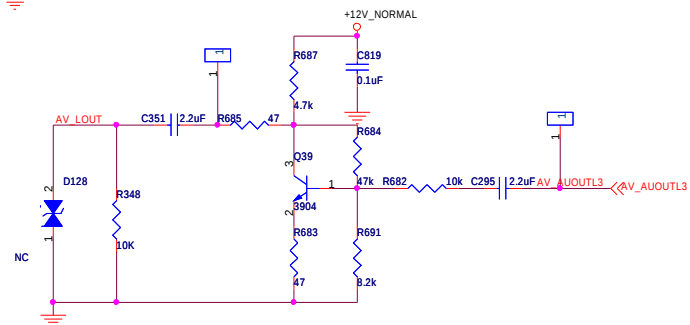
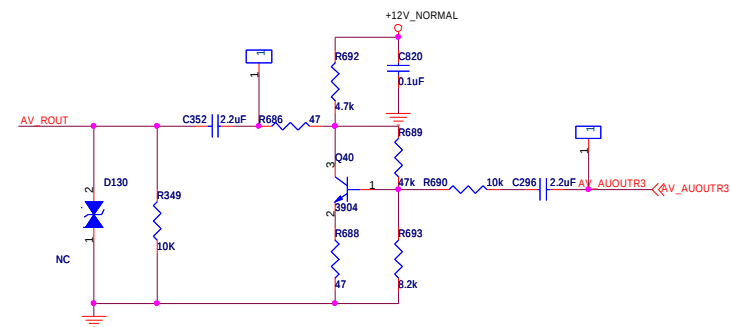
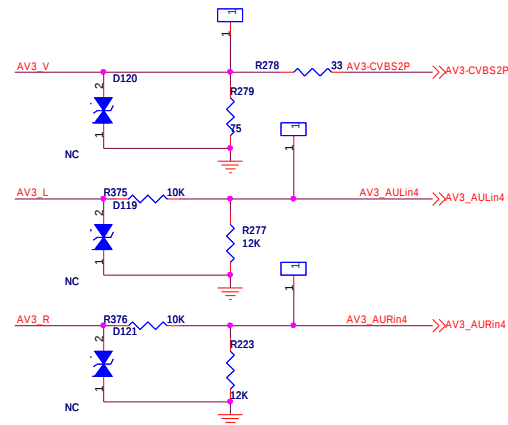
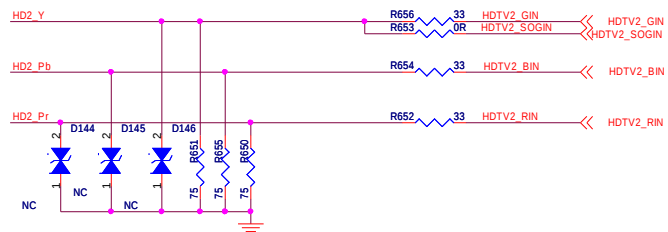
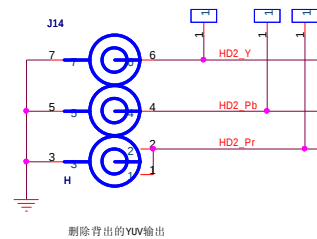
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4F-1, No. 26, Tai-Yuan St., Chupei,
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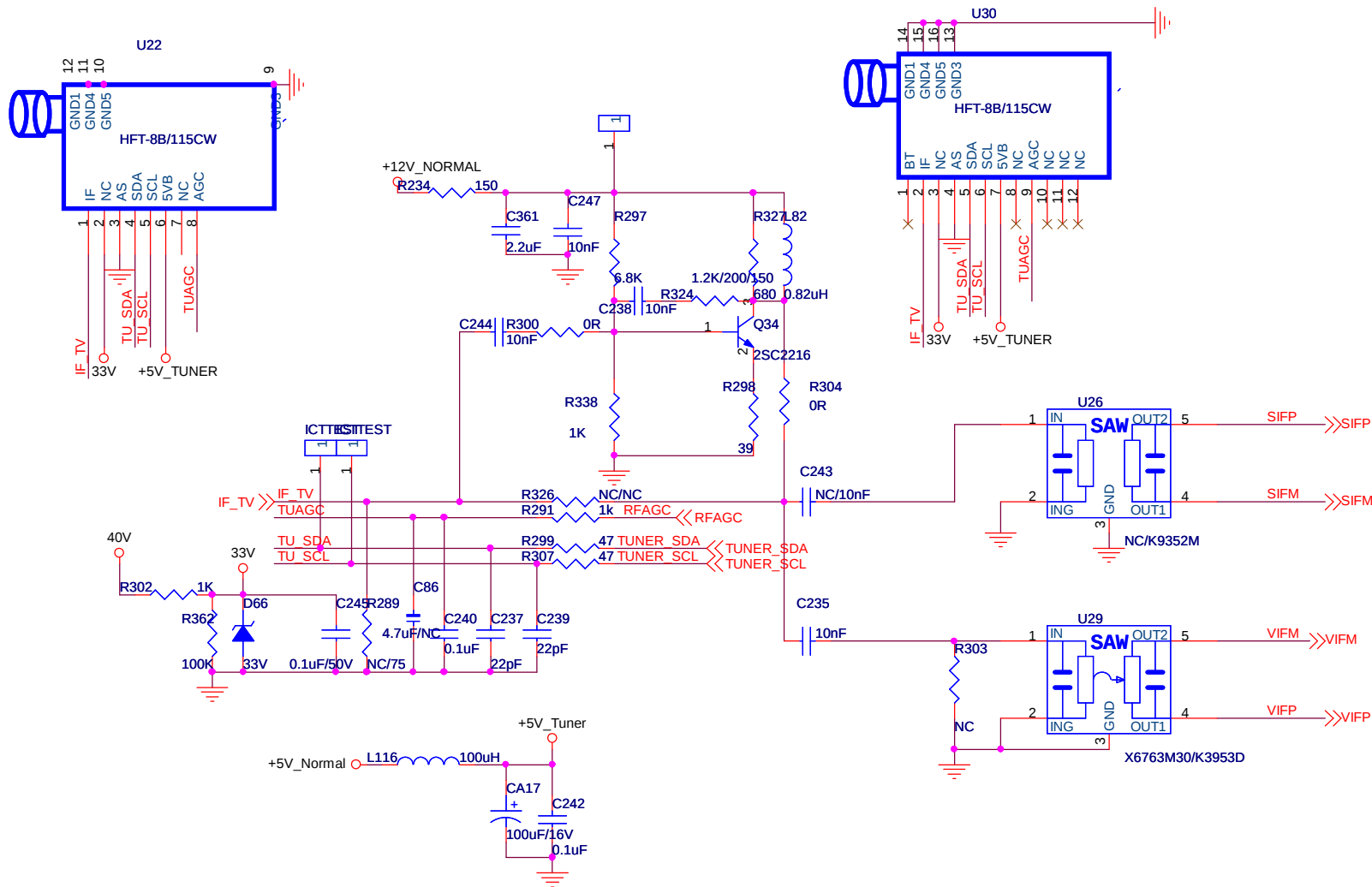
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Rev: **1.0**

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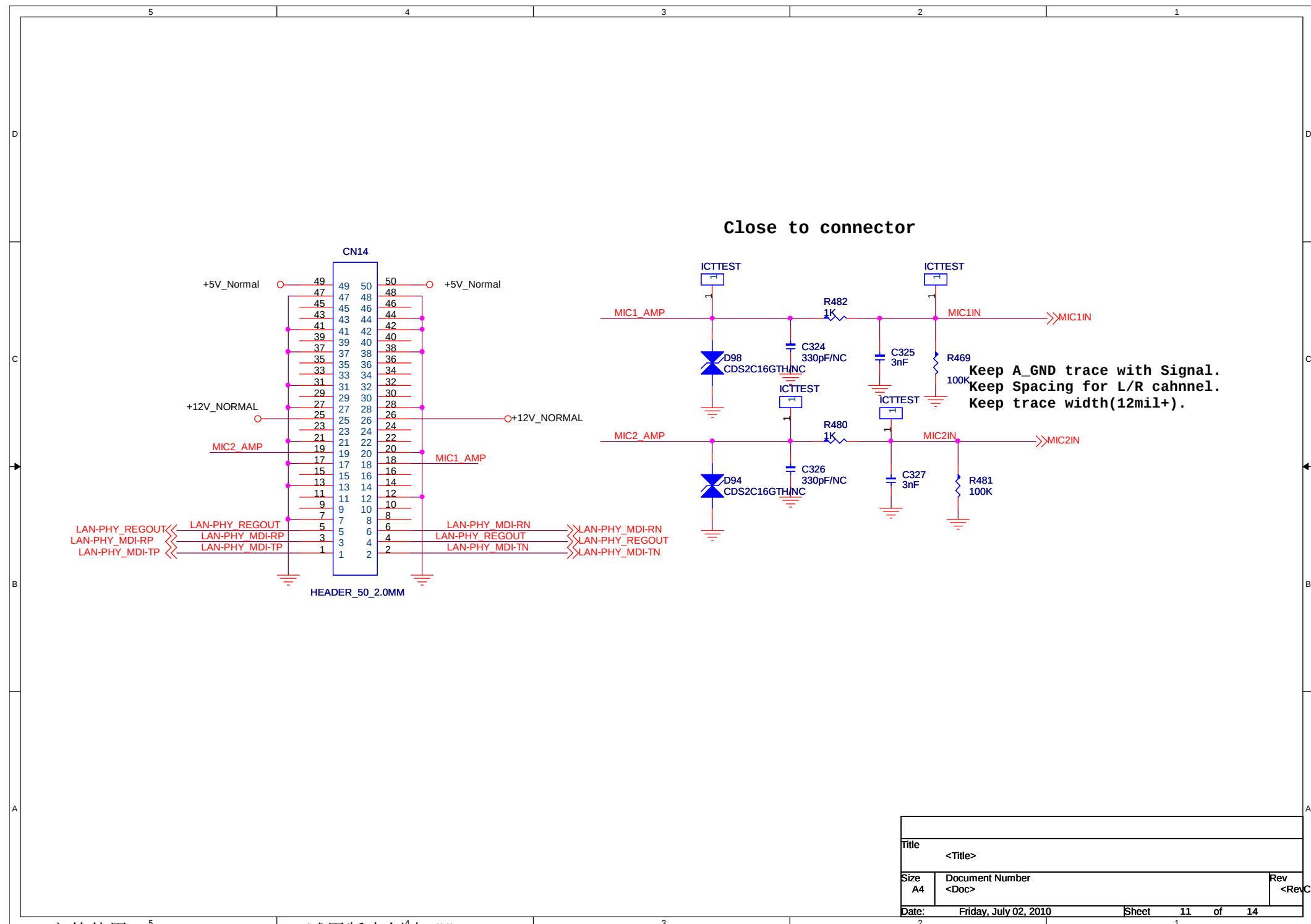


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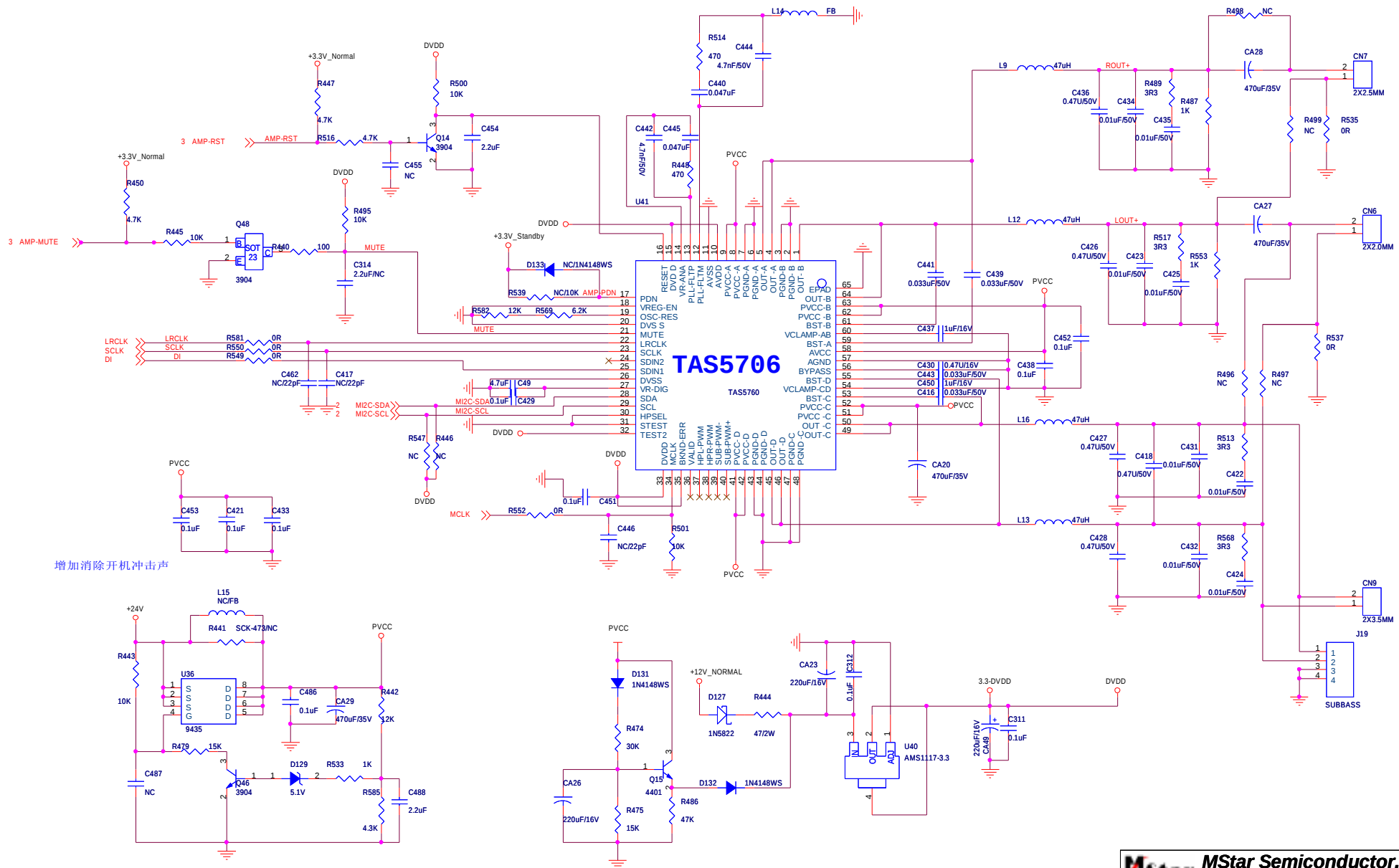


Close to connector

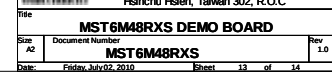
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Keep Spacing for L/R cahnnel.
Keep trace width(12mil+).

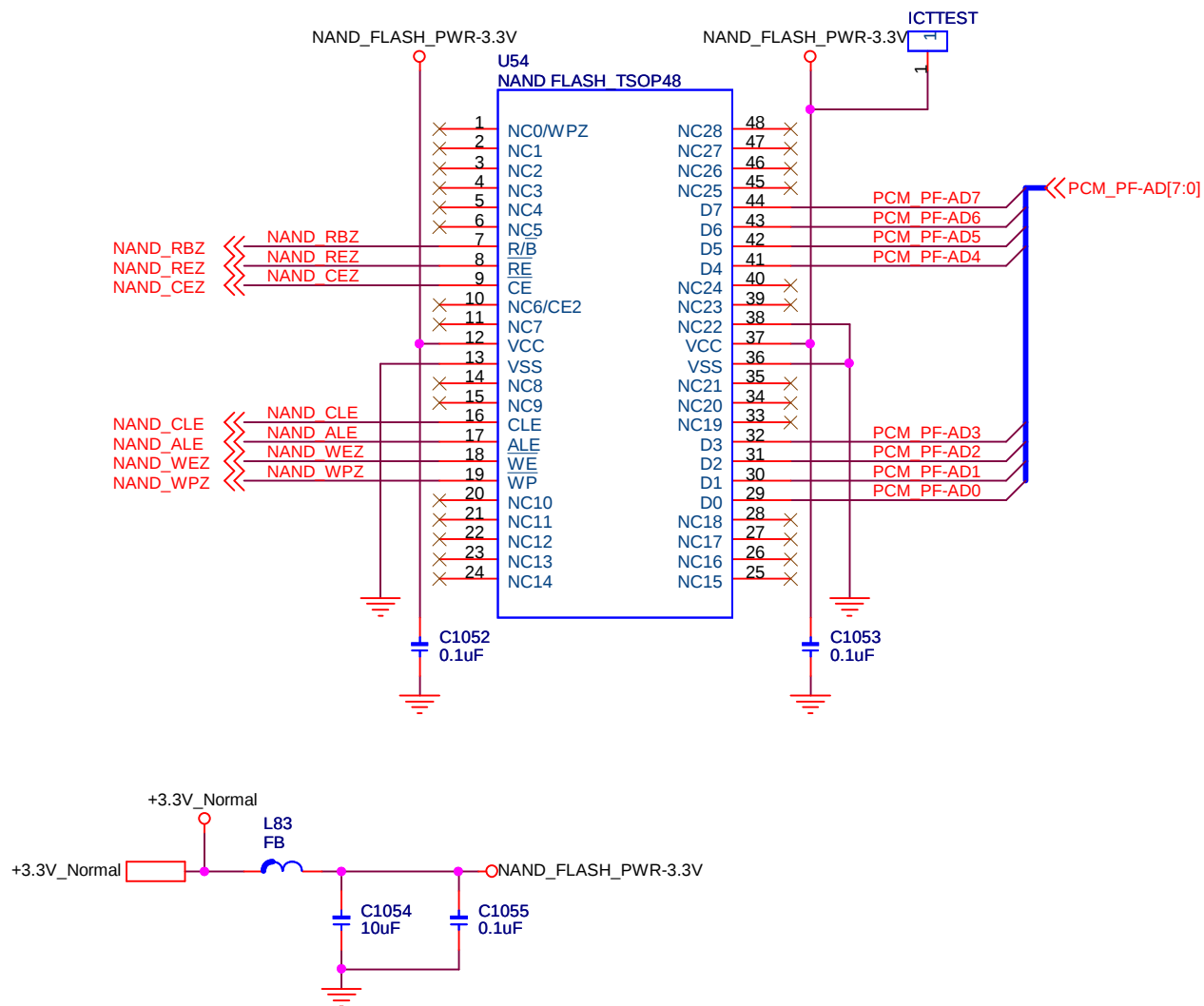
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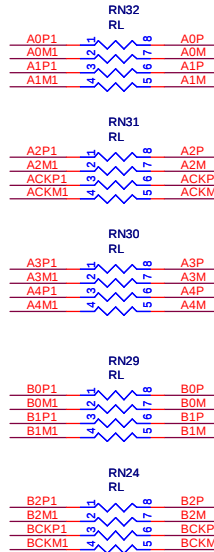
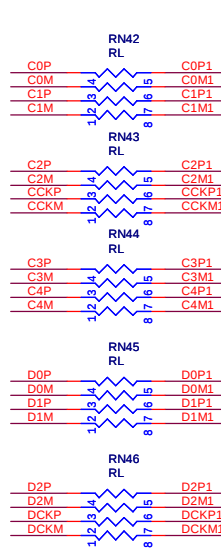
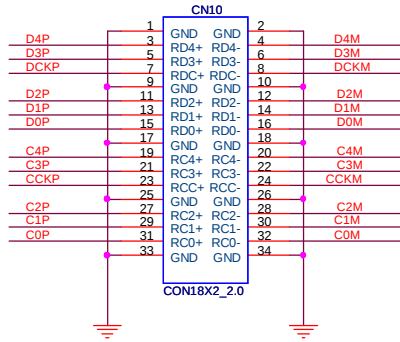
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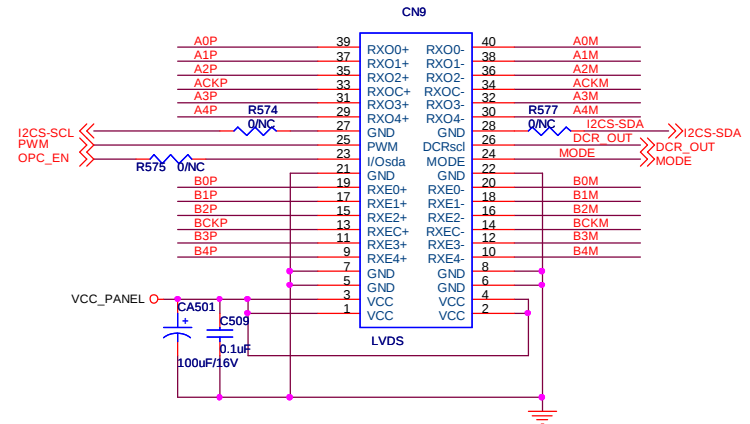


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LVDS OUT1

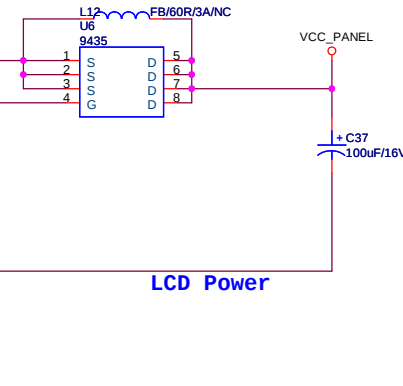
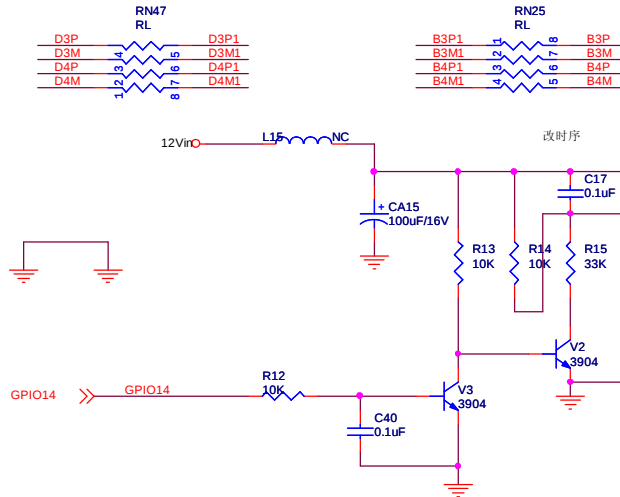
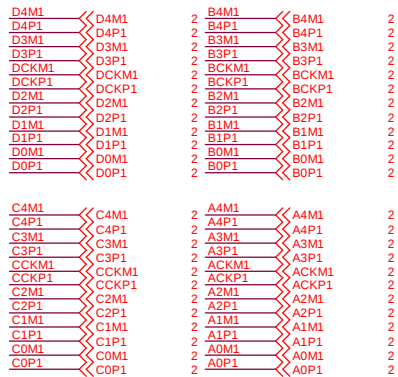


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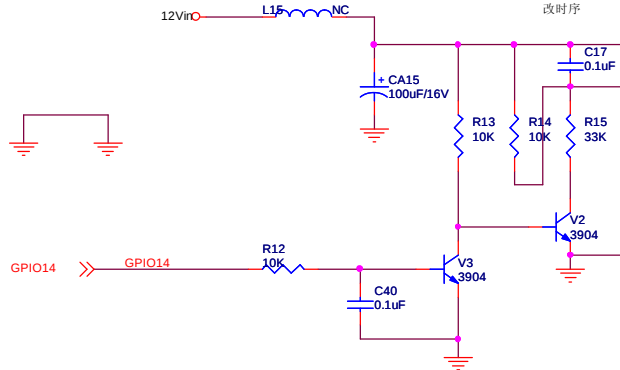


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LVDS_SEL
L: VESA

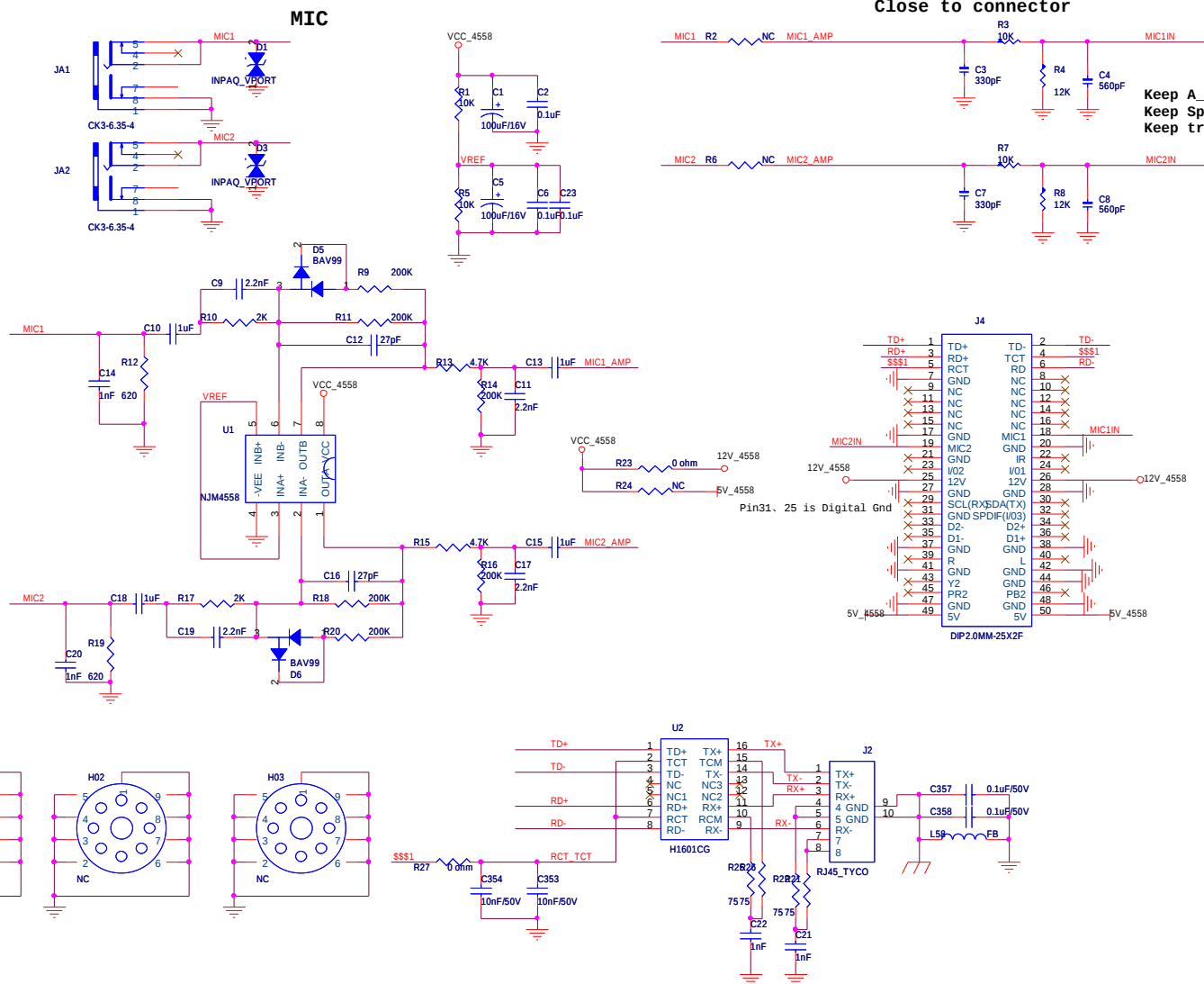
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DCR_ENABLE
L: DISABLE



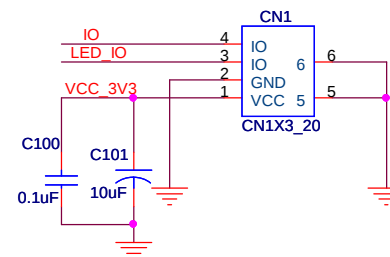
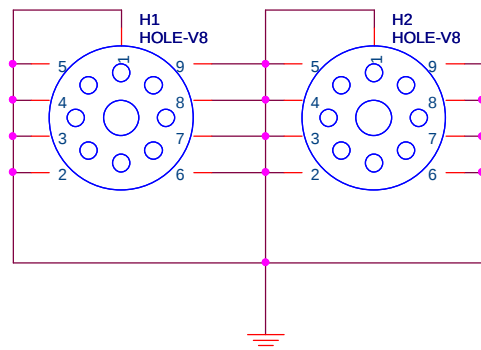
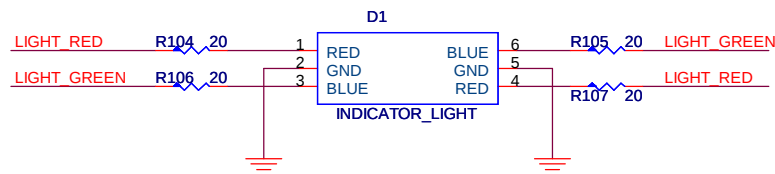
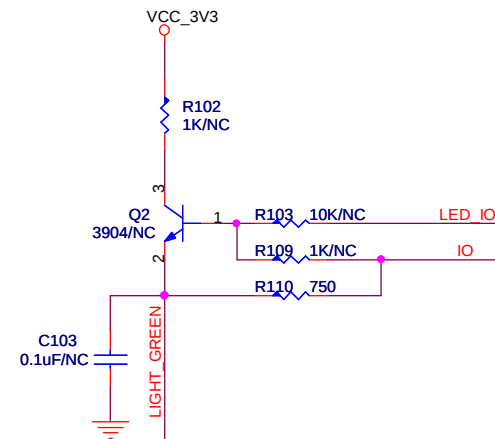
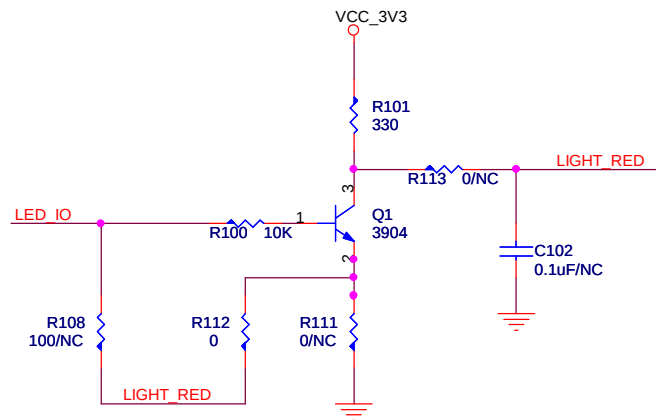
LCD Power



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Title				
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Size	Document Number			Rev
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Date:	Tuesday, June 22, 2010		Sheet	1 of 1



Title		
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A4	<Doc>	<RevCode>
Date:	Friday, May 28, 2010	Sheet 1 of 1