

- 1. Page4. Version1.4: 增加USB的ESD二极管
- 2. Page7. 将ethernet phy由ST. ste100p改为 Realtek RTL8201CP
- 3. Page3 . 将DDR2由K4T51163QE-ZC (L) E7/F7改为K4T1G164QD-ZC (L) E7/F7，容量增加一倍，bank增加至8

V1.4.2

- 1. VTTDDR_REF 修改为对1.8V电源通过两个等值电阻分压产生；
- 2. 由于需要模拟音频信号的输入及输出，所以将audio DAC重新改回到audio codec；
- 3. 修改接口定义，去除TS0，TS1，UART2，I2C1，GPIO1-7，OFFCHIP_INT。增加模拟左右声道音频输入。
- 4. 增加兼容4GB Nandflash电路；增加兼容大于16Mbit SPI flash 封装

V1.4.3

- 1. audio增加micphone输入；

V1.4.7

- 1. 增加调试电路；

V1.4.8

- 1. 3.3V的输入电源选用USB的5V电源，因为LM1117无使能开关；

V1.4.9

- 1. 增加i2c1对3.3V上拉电阻；
- 2. 增加i2c2和GPIO的对5V的上拉电阻；
- 3. 删除U708，U704的网络EN1，分别使能；
- 4. 电池充电使用5V_USB电源

Ver00.01

- 1、把系统的DVDD_33分离开；
- 2、把电容更改成c0402封装的；

Ver00.02_13042009

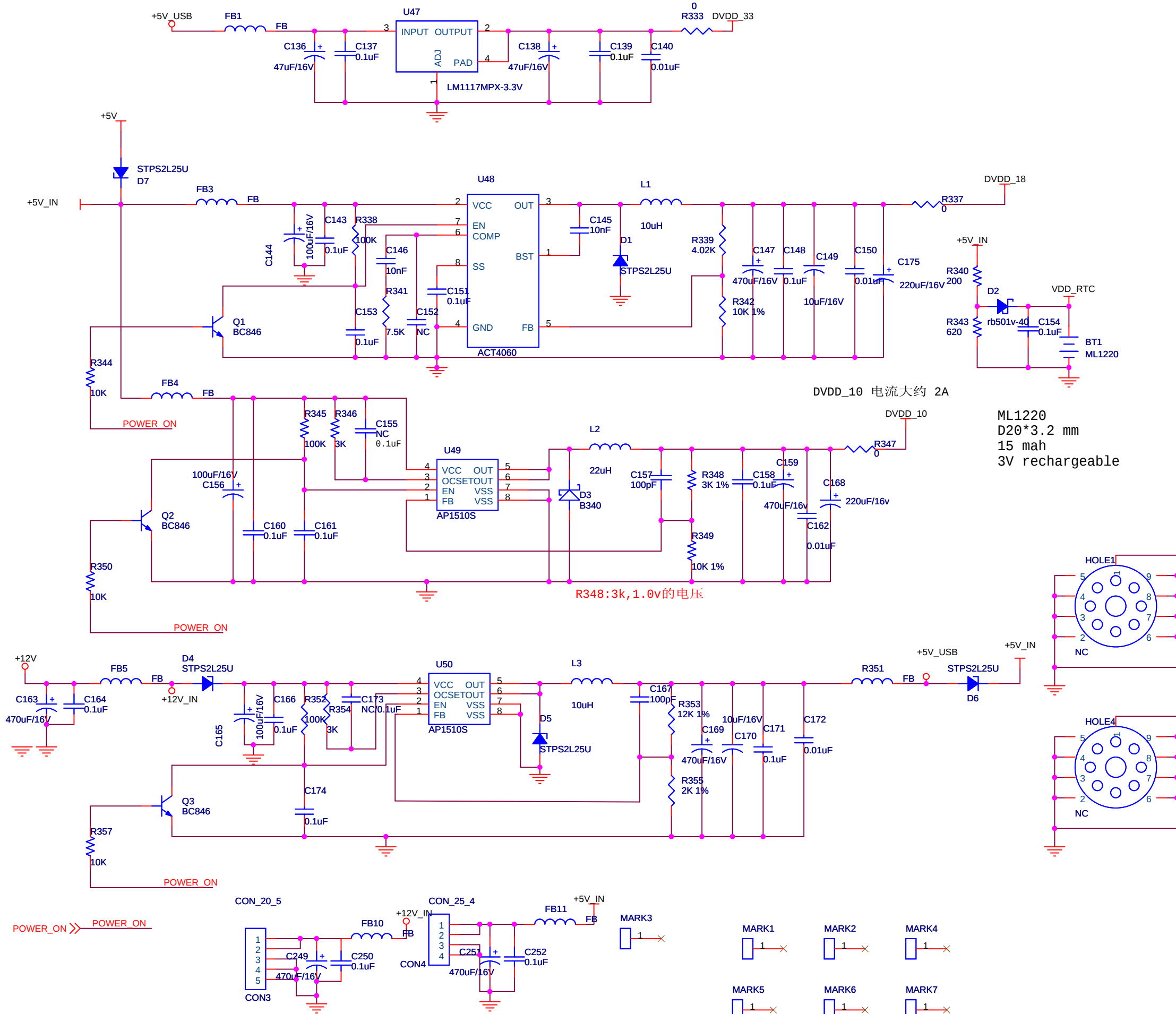
- 1、del;J124、U13、J125；
- 2、change:DDR2 layout；
- 3、在12V_IN上加滤波电容；

Ver_05082009

- 1、Audio 的 I2S_MCLK 串 22 ohm 电阻；

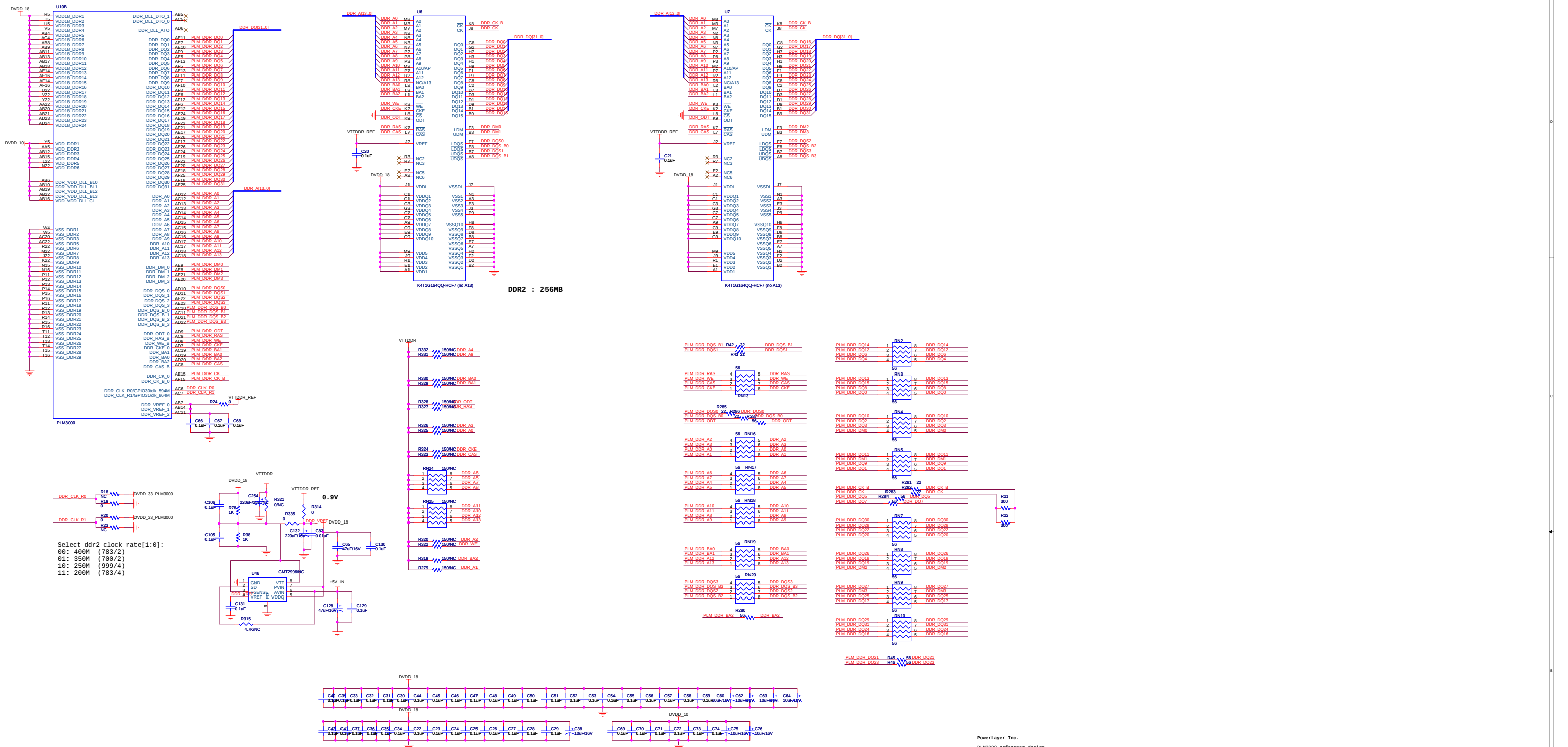
DDR2-800 Memory Max 512MB (Page3)	PLM3000 (Page4)	Audio codec (Page5)
Interface with MB (Page8)		Ethernet (Page7)
Power 1.0V 1.8V 3.3V Vref VRTC (Page2)		NandFlash+SPI Flash NorFlash (Page6)

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PLM3000 reference design
 page2: Power

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PLM3000 reference design
Page3: DDR2 & PLM3000 DDR2 Interface

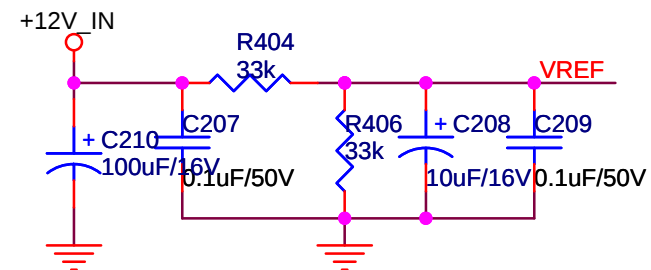
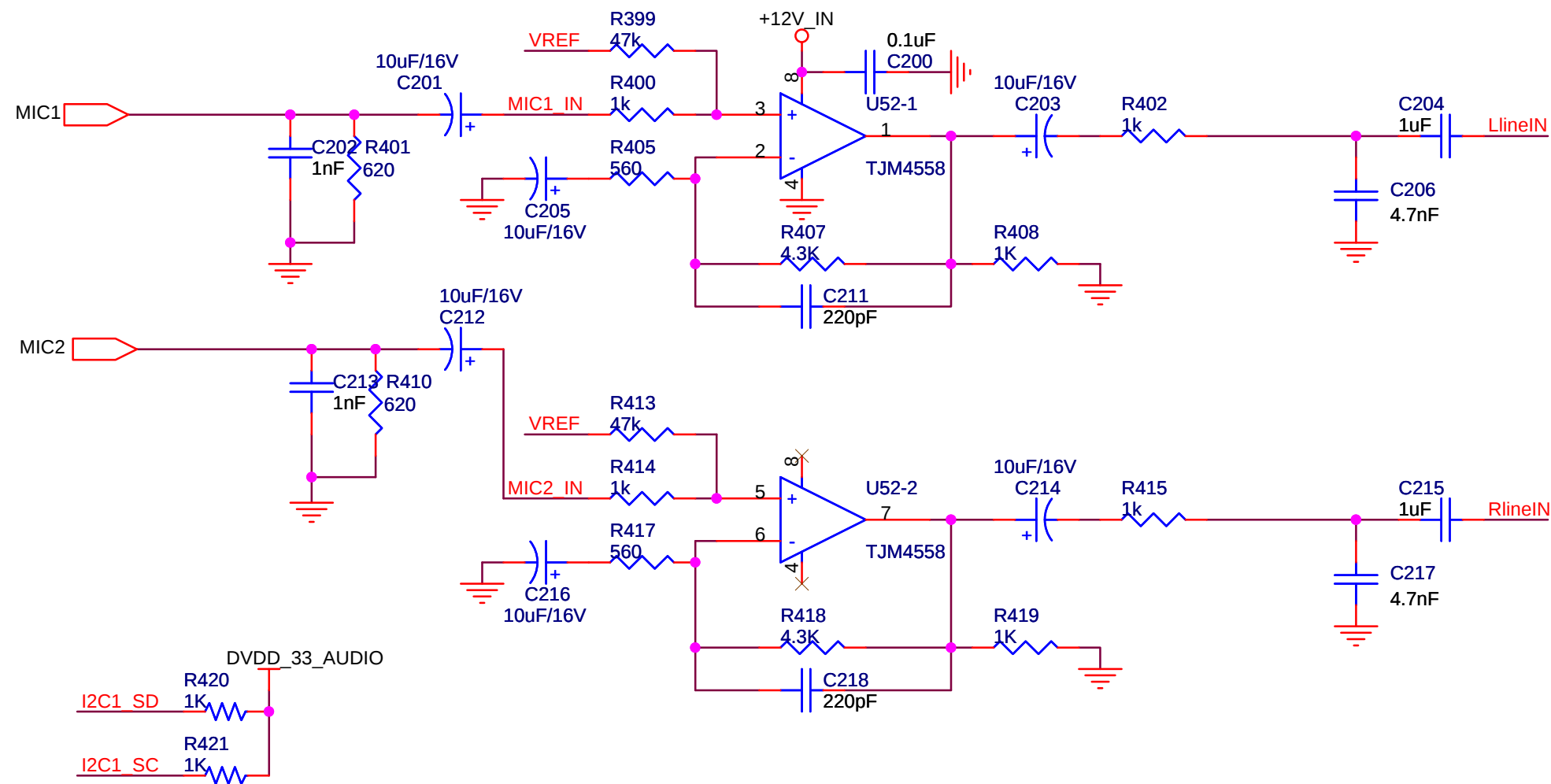
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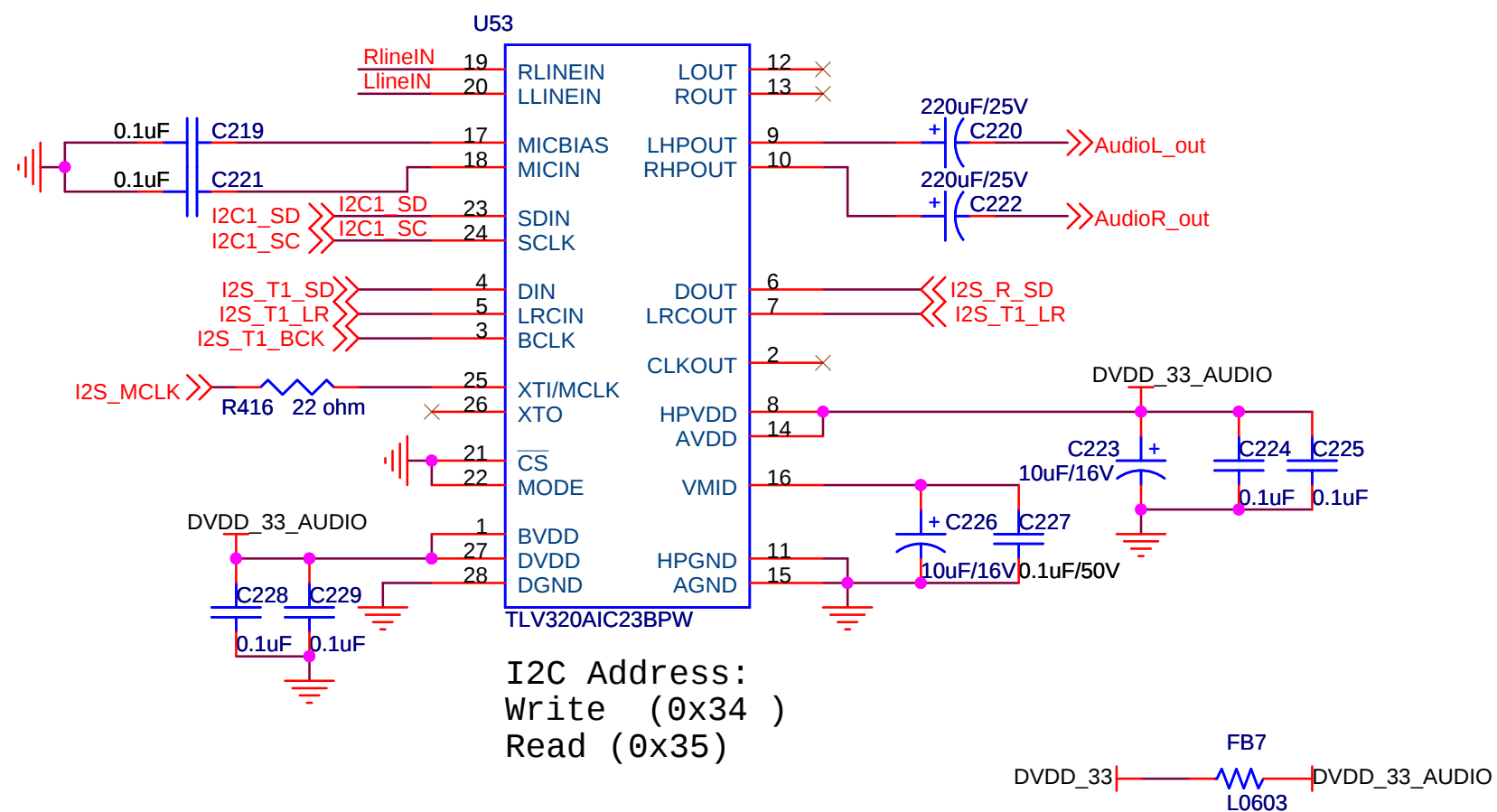
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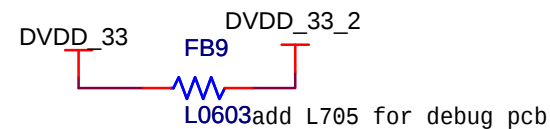


删除一部分电路，原来是用来防止冲击声

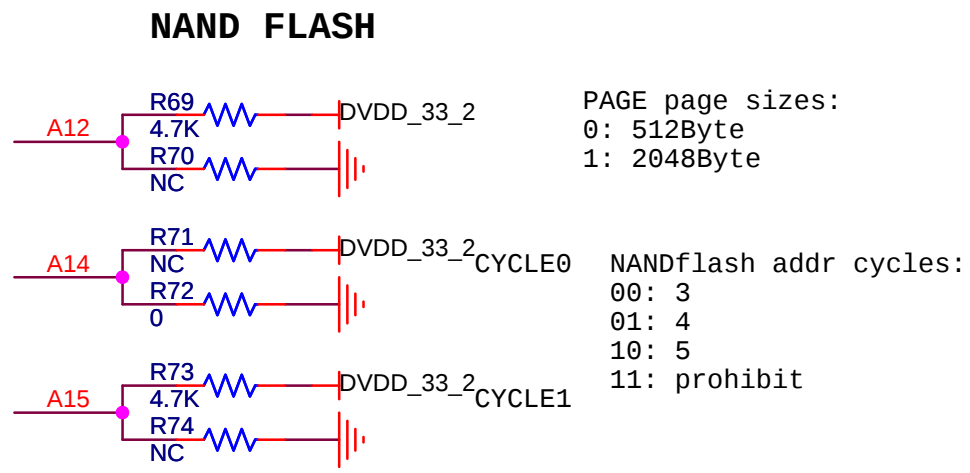
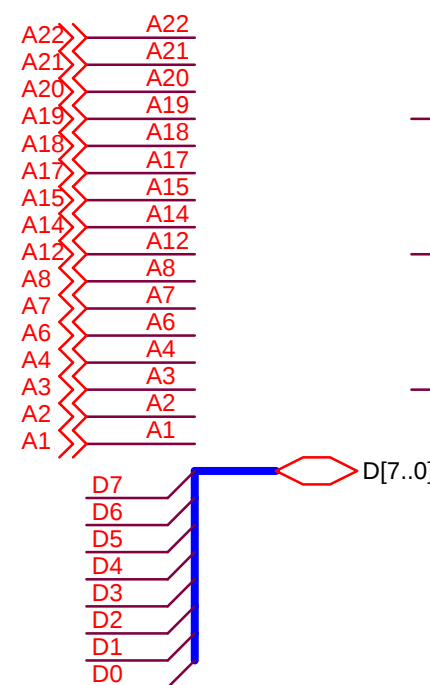
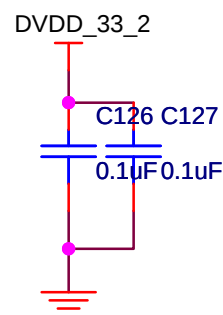
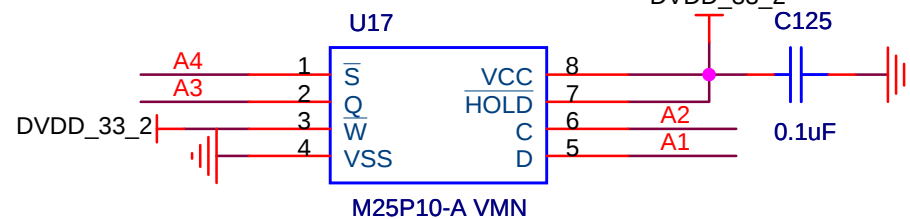
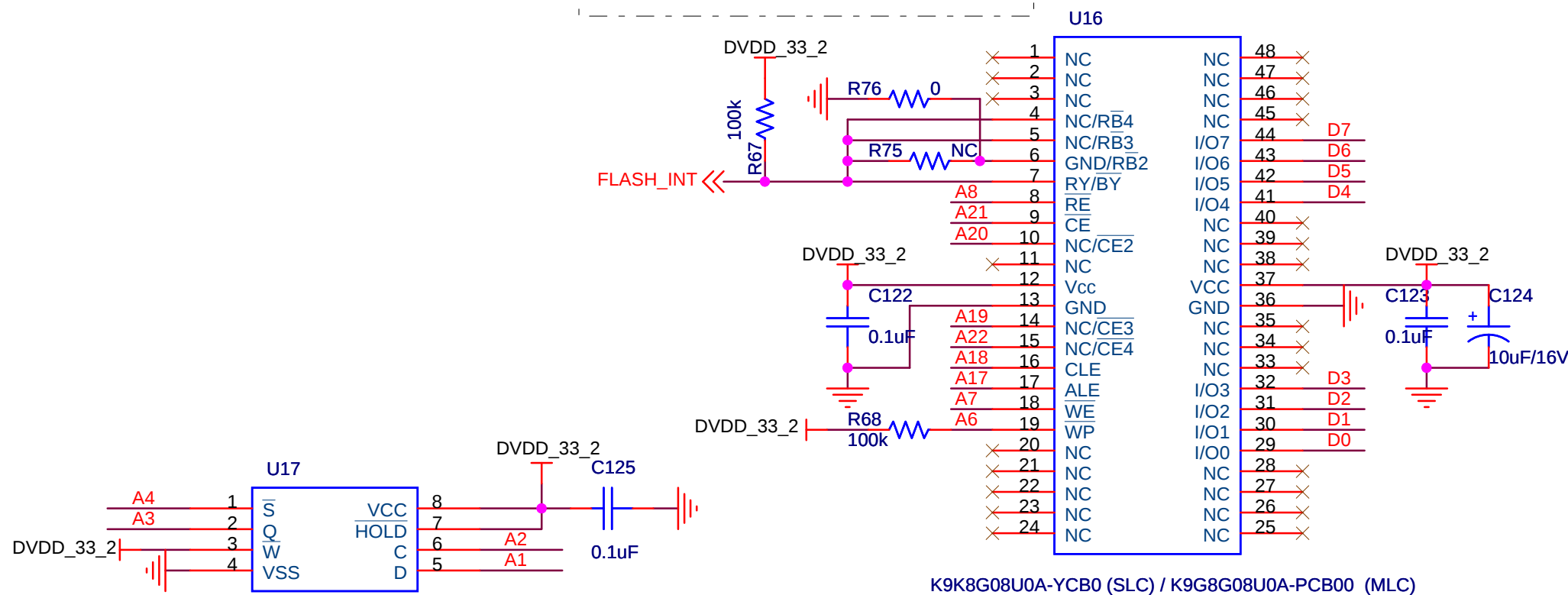


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4GB nandflash: R75 = 0 ;R76 = NC;
1GB nandflash: R75 = NC;R76 = 0 ;
A21=CE0; A20=CE1;A19=CE2;A22=CE3
A8=RE;A18=CLE;A17=ALE;A7=WE;A6=WP



Option1 : NOR FLASH
Option2 : Serial Flash + Nandflash

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