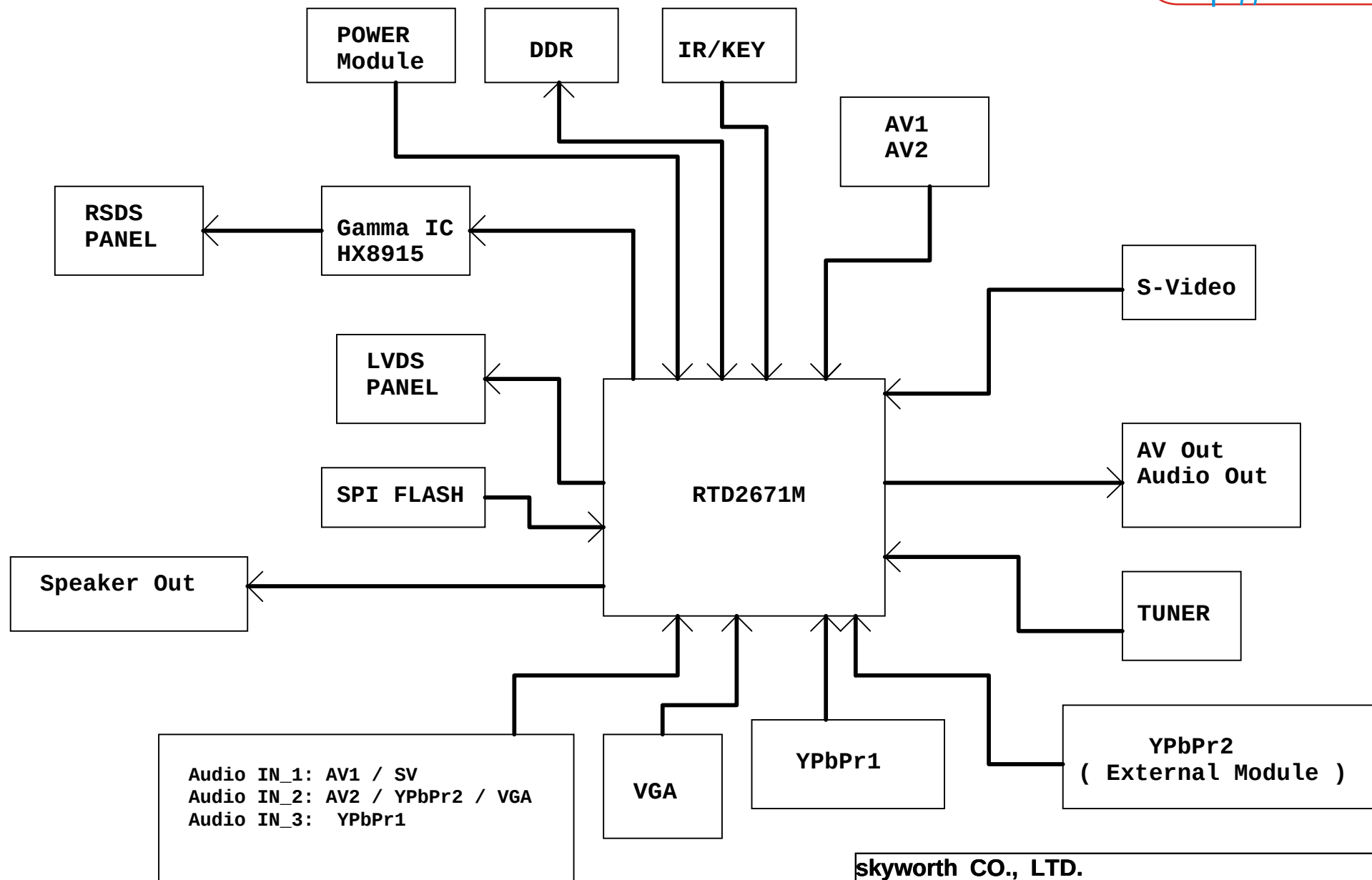
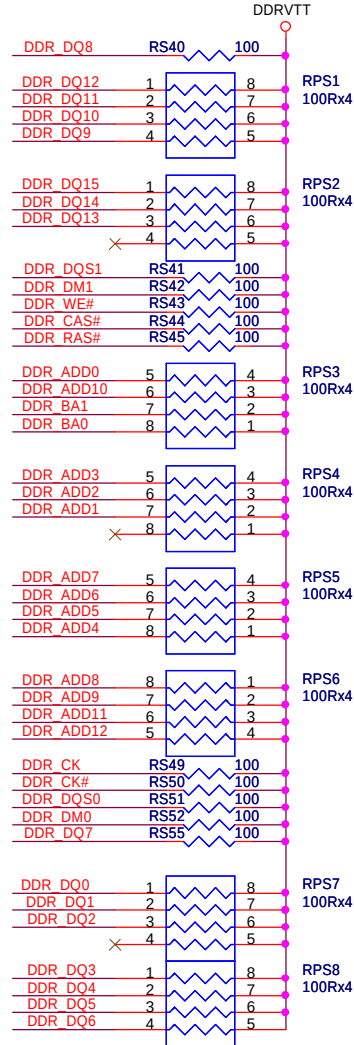
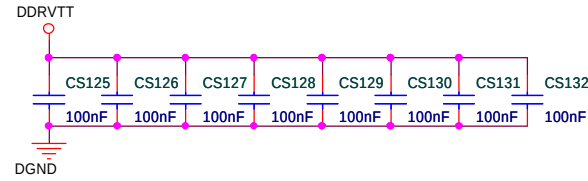
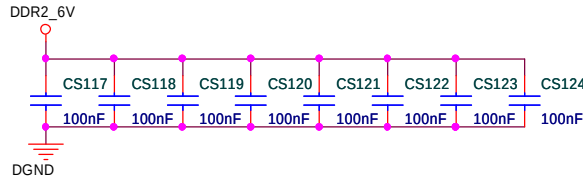
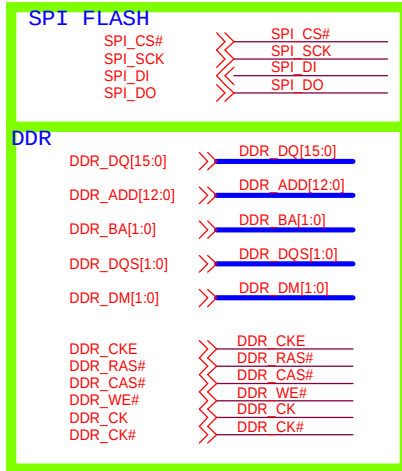


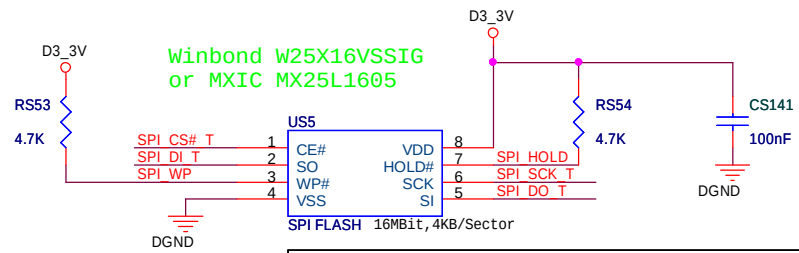
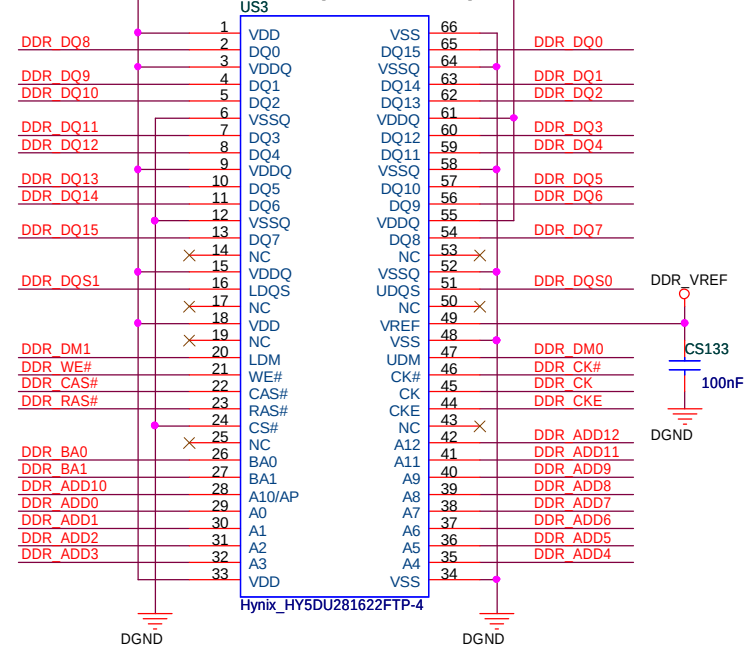


skyworth CO., LTD.					
Title P01_Block Diagram					
Document Number RTD2671M LCD TV Controller					
Size A	Design	Check	Auditing	Modify Rev	Rev A
	Design of name	Check of name	Auditing of name	Modify Rev	
Date:	Thursday, November 06, 2008		Sheet	2	of 16

CO., LTD.				
RTD2671M-LQFP216				
RTD2671M LCD TV Controller				
Part Number				
Check	Aussling	Modify Rev	Rev	
of name	Check of name	Issuing of name	Modify Rev	A
ry, November 06, 2008	Issue	3	of	16

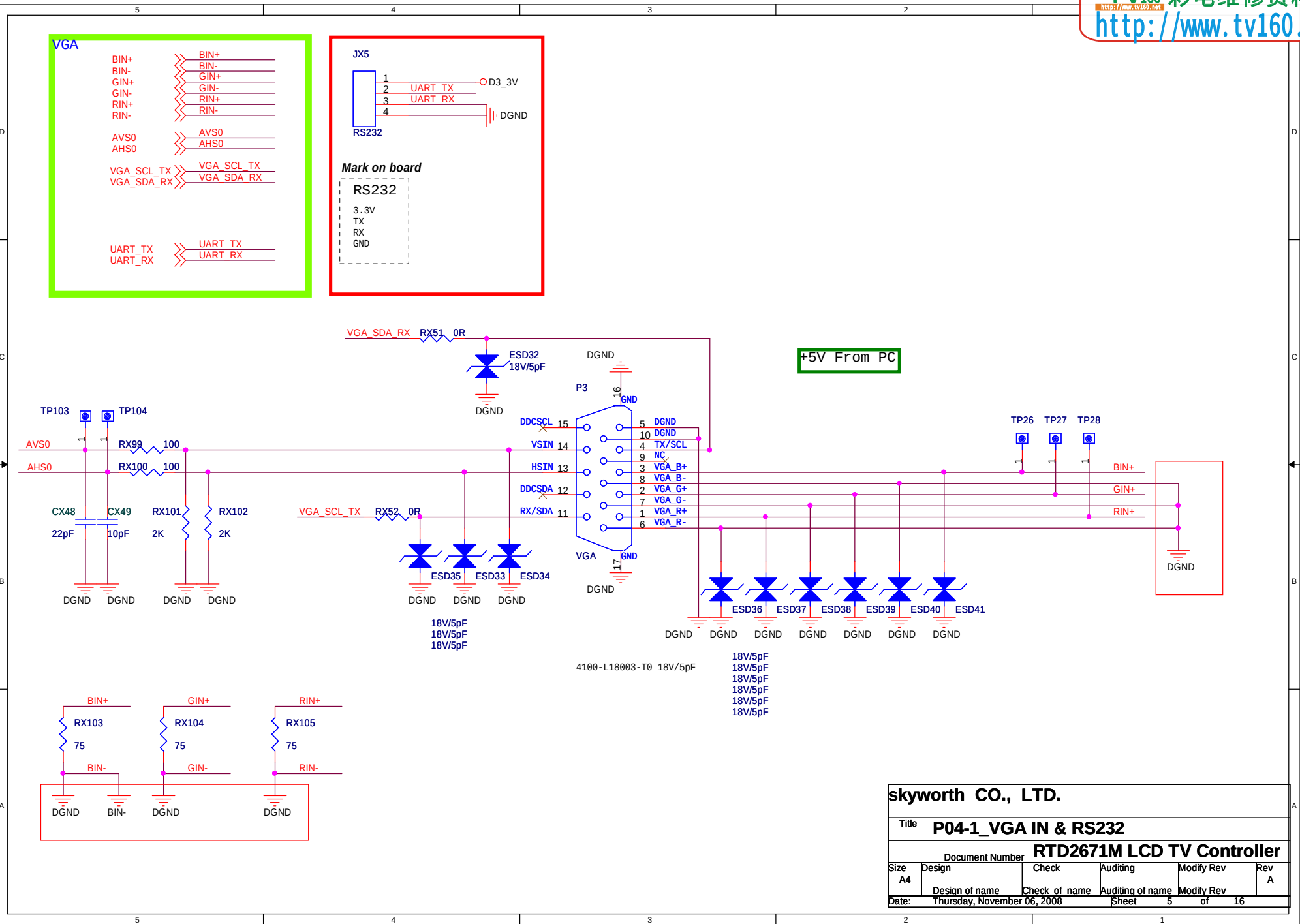


DDRI (8Mb x 16)



skyworth CO., LTD.

Title P03_Memory					
Document Number RTD2671M LCD TV Controller					
Size	Design	Check	Auditing	Modify Rev	Rev
A4	Design of name	Check of name	Auditing of name	Modify Rev	A
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Title P04-1_VGA IN & RS232					
Document Number RTD2671M LCD TV Controller					
Size A4	Design	Check	Auditing	Modify Rev	Rev A
	Design of name	Check of name	Auditing of name	Modify Rev	
Date:	Thursday, November 06, 2008		Sheet 5	of 16	

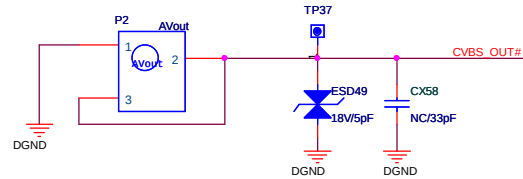
VIDEO DECODER IN

YPBPR_PR1 >> YPBPR_PR1
YPBPR_PR1 >> YPBPR_PR1-

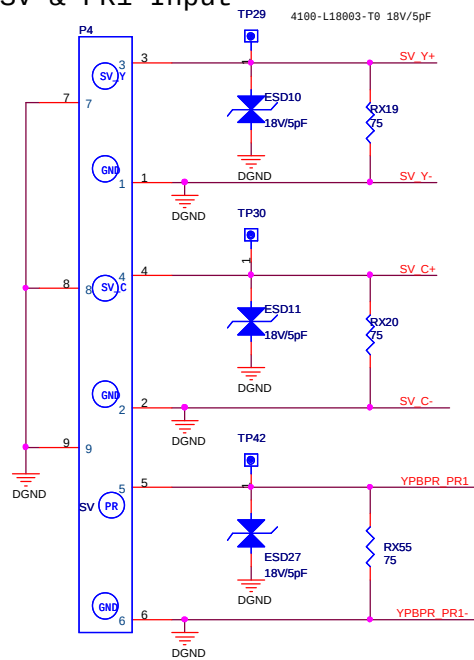
SV_Y+ >> SV_Y+
SV_Y- >> SV_Y-
SV_C+ >> SV_C+
SV_C- >> SV_C-

CVBS_OUT_ >> CVBS_OUT

X303的接线有改



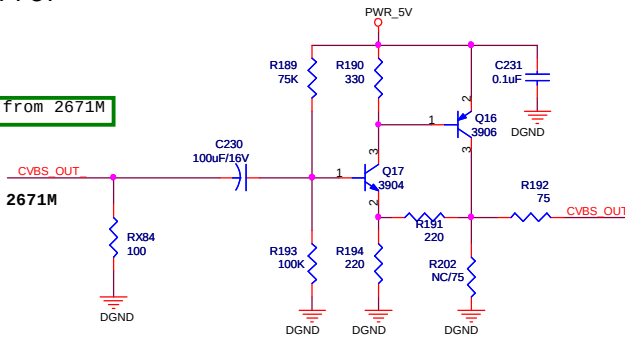
SV & PR1 Input



Video buffer

AVout from 2671M

Near 2671M



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Title SV IN&AVout

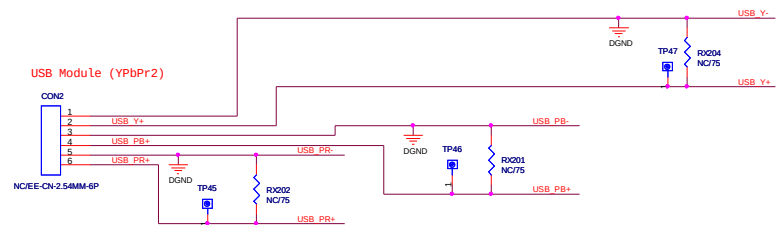
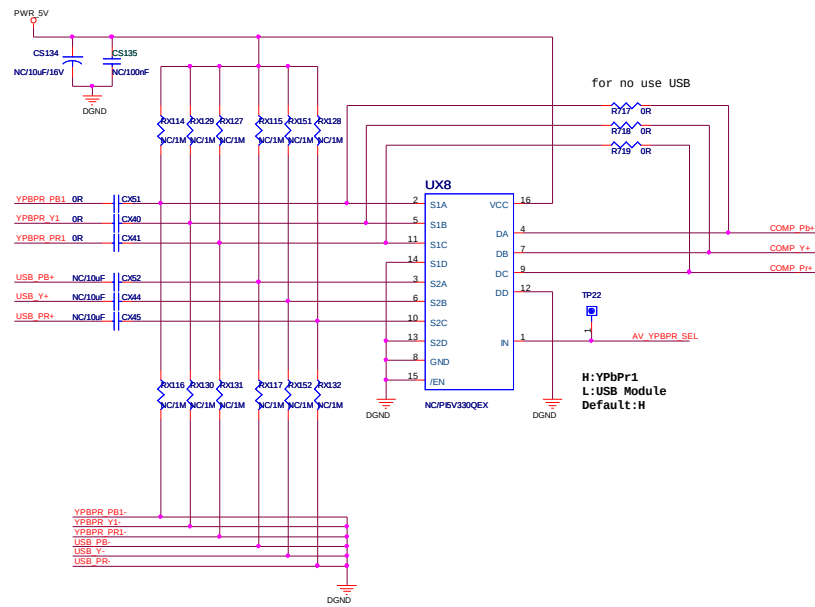
Document Number RTD2671M LCD TV Controller

Size	Design	Check	Auditing	Modify Rev	Rev
B	Design of name	Check of name	Auditing of name	Modify Rev	A
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The diagram illustrates the connections for the YBPDPF and AV_YBPBR_SEL signals. On the left, the YBPDPF signal is shown with three outputs: COMP_Y+, COMP_Pb+, and COMP_Pv+, each connected to a corresponding input on the right. The AV_YBPBR_SEL signal is shown with two outputs: YBPBR_PR1 and YBPBR_PR1- (labeled as YBPBR_PR1- in the diagram). These are connected to inputs on the right. The right side of the diagram shows four inputs: COMP_Y+, COMP_Pb+, COMP_Pv+, and two inputs labeled YBPBR_PR1 and YBPBR_PR1- (labeled as YBPBR_PR1- in the diagram). The inputs are connected to the outputs of the YBPDPF and AV_YBPBR_SEL signals.

The schematic diagram illustrates the power supply section of the TMS320C49 evaluation module. It features a 5V input line connected to a series of diodes (ESD07, ESD05, ESD03, ESD06) and resistors (RQ13, RQ63, RQ64) to various pins (AV2, Y1, Y1-, Y2+, Y2-, YB1, YB1-). Ground connections are shown for each component.

- Input:** 5V (Pin 1)
- Components:**
 - TP31: 4180-L18803-T0 18V/5pF
 - ESD07: 18V/5pF
 - RQ13: 75
 - ESD05: 18V/5pF
 - RQ63: 75
 - TP36: 4180-L18803-T0 18V/5pF
 - ESD03: 18V/5pF
 - RQ64: 75
 - TP41: 4180-L18803-T0 18V/5pF
 - ESD06: 18V/5pF
- Output Pins:**
 - AV2 (Pin 6)
 - Y1 (Pin 5)
 - Y1- (Pin 1)
 - Y2+ (Pin 4)
 - Y2- (Pin 2)
 - YB1 (Pin 3)
 - YB1- (Pin 1)
- Ground Connections:**
 - DGND
 - CVBS1+
 - CVBS1-
 - CVBS2+
 - CVBS2-
 - YBPBR Y1
 - YBPBR Y1-
 - YBPBR Y2+
 - YBPBR Y2-
 - YBPBR YB1
 - YBPBR YB1-



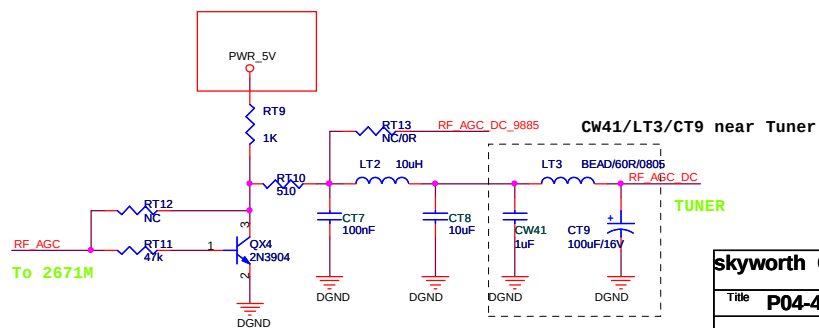
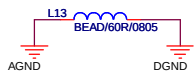
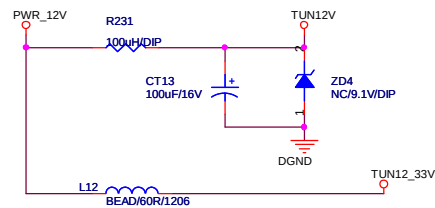
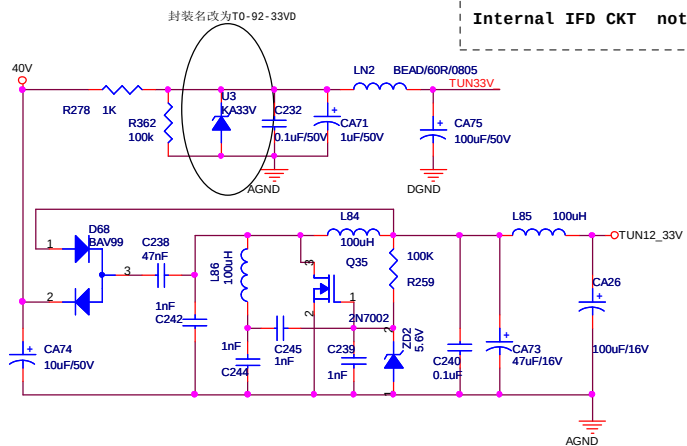
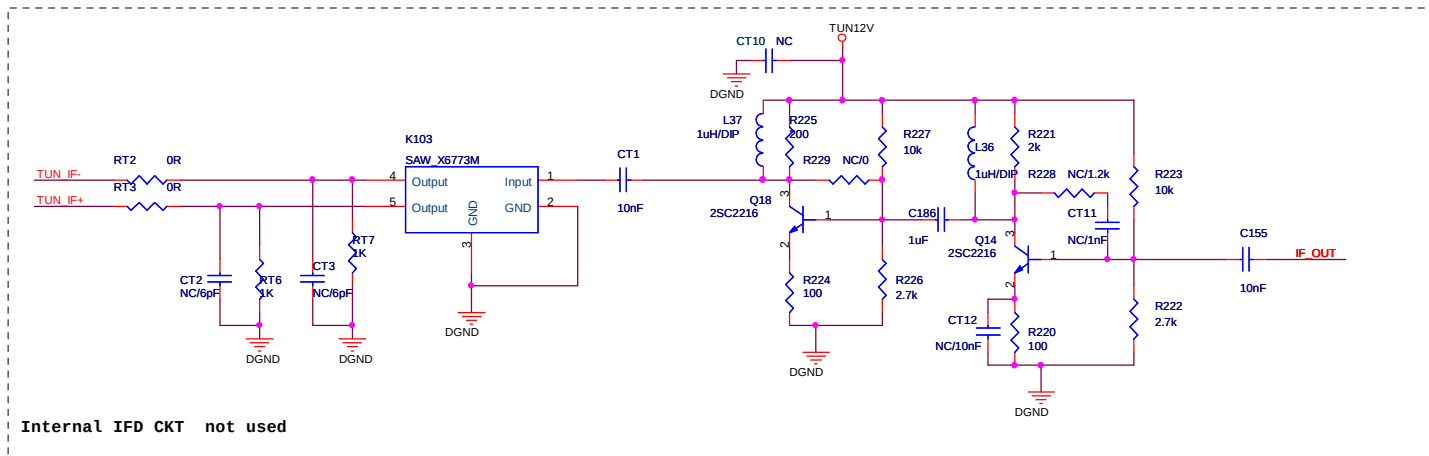
IF Demod

TUN_IF+ << TUN_IF+
TUN_IF- << TUN_IF-
IF_OUT << IF_OUT

RF_AGC_DC_9885 << RF_AGC_DC_9885

RF_AGC_DC << RF_AGC_DC
RF_AGC << RF_AGC

TUN33V_ << TUN33V_



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Title					
P04-4_Tuner w/o IF					
Document Number					
RTD2671M LCD TV Controller					
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IF_OUT << IF_OUT

RF_AGC_DC << RF_AGC_DC

I2C_M1_SDA << I2C_M1_SDA

I2C_M1_SCL << I2C_M1_SCL

Tuner_CVBS+ << Tuner_CVBS+

Tuner_CVBS- << Tuner_CVBS-

TUNER_MONO << TUNER_MONO

RF_AGC_DC_98B5 << RF_AGC_DC_98B5

Title P04-5_Tuner with IF					
Document Number RTD2671M LCD TV Controller					
Size B	Design	Check	Auditing	Modify Rev	Rev A
	Design of name	Check of name	Auditing of name	Modify Rev	
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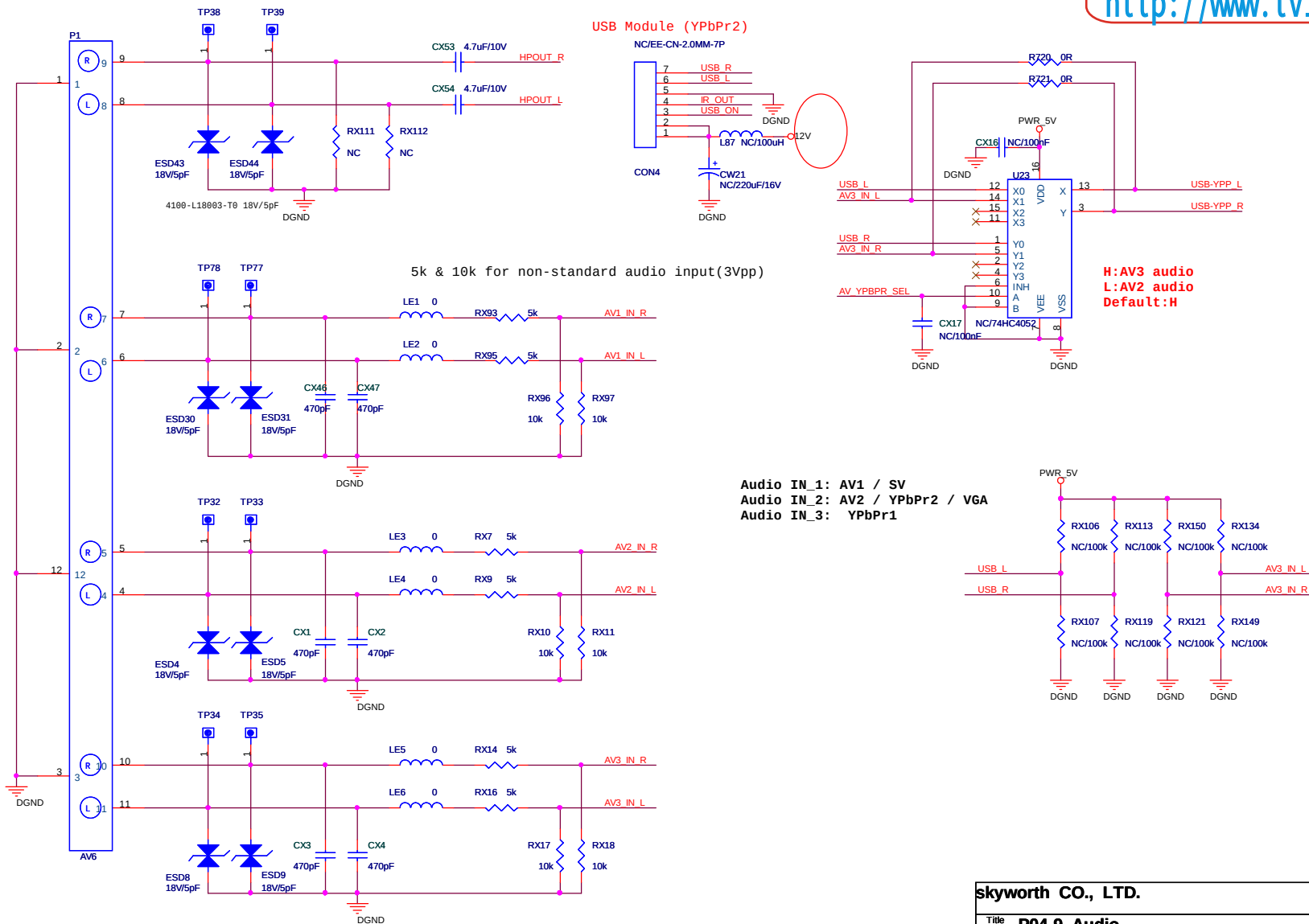
Audio

AV1_IN_L
AV1_IN_R
AV2_IN_L
AV2_IN_R
USB-YPP_R
USB-YPP_L

HPOUT_L
HPOUT_R

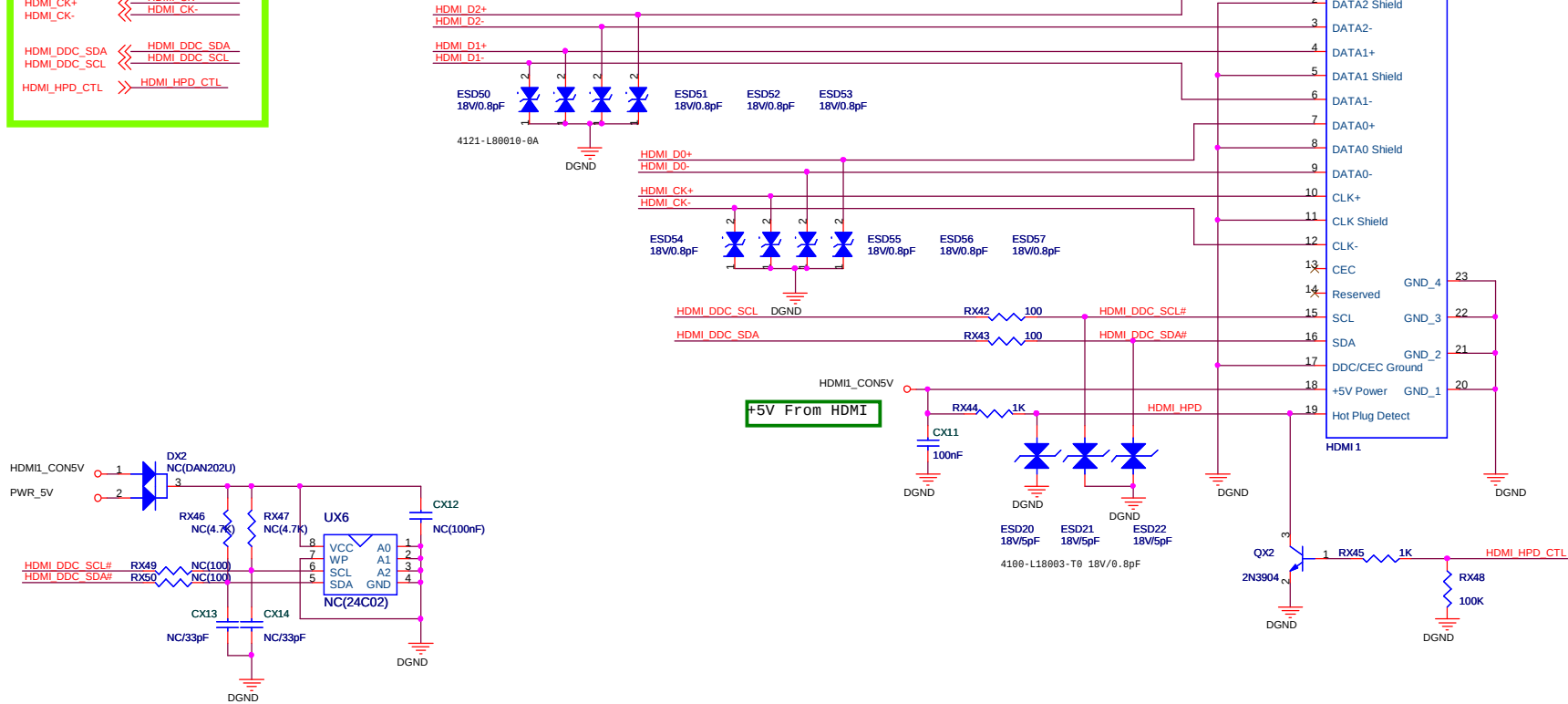
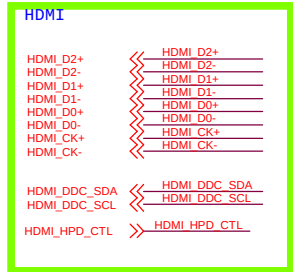
USB_ON
IR_OUT

AV_YBPBR_SEL

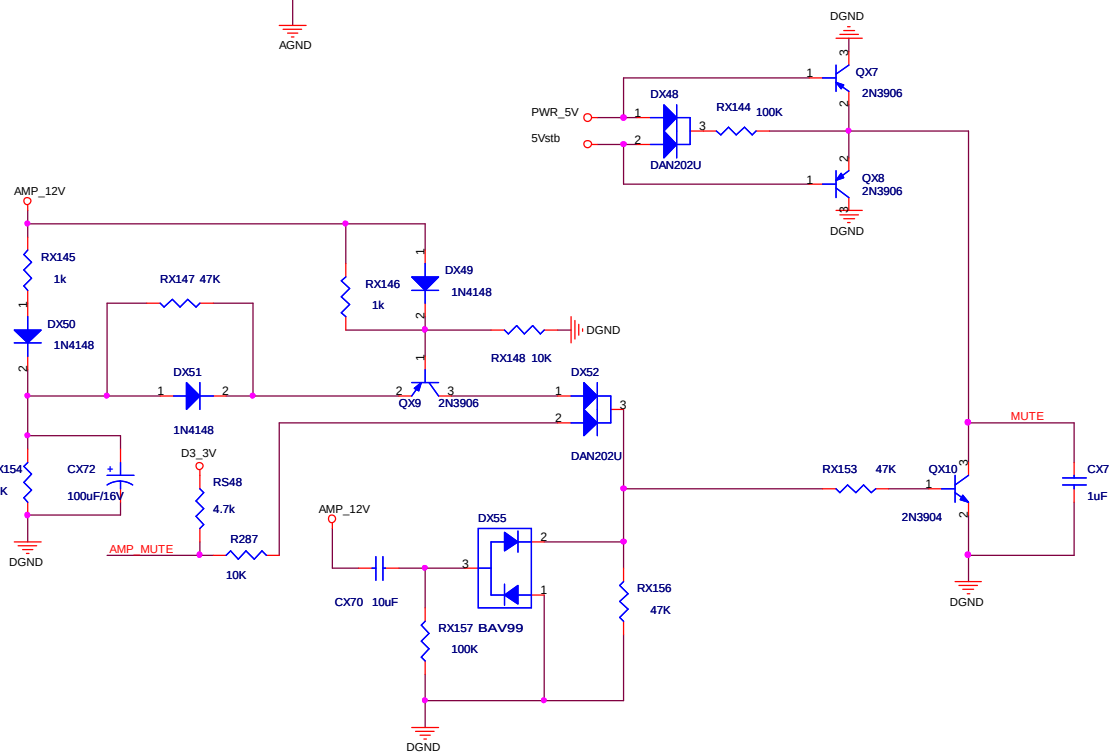
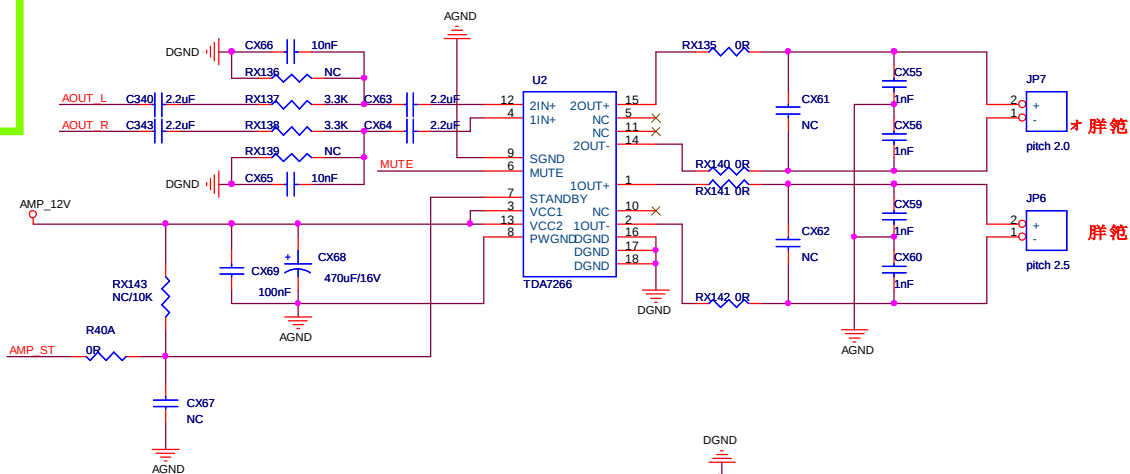
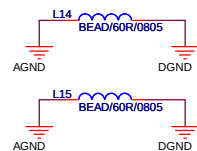
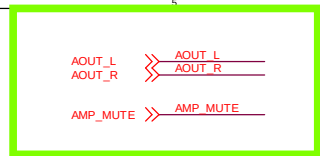


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Title P04_9_Audio					
Document Number RTD2671M LCD TV Controller					
Size	Design	Check	Auditing	Modify Rev	Rev
B	Design of name	Check of name	Auditing of name	Modify Rev	A
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skyworth CO., LTD.					
Title P04-6_HDMI					
Document Number RTD2671M LCD TV Controller					
Size B	Design	Check	Auditing	Modify Rev	Rev A
	Design of name	Check of name	Auditing of name	Modify Rev	
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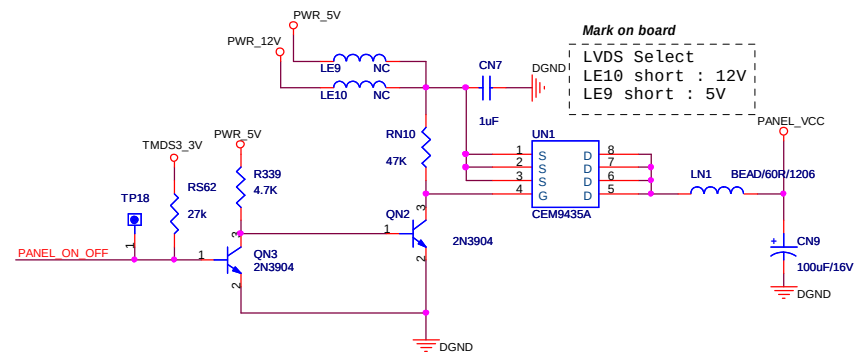
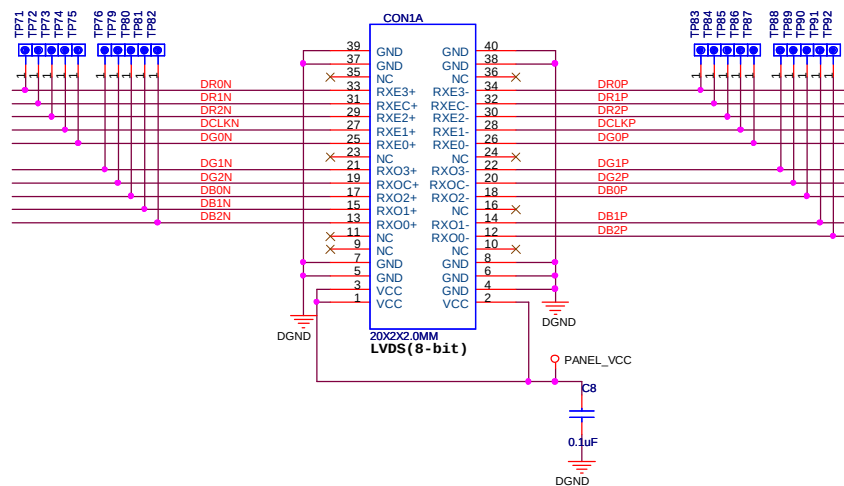
skyworth CO., LTD.					
Title P05_AMP					
Document Number RTD2671M LCD TV Controller					
Size	Design	Check	Auditing	Modify Rev	Rev
B	Design of name	Check of name	Auditing of name	Modify Rev	A
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PANEL

DB2P	DB2P
DB2N	DB2N
DB1P	DB1P
DB1N	DB1N
DB0P	DB0P
DB0N	DB0N
DG2P	DG2P
DG2N	DG2N
DG1P	DG1P
DG1N	DG1N
DG0P	DG0P
DG0N	DG0N
DCLKP	DCLKP
DCLKN	DCLKN
DR2P	DR2P
DR2N	DR2N
DR1P	DR1P
DR1N	DR1N
DR0P	DR0P
DR0N	DR0N

GPIO CONTROL

PANEL_ON_OFF → PANEL_ON_OFF

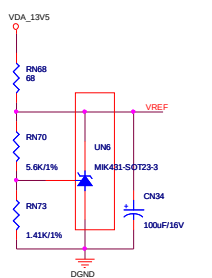
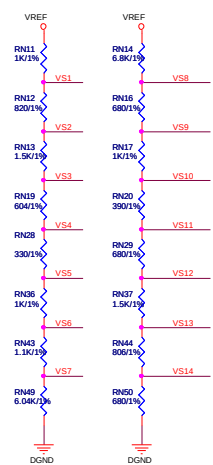
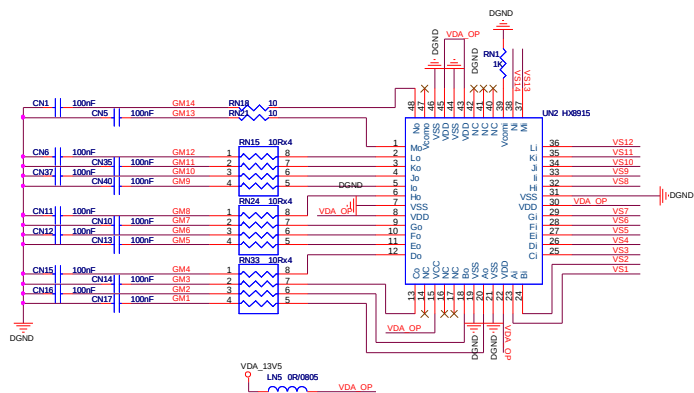


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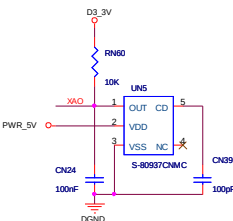
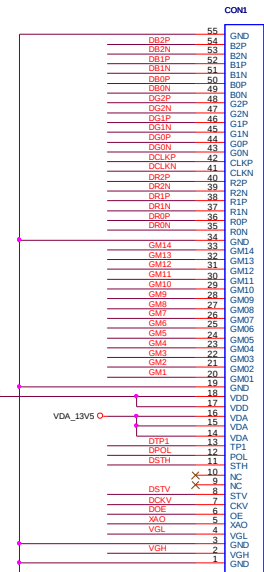
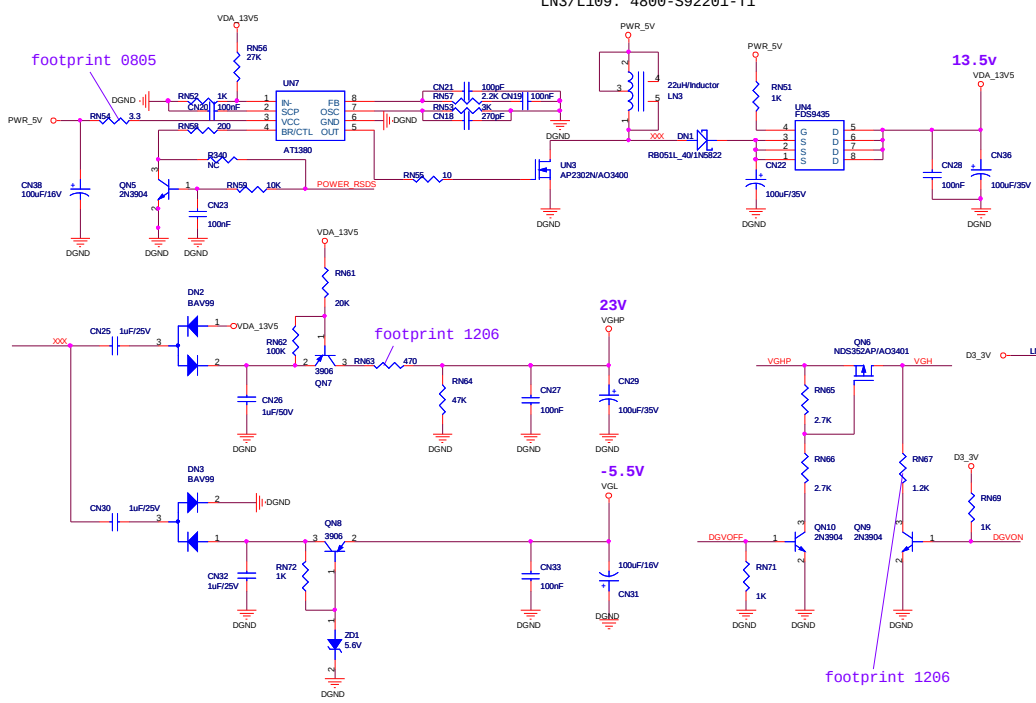
Title P07_LVDS Output					
Document Number RTD2671M LCD TV Controller					
Size B	Design	Check	Auditing	Modify Rev	Rev A
	Design of name	Check of name	Auditing of name	Modify Rev	
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ICUN	
DGVON	DGVON
DGVOFF	DGVOFF
DSTV	DSTV
DPOL	DPOL
DOE	DOE
DTP1	DTP1
DSTH	DSTH

POWER_RSDS	POWER_RSDS
DB2P	DB2P
DB2N	DB2N
DB1P	DB1P
DB1N	DB1N
DB0P	DB0P
DB0N	DB0N
DB2P	DB2P
DB2N	DB2N
DB1P	DB1P
DB1N	DB1N
DB0P	DB0P
DB0N	DB0N
DB2P	DB2P
DB2N	DB2N
DB1P	DB1P
DB1N	DB1N
DB0P	DB0P
DB0N	DB0N
DB2P	DB2P
DB2N	DB2N
DB1P	DB1P
DB1N	DB1N
DB0P	DB0P
DB0N	DB0N

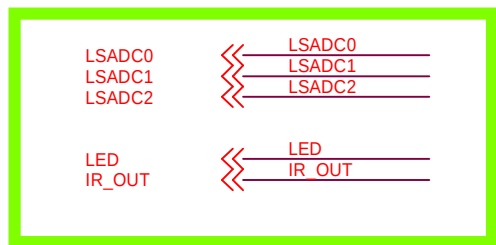


LN3/L109: 4800-S92201-T1

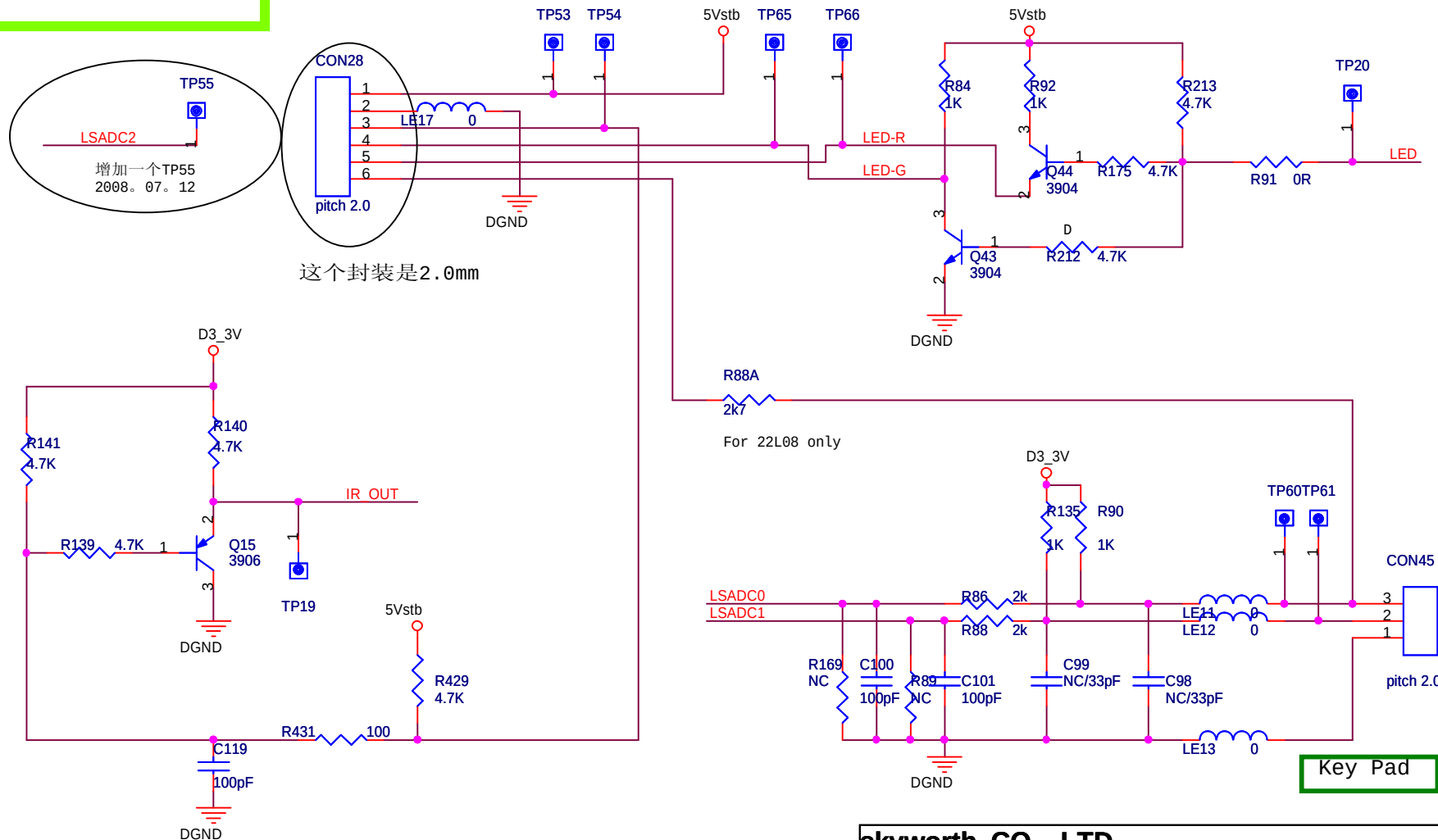


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Title: P08_RSDS Output					
Document Number: RTD2671M LCD TV Controller					
Size	Design	Check	Auditing	Modify	Rev
C	Design of name	Check of name	Auditing of name	Modify	A
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8R03机芯图纸



CON28 pin6 = power key in front of TV set.



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Title P08_IR/KEY

Document Number RTD2671M LCD TV Controller

Size	Design	Check	Auditing	Modify Rev	Rev
A	Design of name	Check of name	Auditing of name	Modify Rev	A
Date:	Thursday, November 06, 2008	Sheet	16	of	16