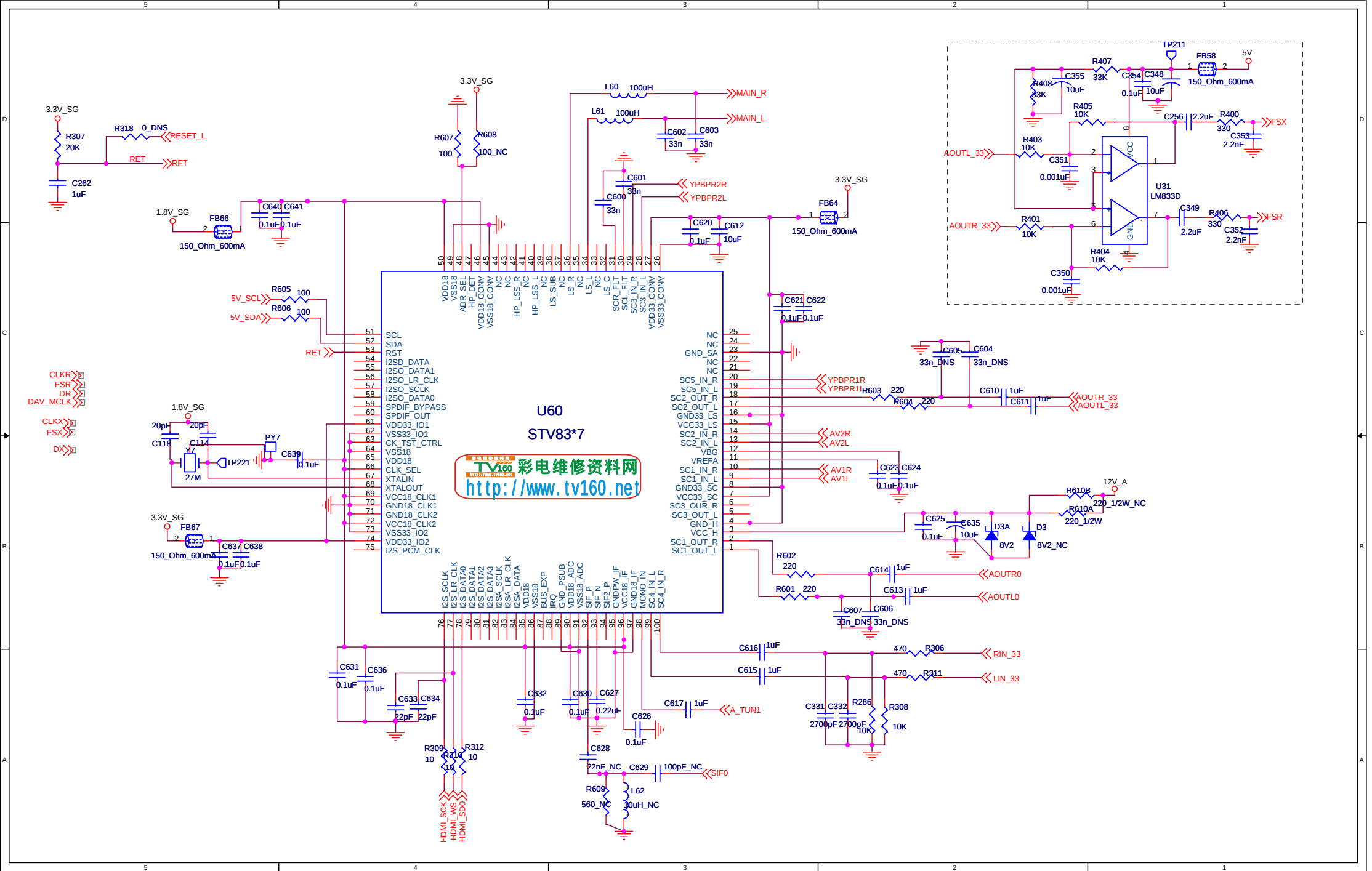
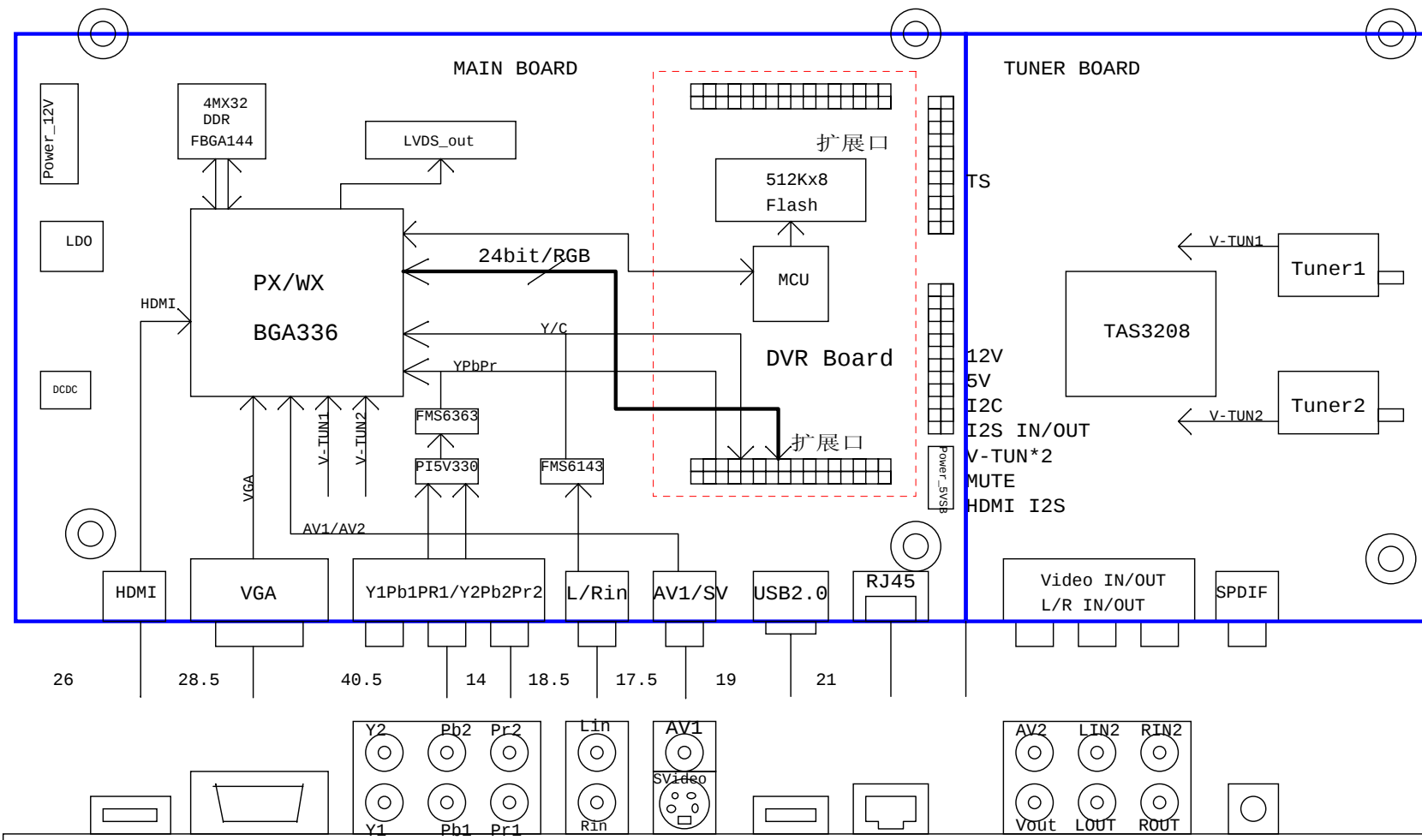
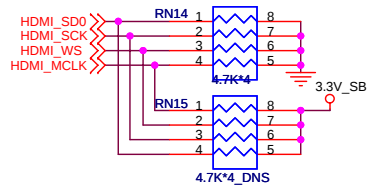
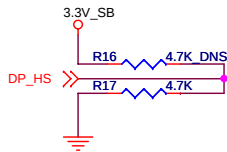
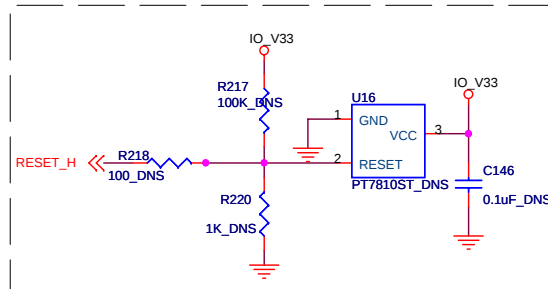




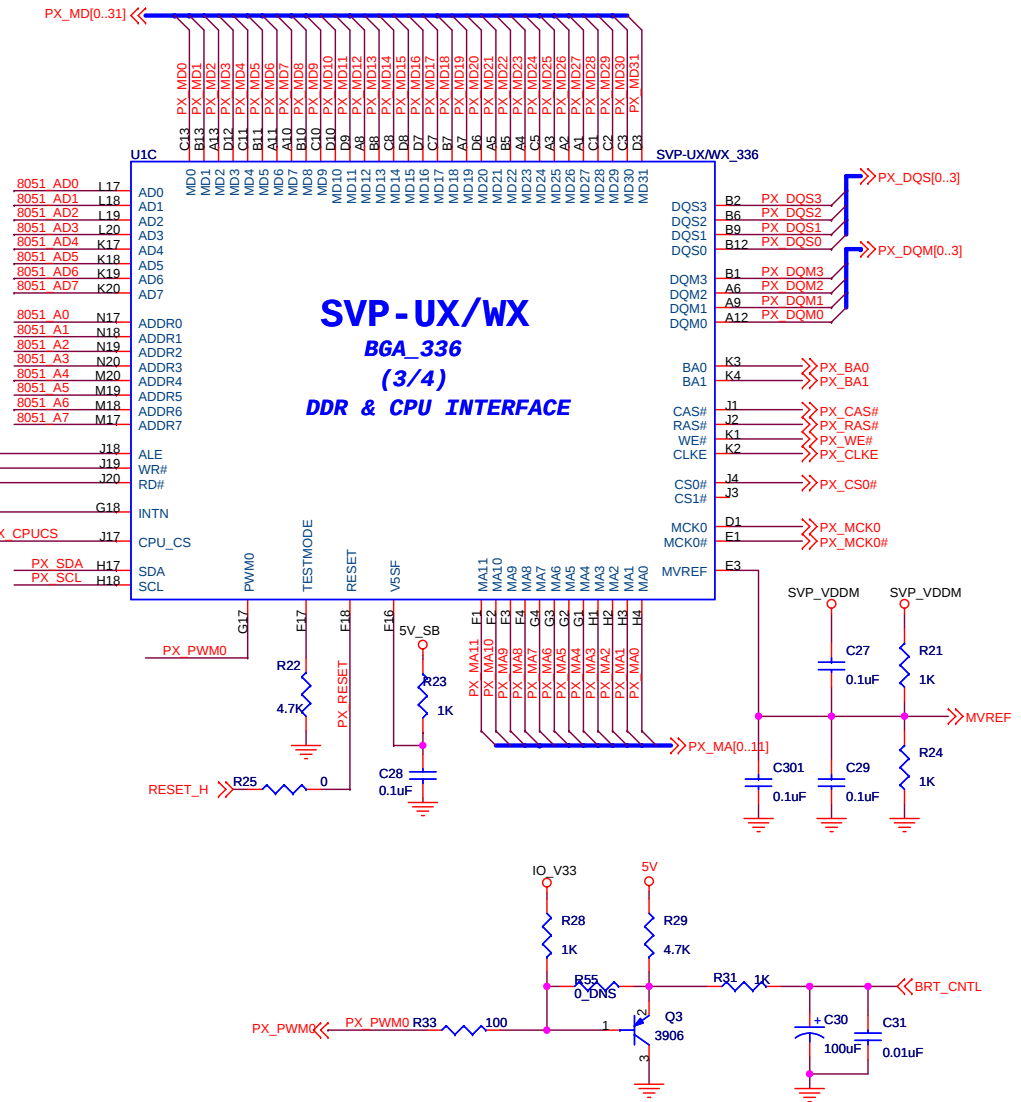




Place close to SVP-PX

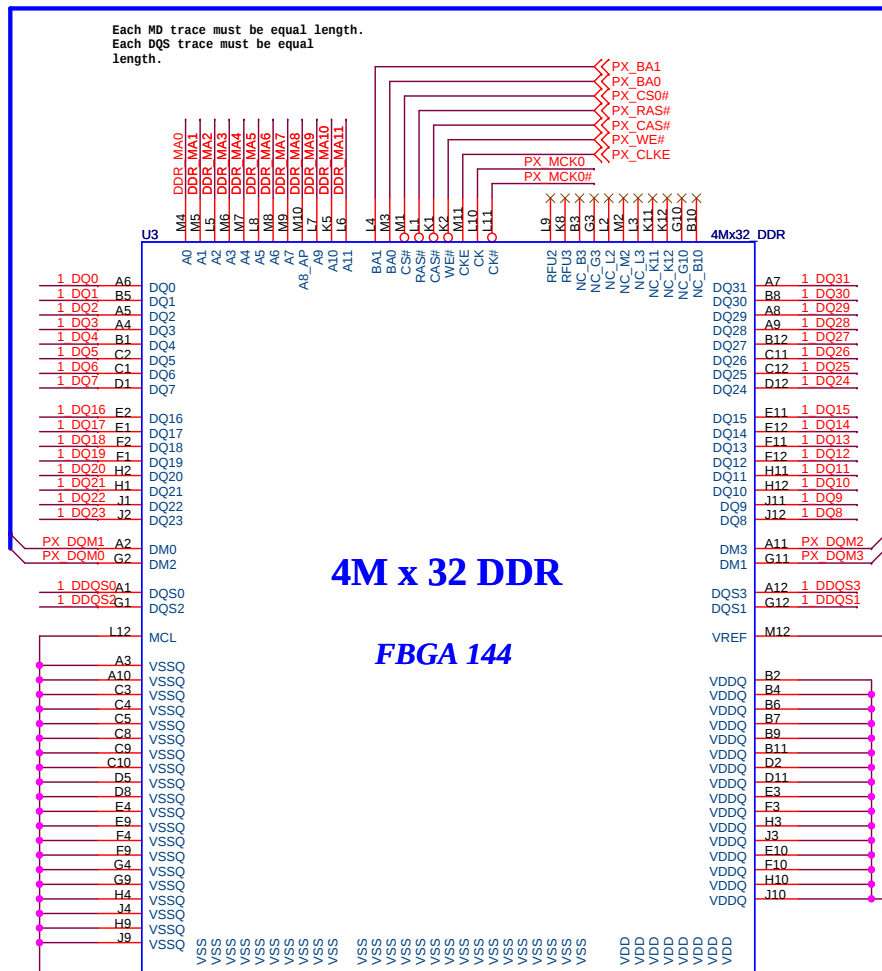
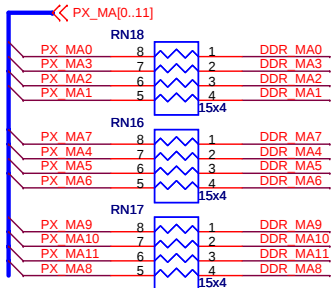


Pin Name	Pin No.	1 (HIGH)	0 (LOW)
DP_HS	P19	MPU in A/D Multiplex Mode	MPU in A/D Separate Mode
WS	U12	Use ALE to latch Address	Use Falling Edges of WR# & RD# to latch Address
SD0	V12	Use Rising Edge of WR# to latch data	Use Falling Edge of WR# to latch data
SCK	Y11	I2C Slave Address = 0x7E/7F	I2C Slave Address = 0x7C/7D

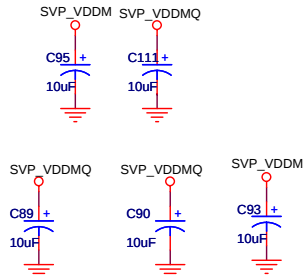
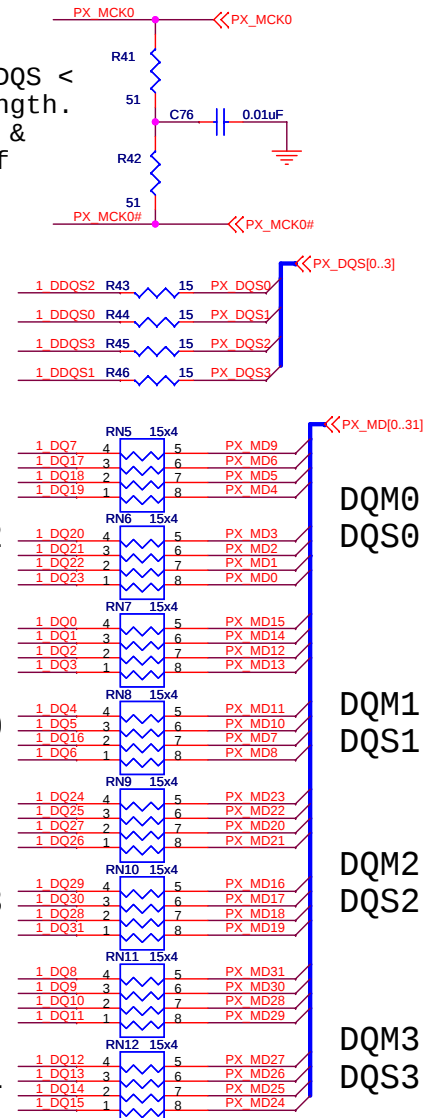


PRELIMINARY

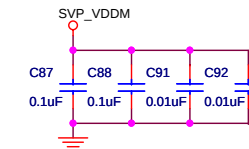
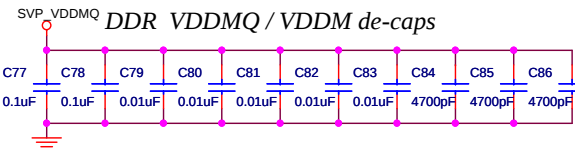
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04:SVP-UX/WX-3			
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Flight time of DQ & DQS < 500ps or 3" trace length.
Mis-match between DQ & DQS < 100ps or 2/3 of trace length.



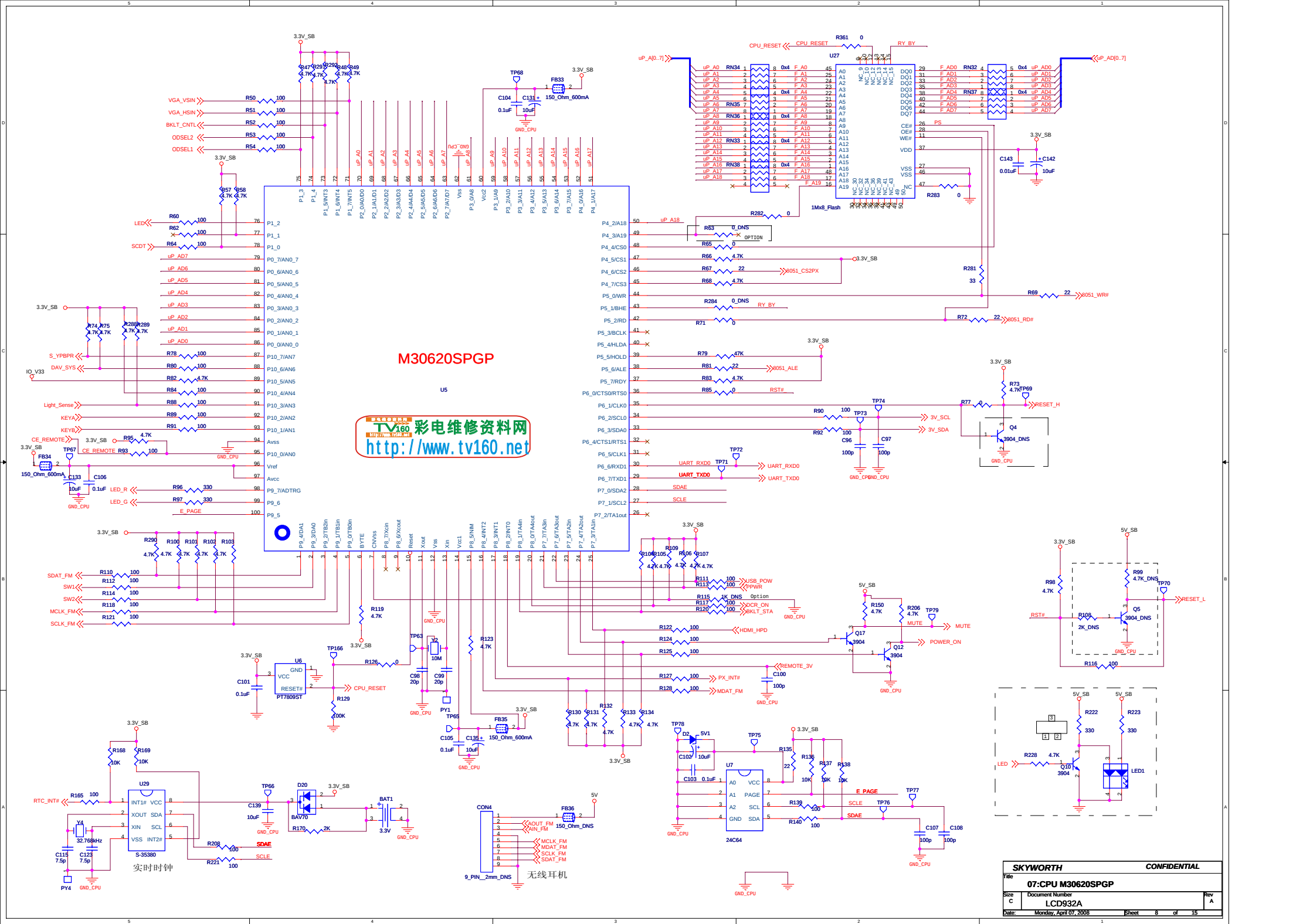
10uF uses either 0805 0805 6.3v or 10v

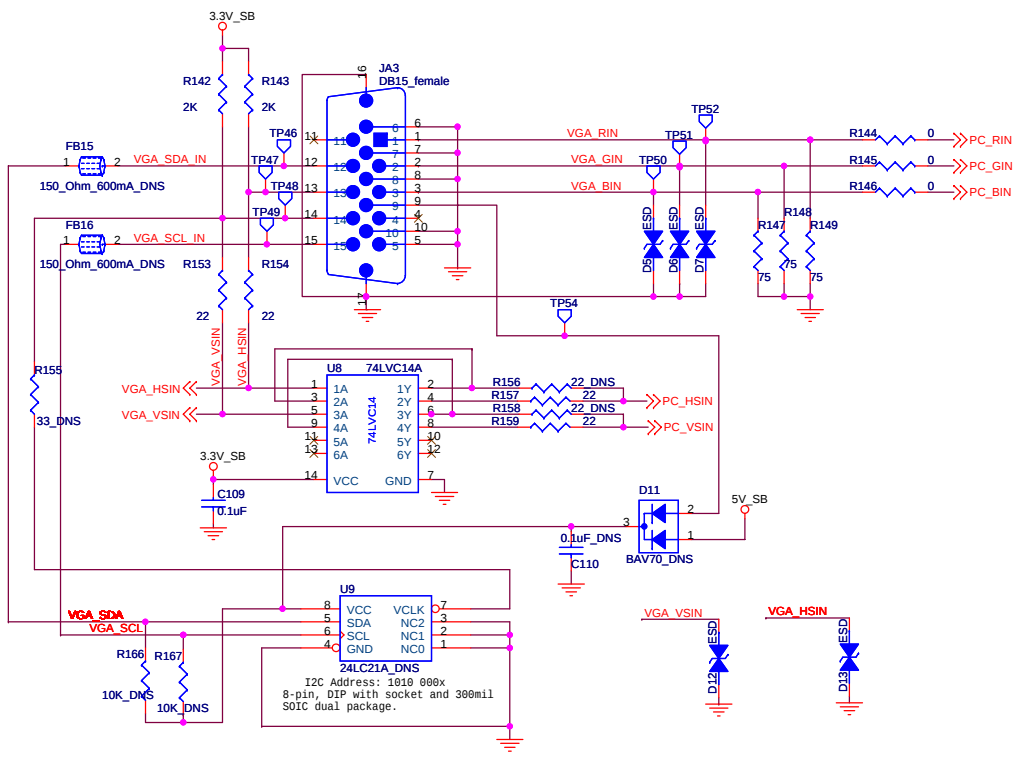
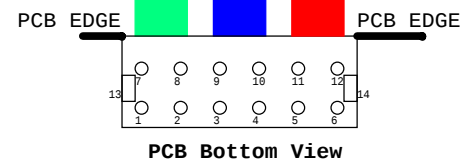
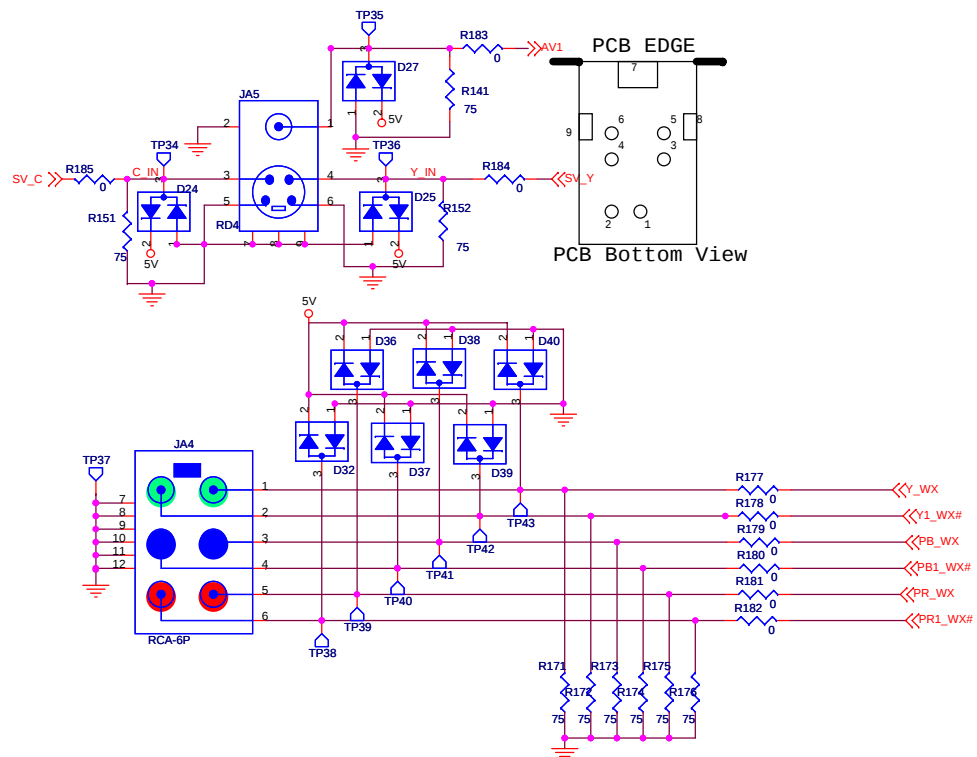


MEMORY DATA BUS SWAPPING
SCHEME FOR OPTIMAL ROUTING.

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06:4MX32 DDR			
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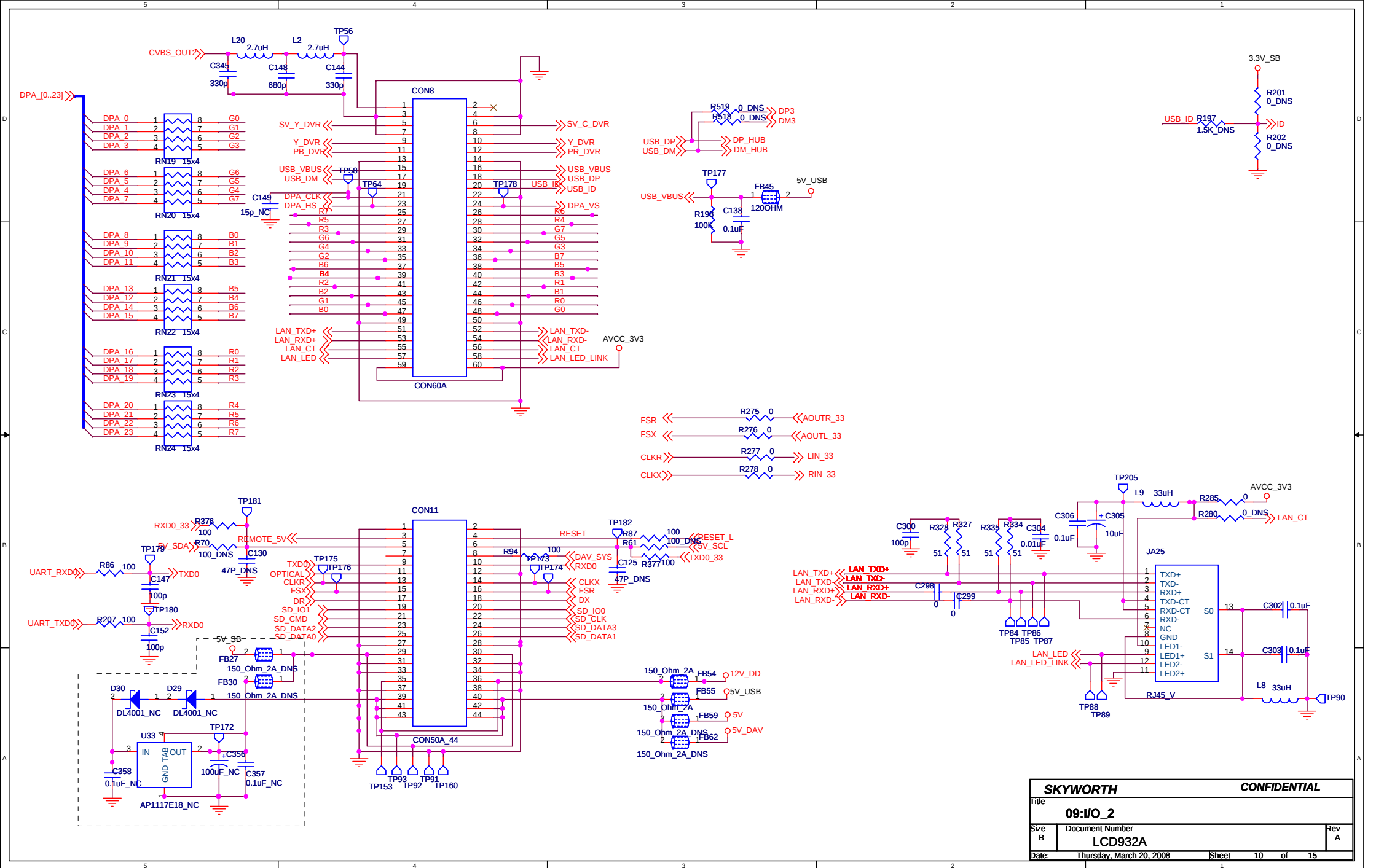
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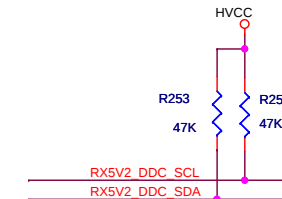
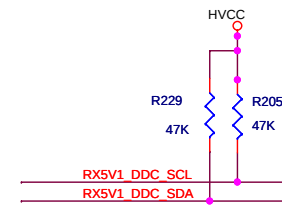
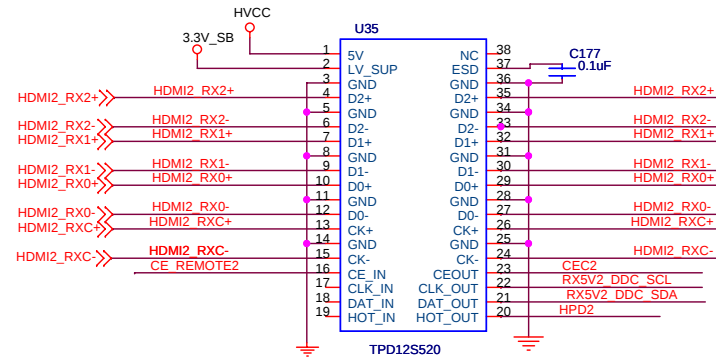
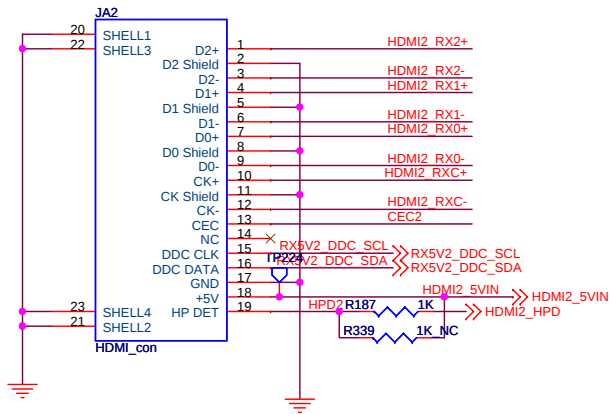
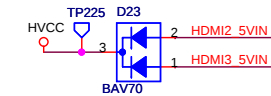
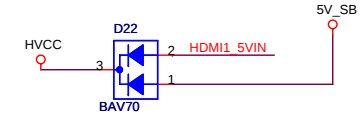
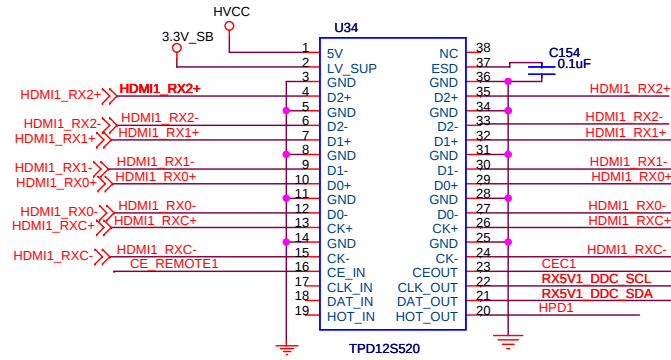
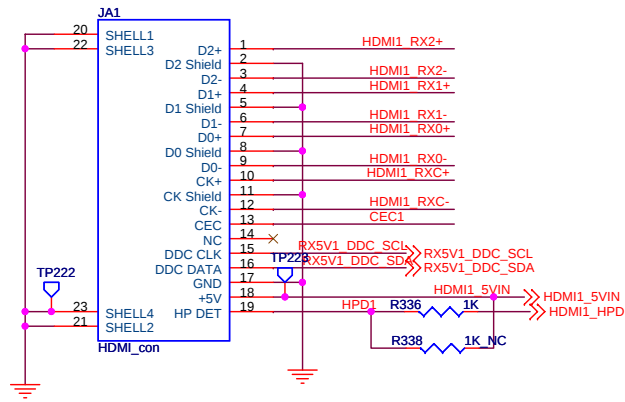




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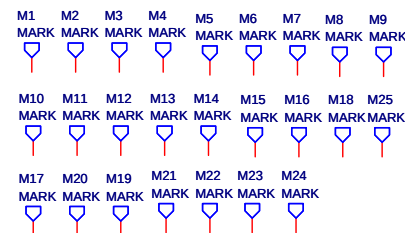
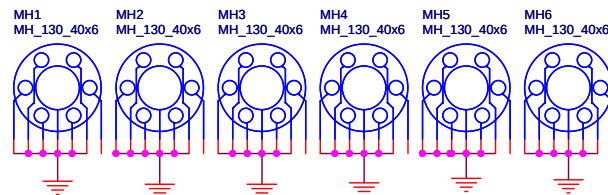
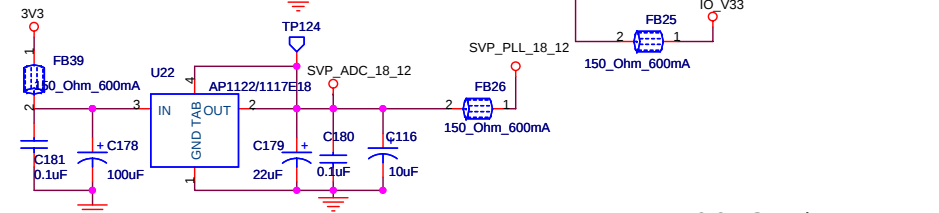
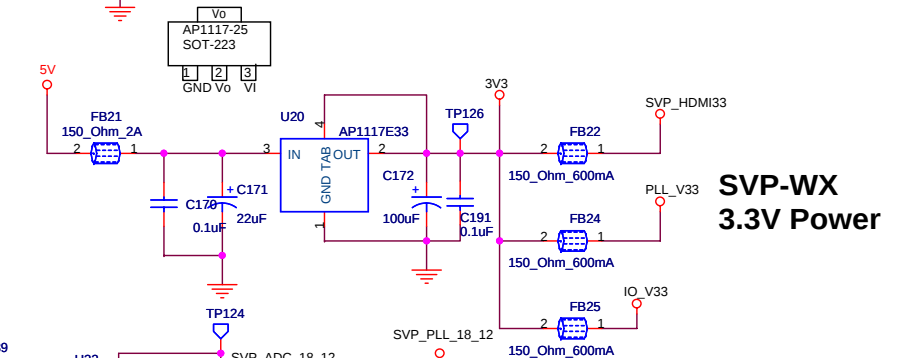
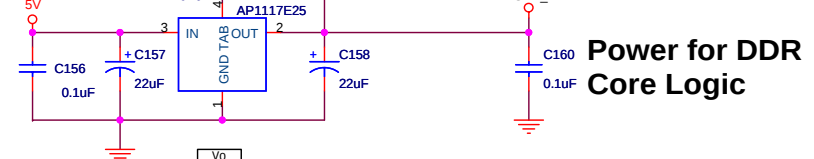
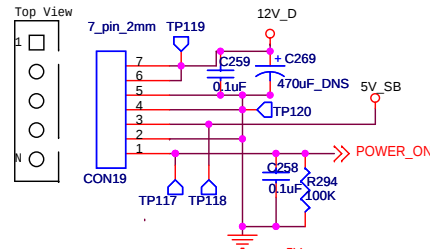
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08:I/O_1 COMPONENTS IN			
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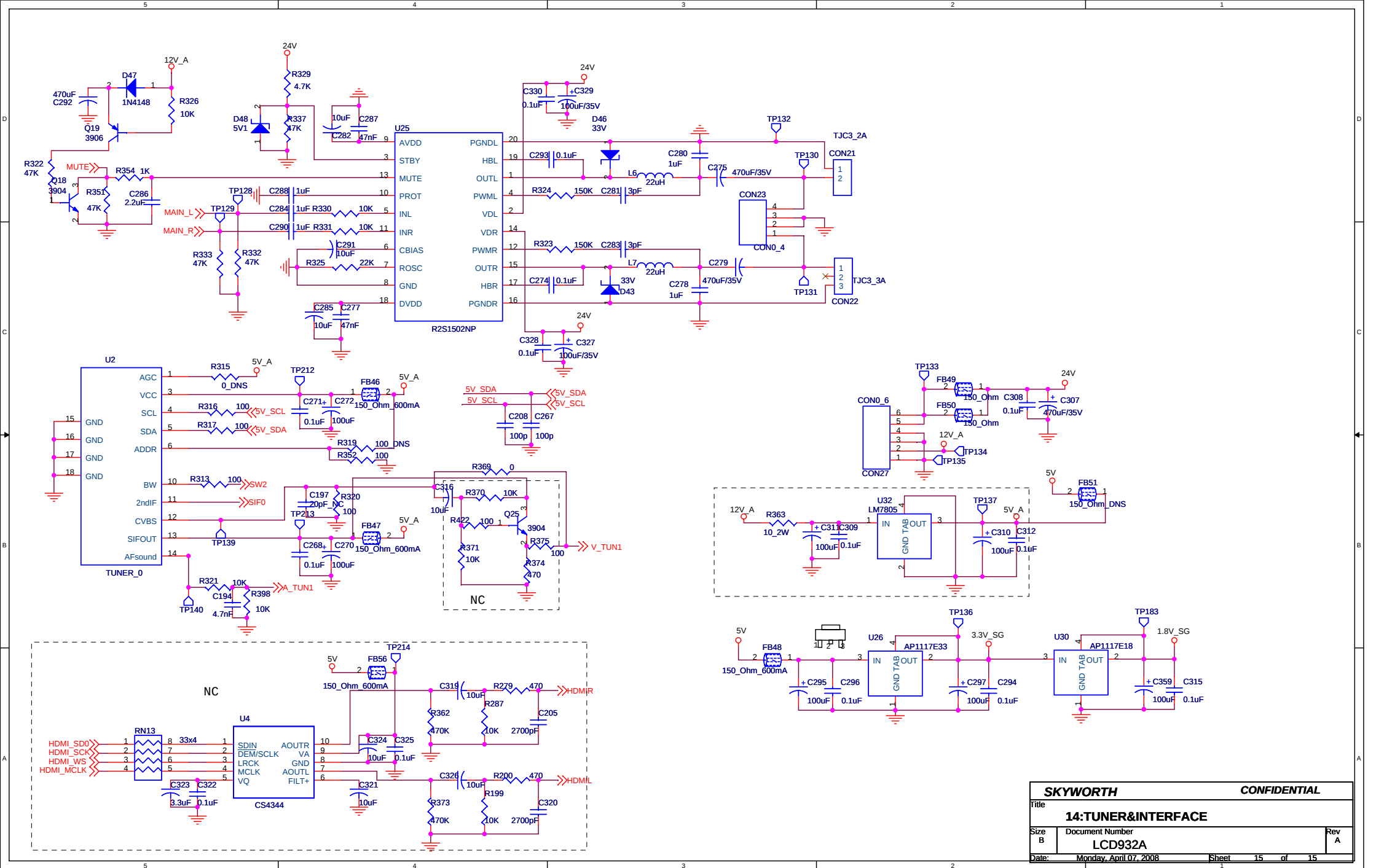


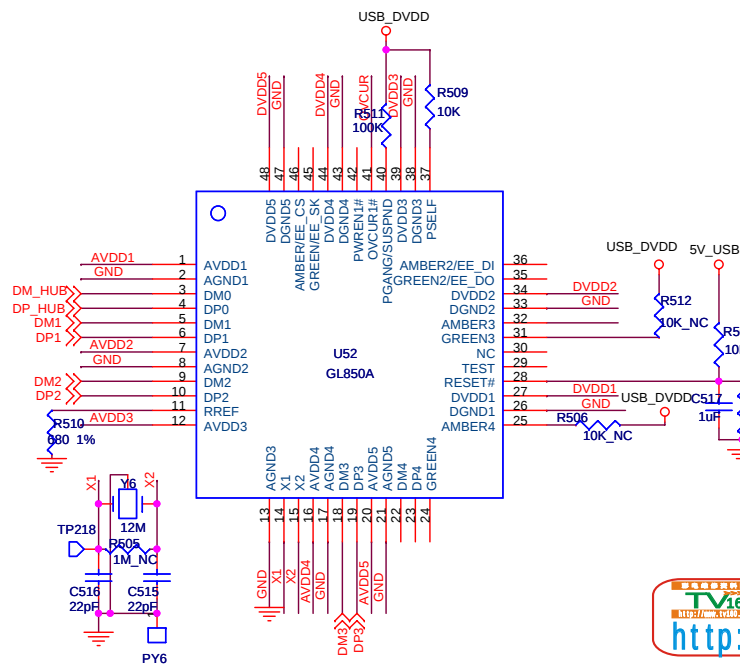
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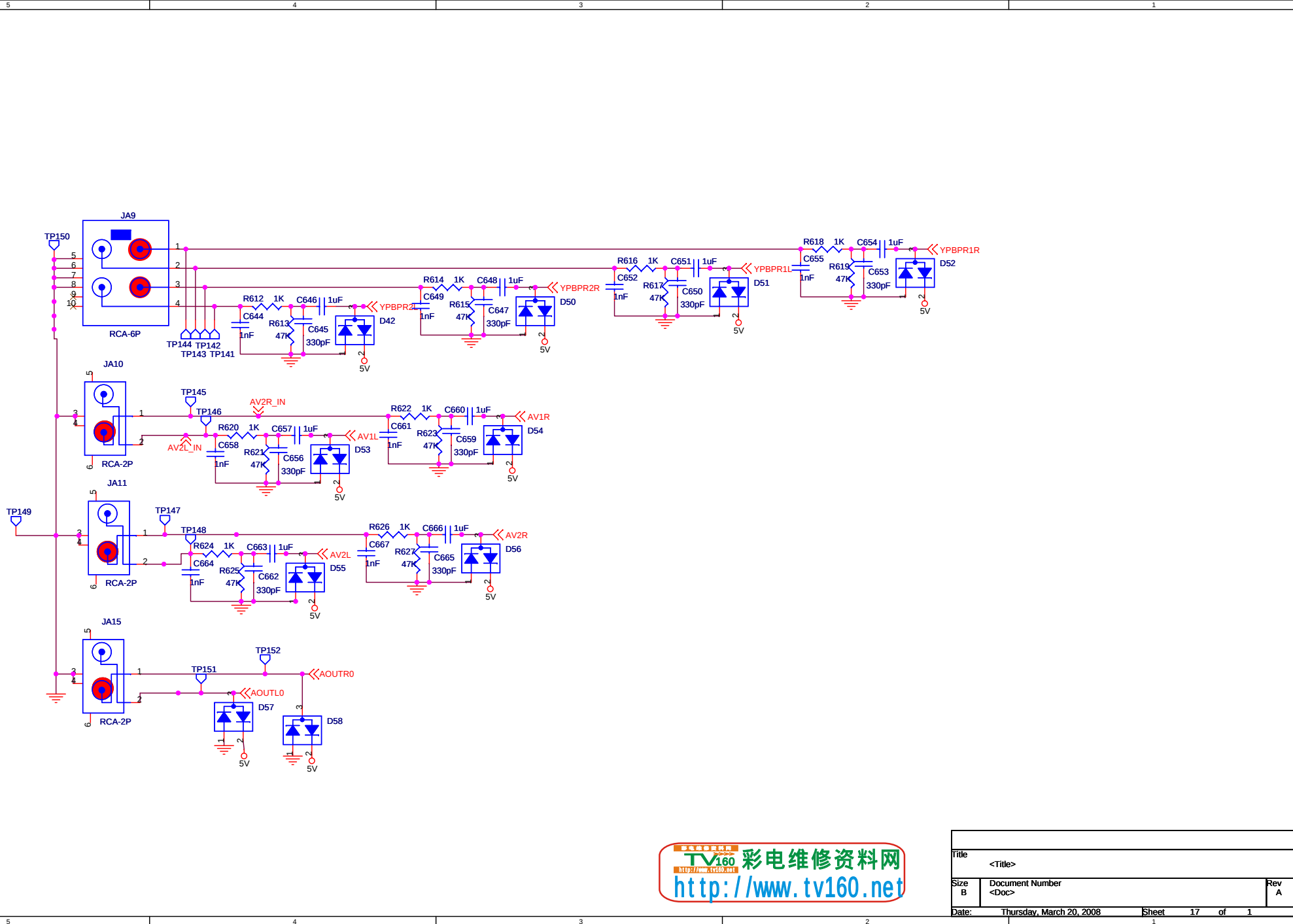
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12:POWER1			
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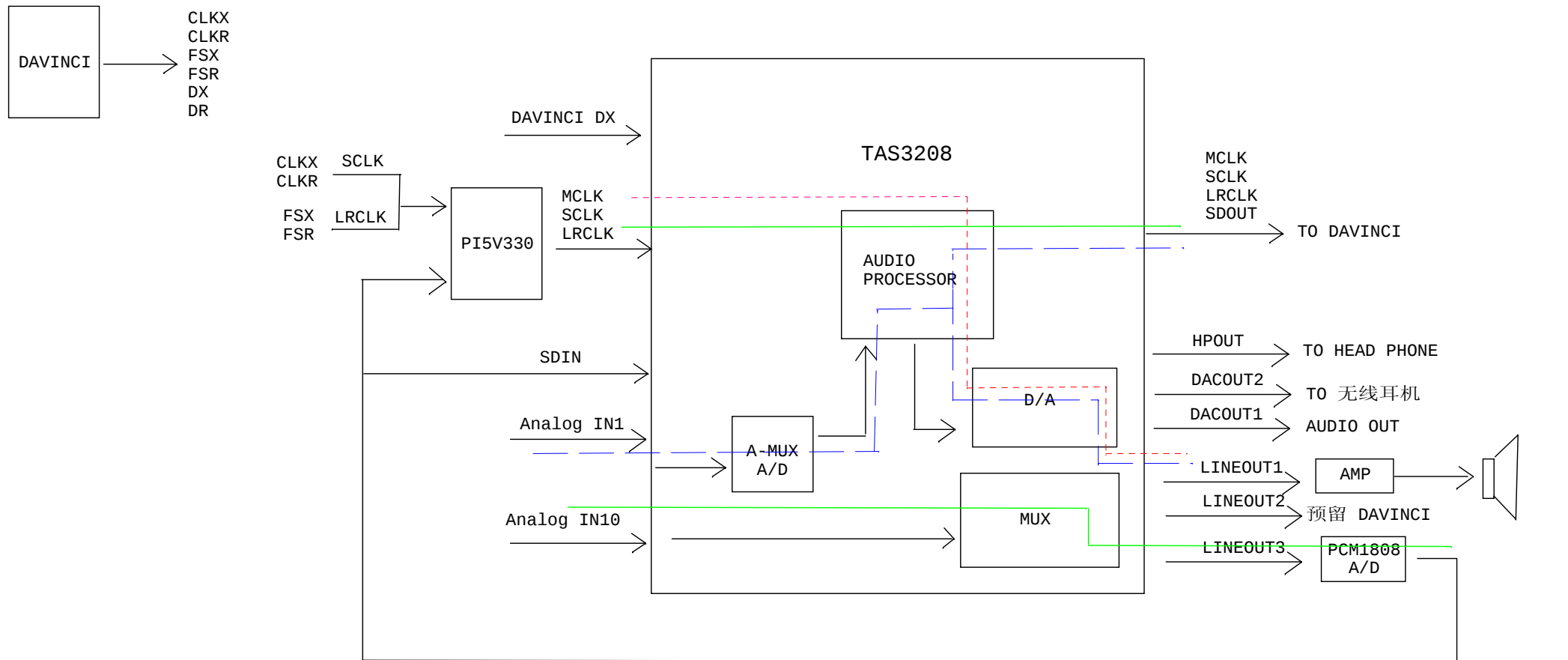






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TV160 彩电维修资料网
<http://www.tv160.net>

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主通道（喇叭）始终要有音效
 普通播放时 ————
 录当前通道时 ————
 录其他通道时 ————