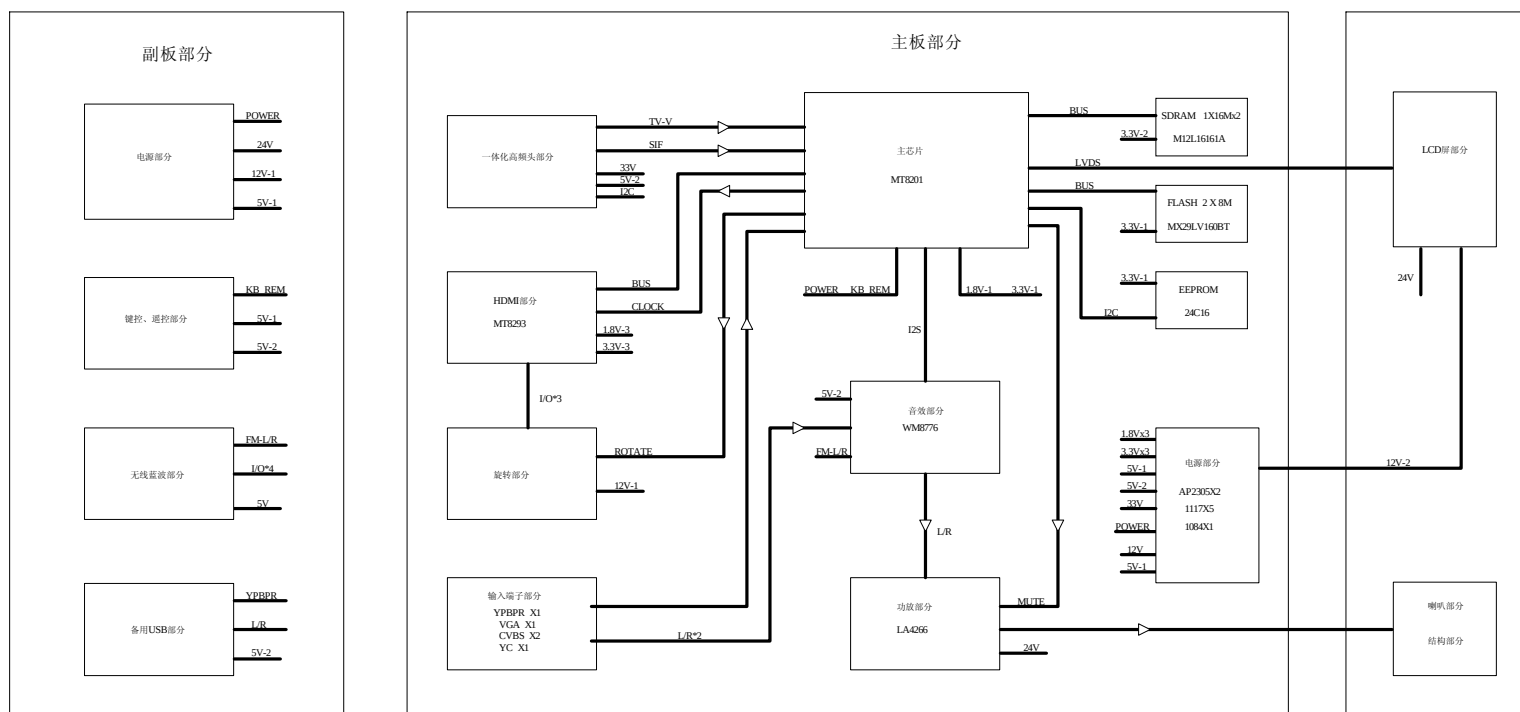
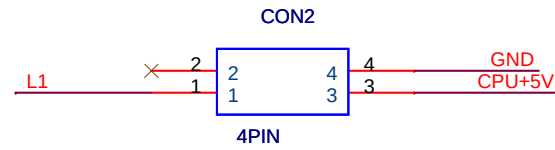
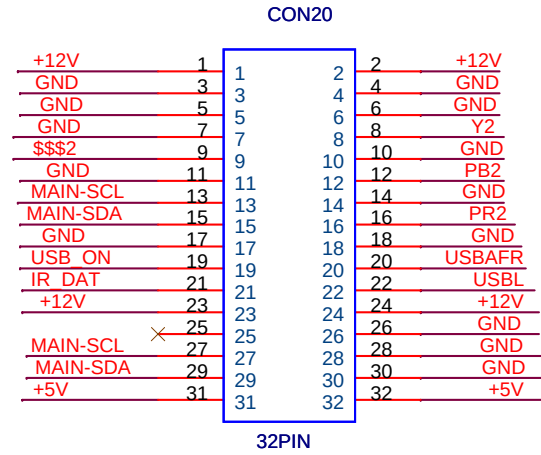
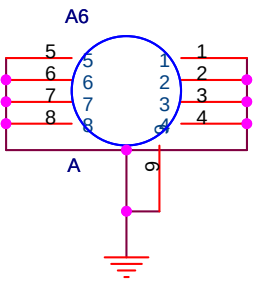
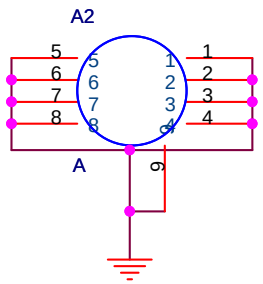
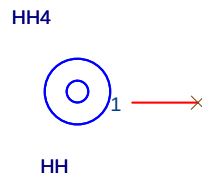
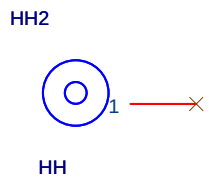
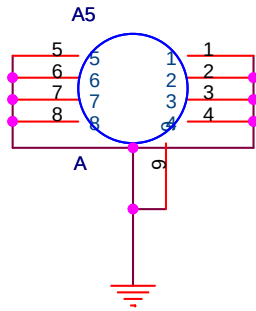
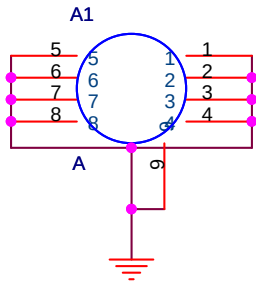


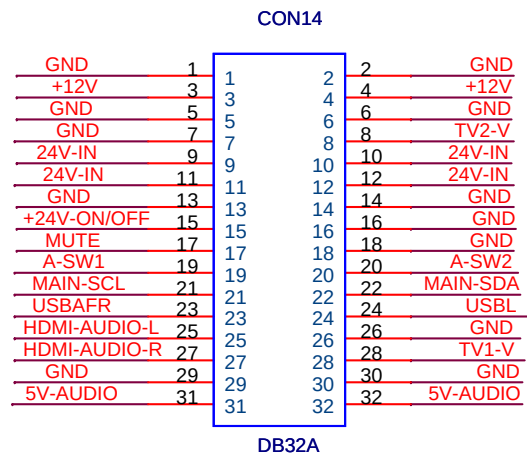
8TM1-MT8201系统框图



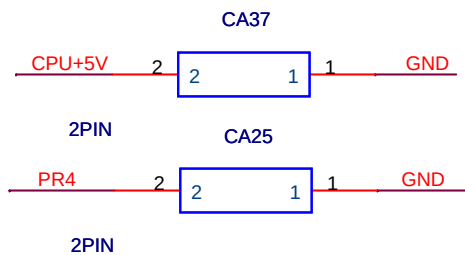
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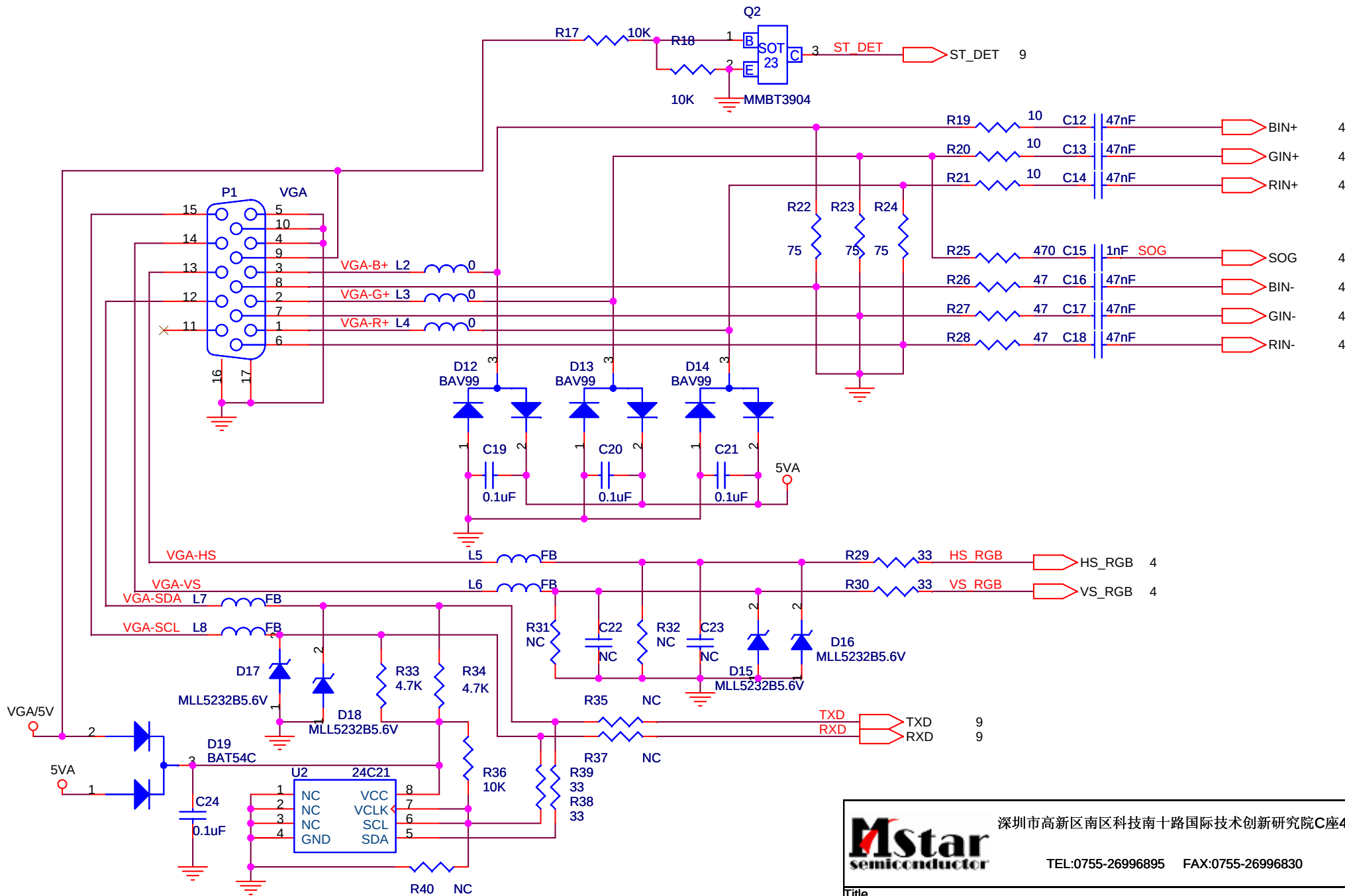


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Date:	Wednesday, March 15, 2006		Sheet 1 of 1



DB32A



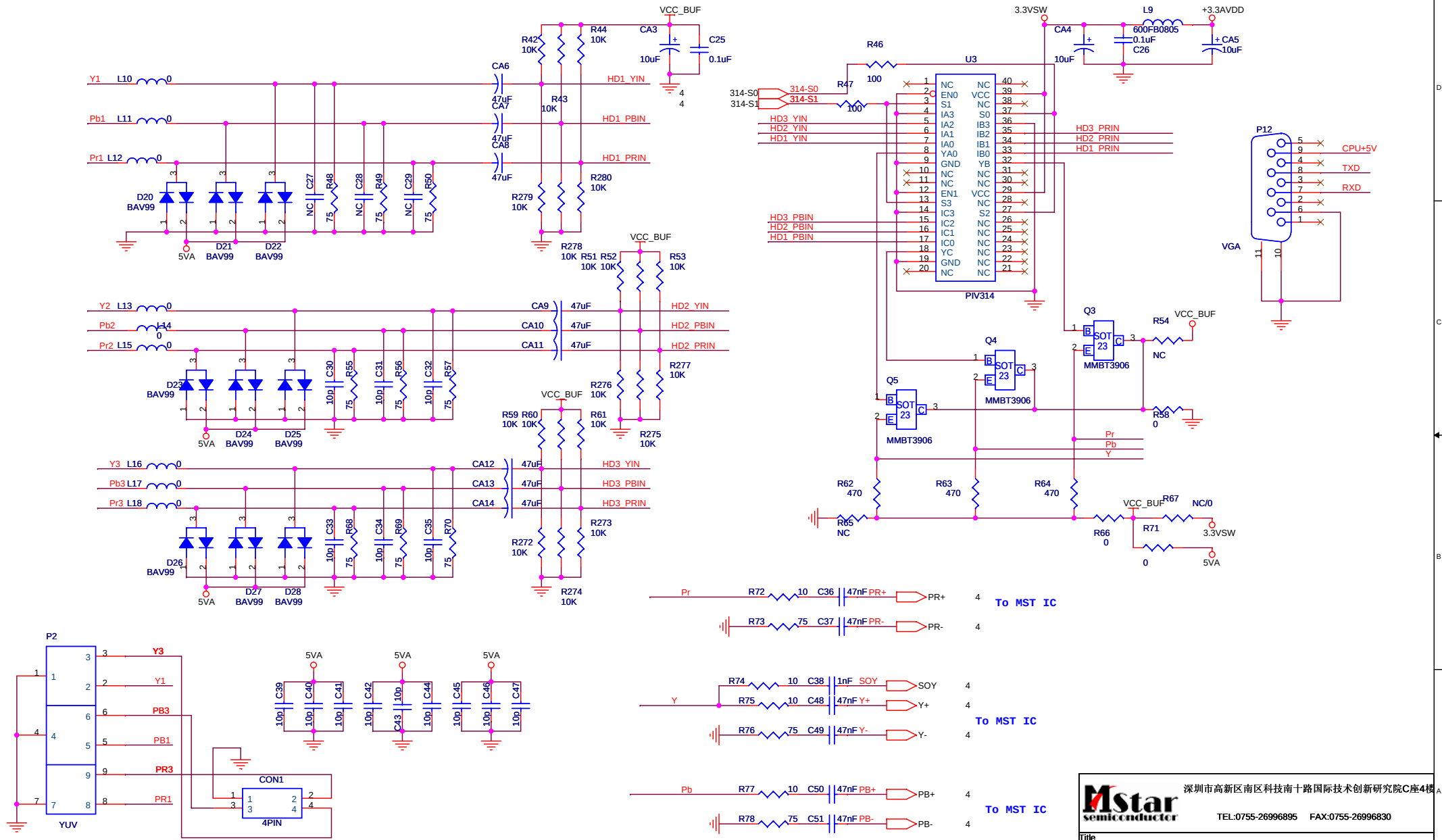


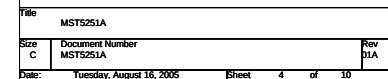


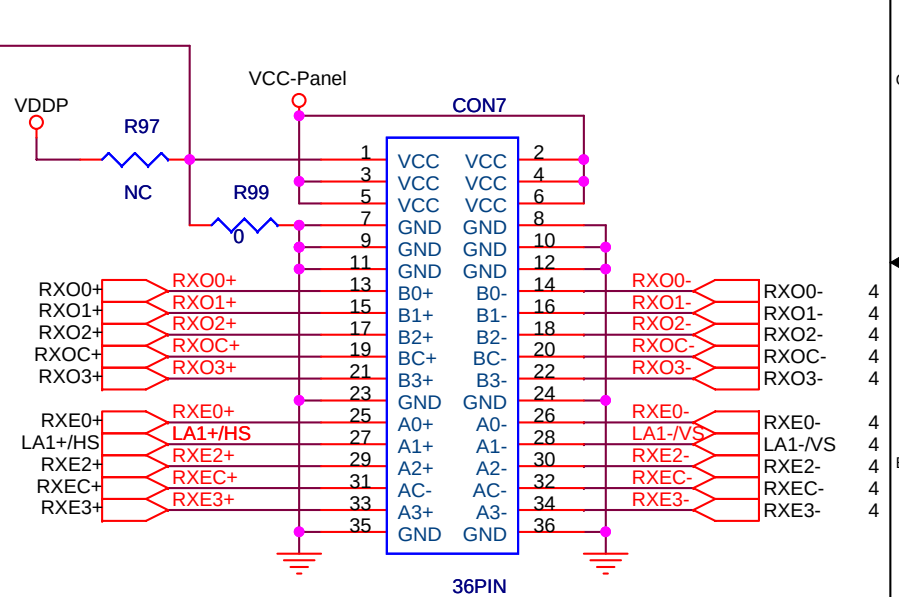
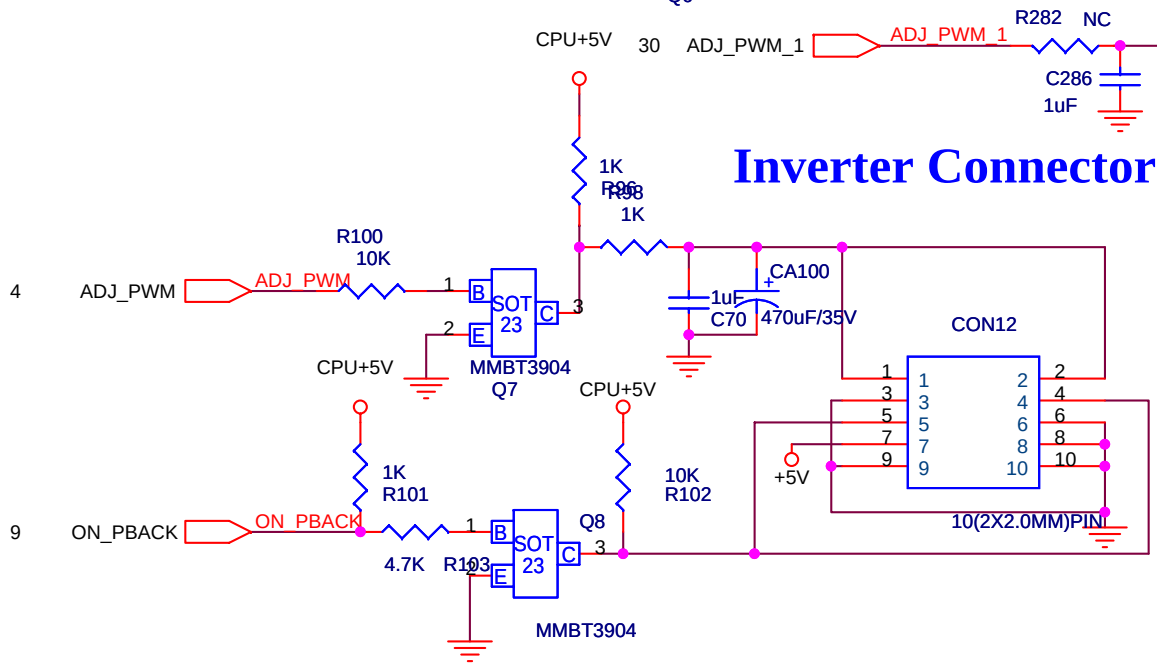
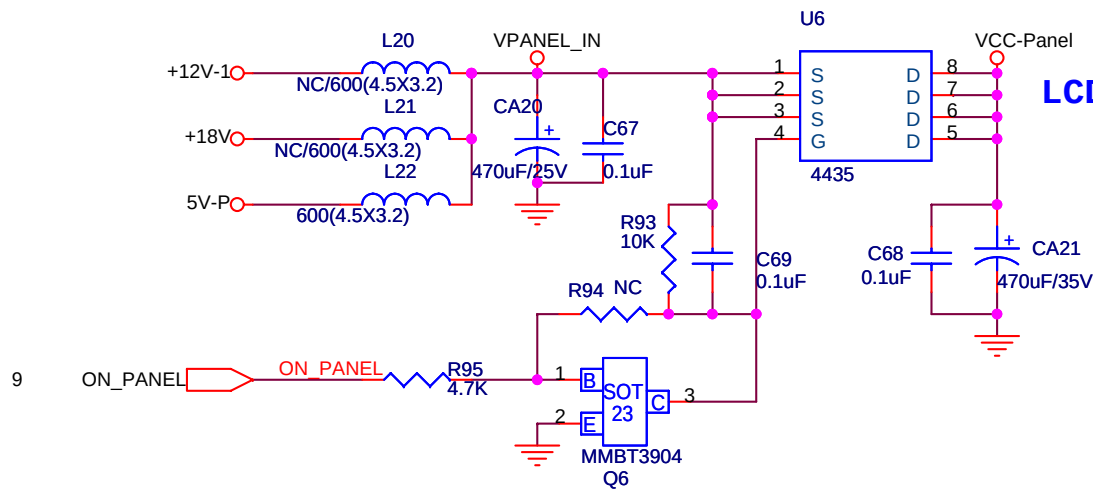
深圳市高新区南区科技南十路国际技术创新研究院C座4楼

TEL:0755-26996895 FAX:0755-26996830

Title VGA		
Size A	Document Number MST5251A	Rev 01A
Date:	Wednesday, March 15, 2006	Sheet 2 of 10







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TEL:0755-26996895 FAX:0755-26996830

Title

LVDS Panel IF

Size
A

Document Number
MST5251A

Rev
01A

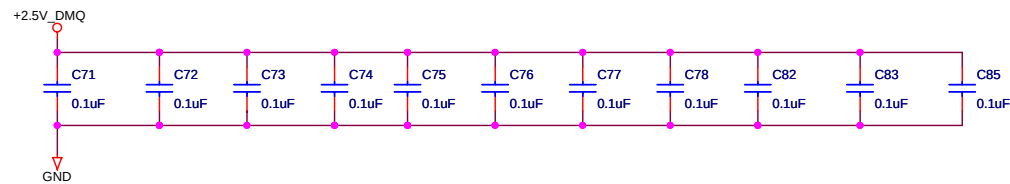
Date: Wednesday, March 15, 2006

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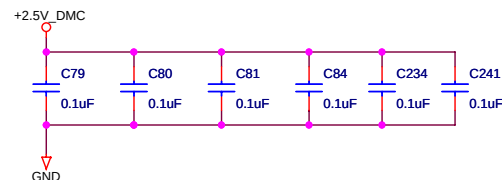
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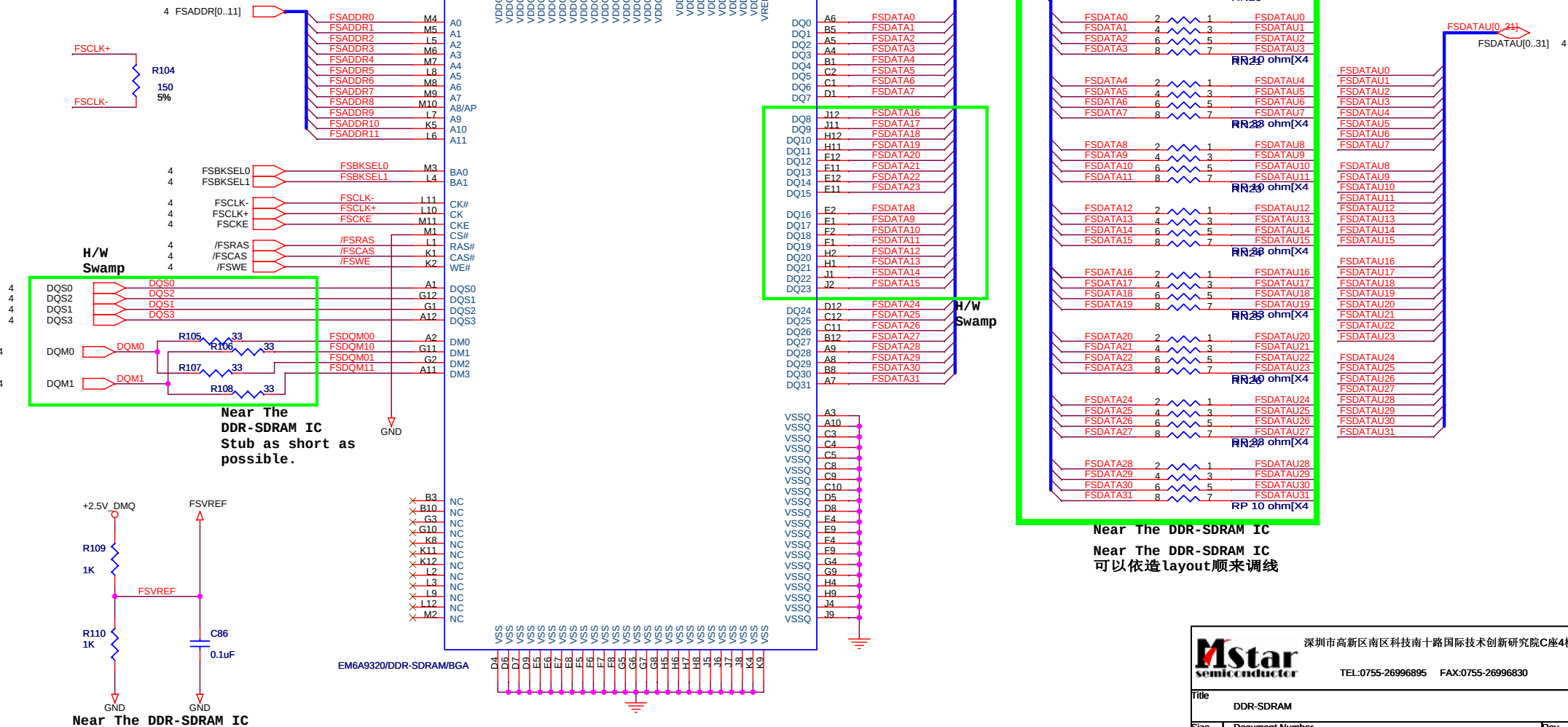
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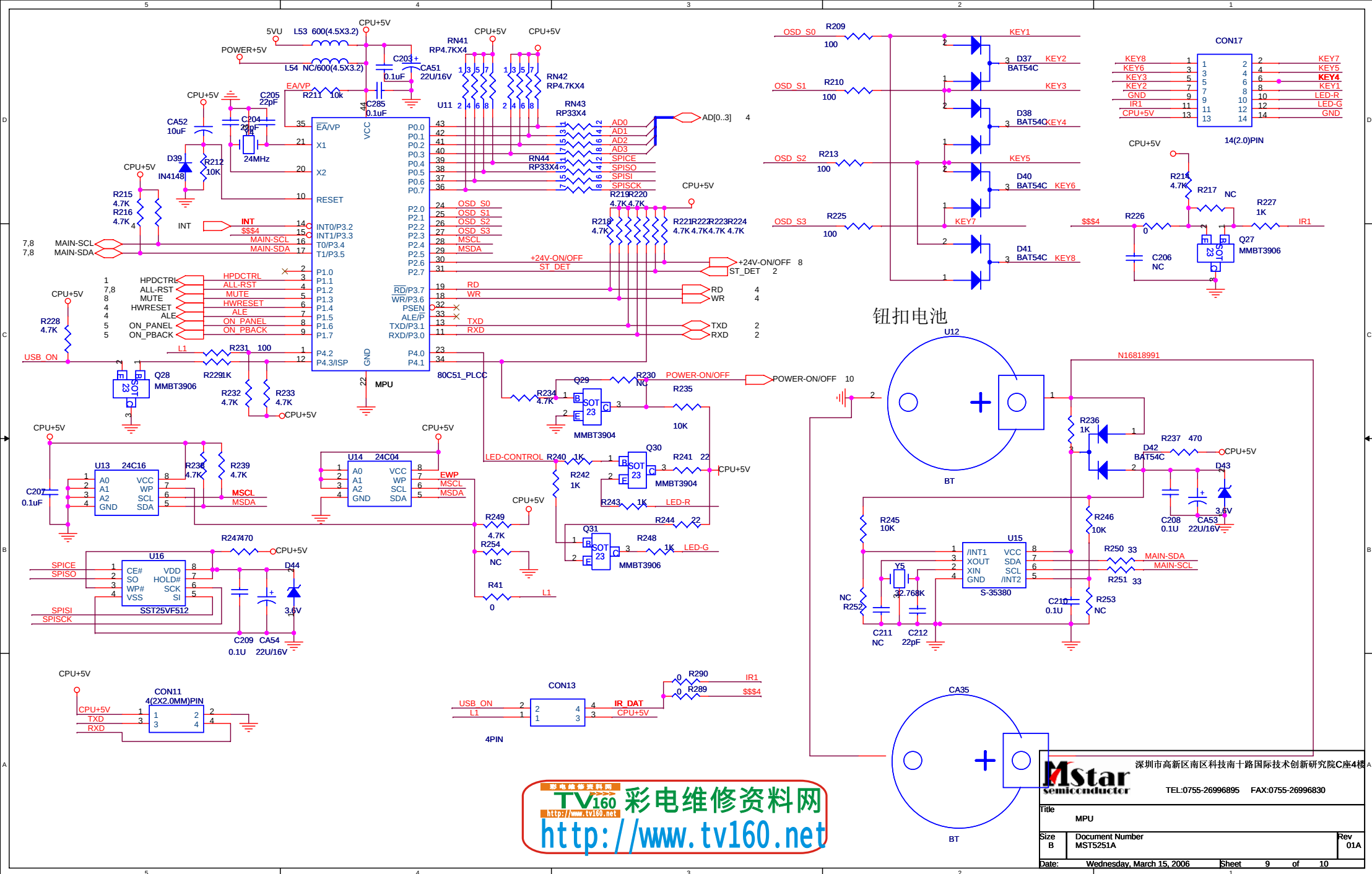


Place this parallel termination close to corresponding memory IC Pins

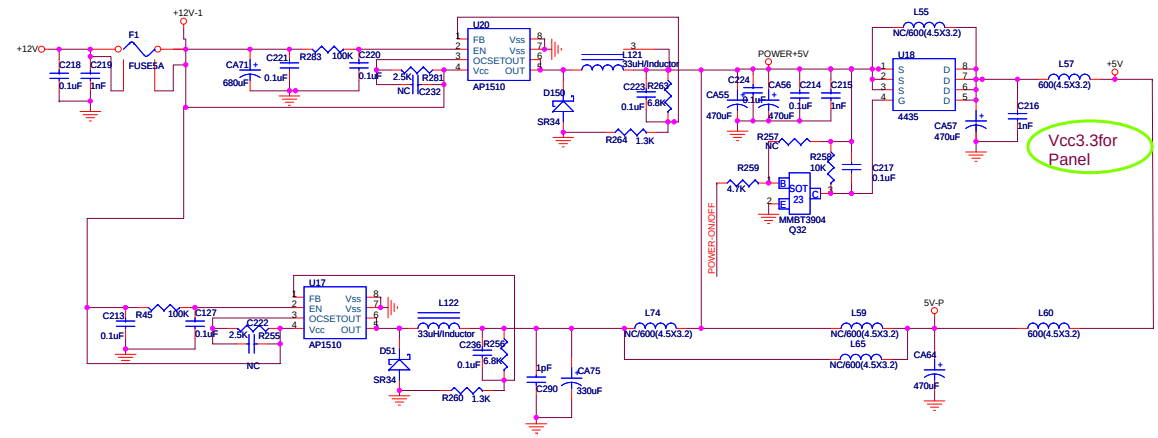


+2.5V_DMQ +2.5V_DMC FSVREF

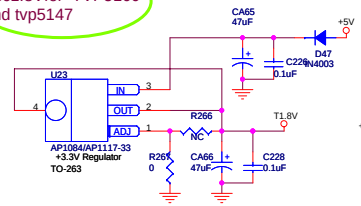




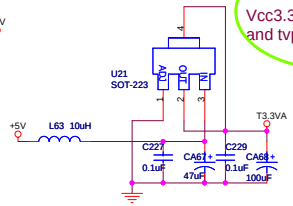
+12V: 12V DC
ADAPTER IN



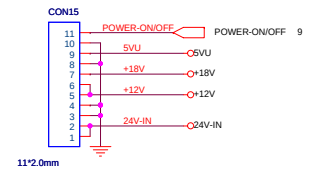
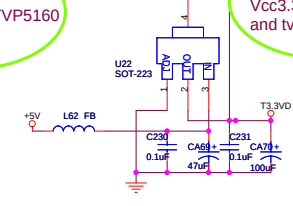
Vcc1.8Vfor TVP5160
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Vcc3.3VA for TVP5160
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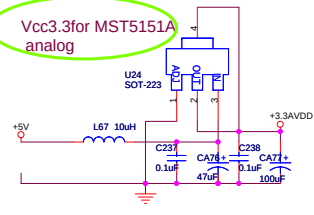


Vcc3.3VD for TVP5160
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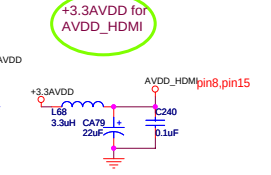


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TV160 彩电维修资料网
<http://www.tv160.net>

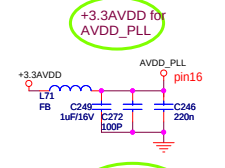
Vcc3.3for MST5151A
analog



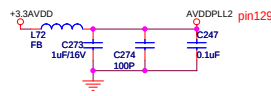
+3.3AVDD for
AVDD_HDMI



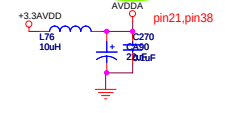
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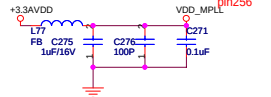
3.3V(6251) for
AVDDPLL2



+3.3AVDD for
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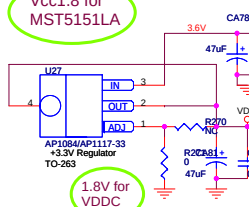


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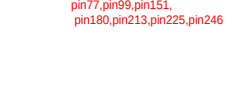


$1.25 \times (1 + R188/R187) = 1.8V \text{---} R187=100R, R188=44R$

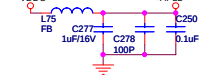
Vcc1.8 for
MST5151LA



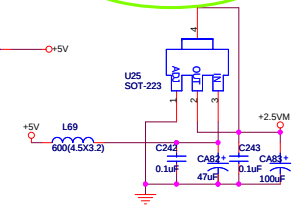
1.8V for
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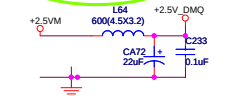
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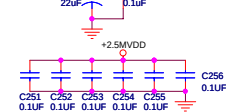
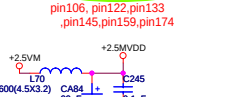
2.5V for MST5151A and
DDRAM

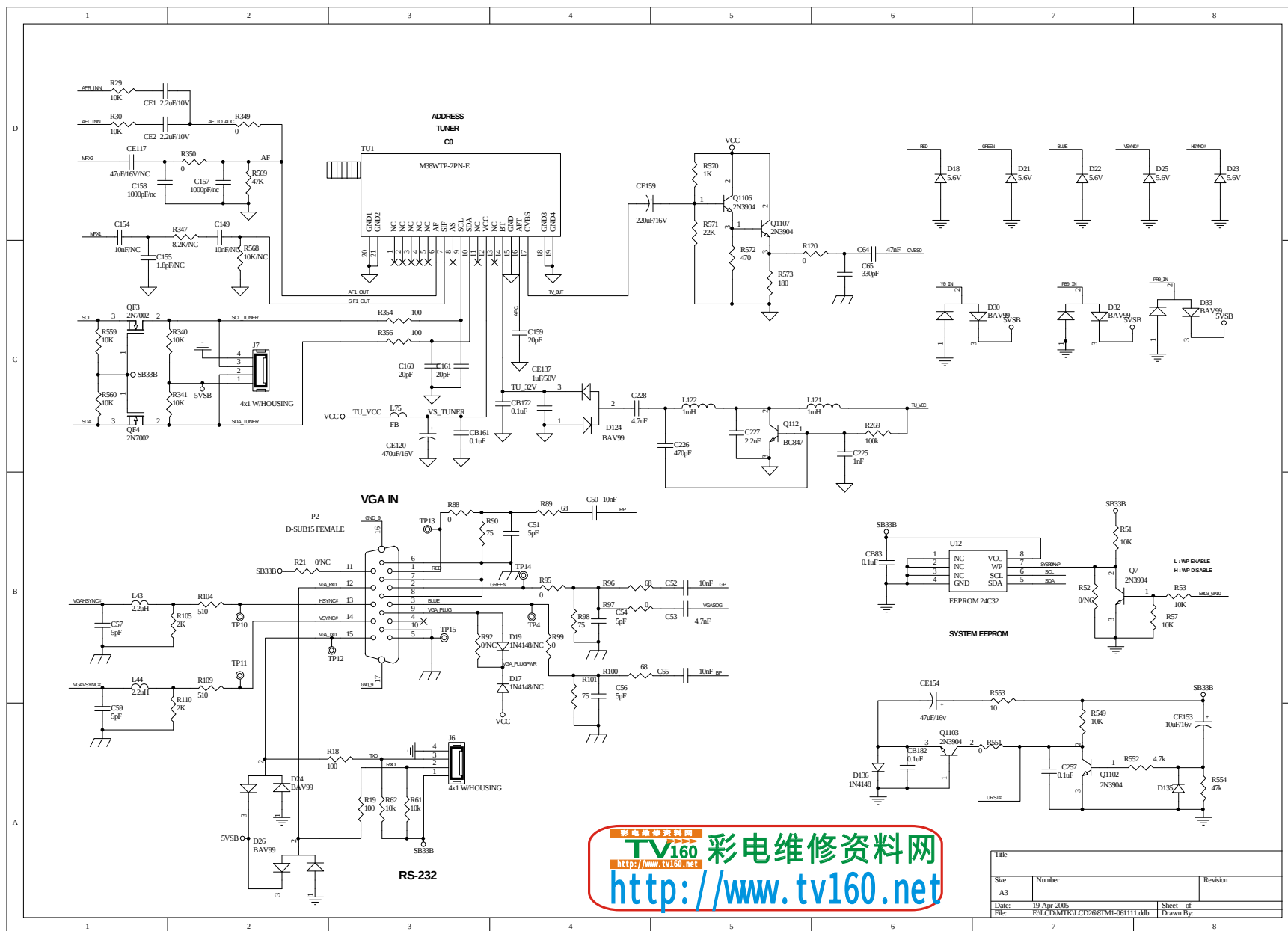


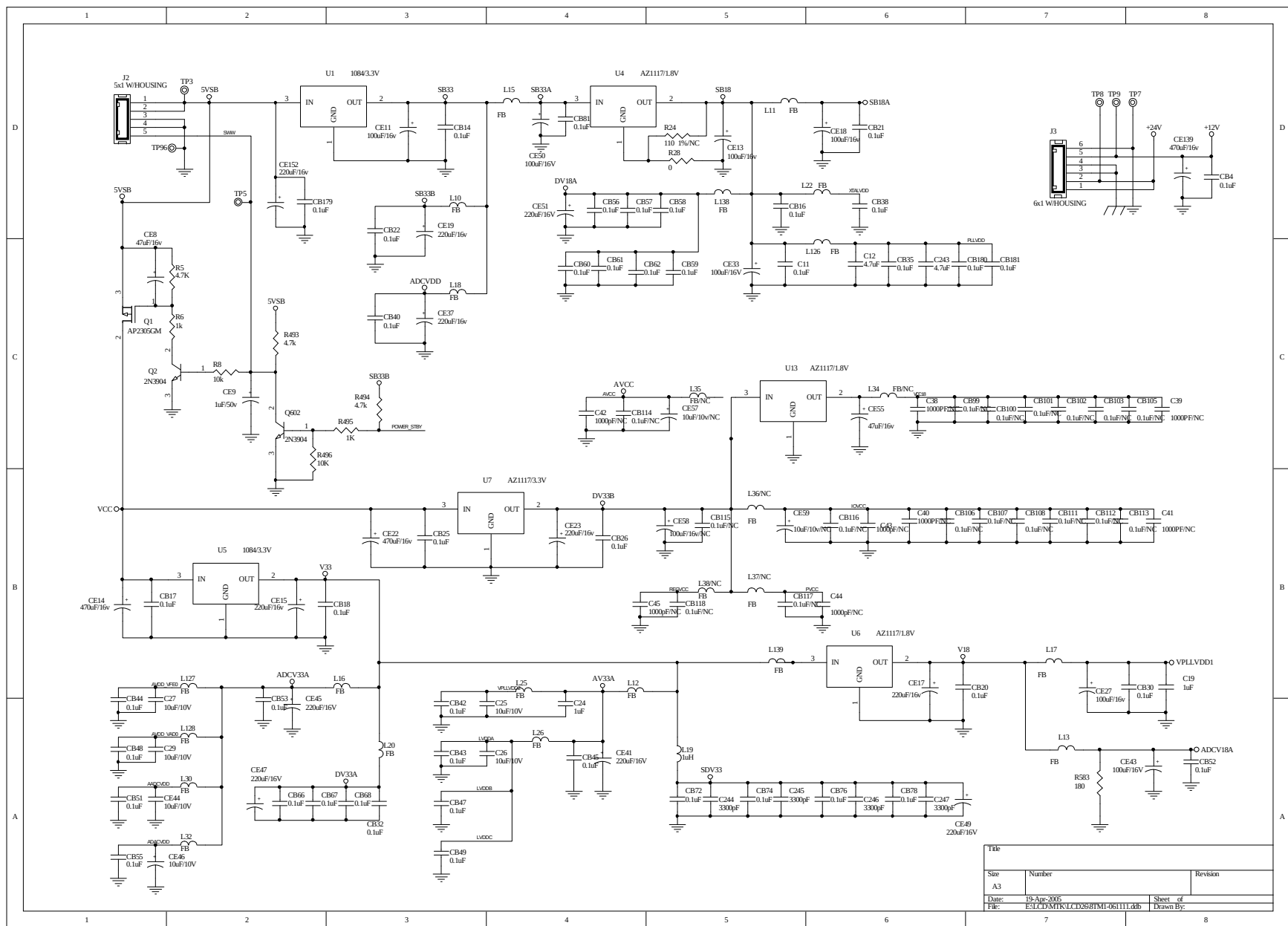
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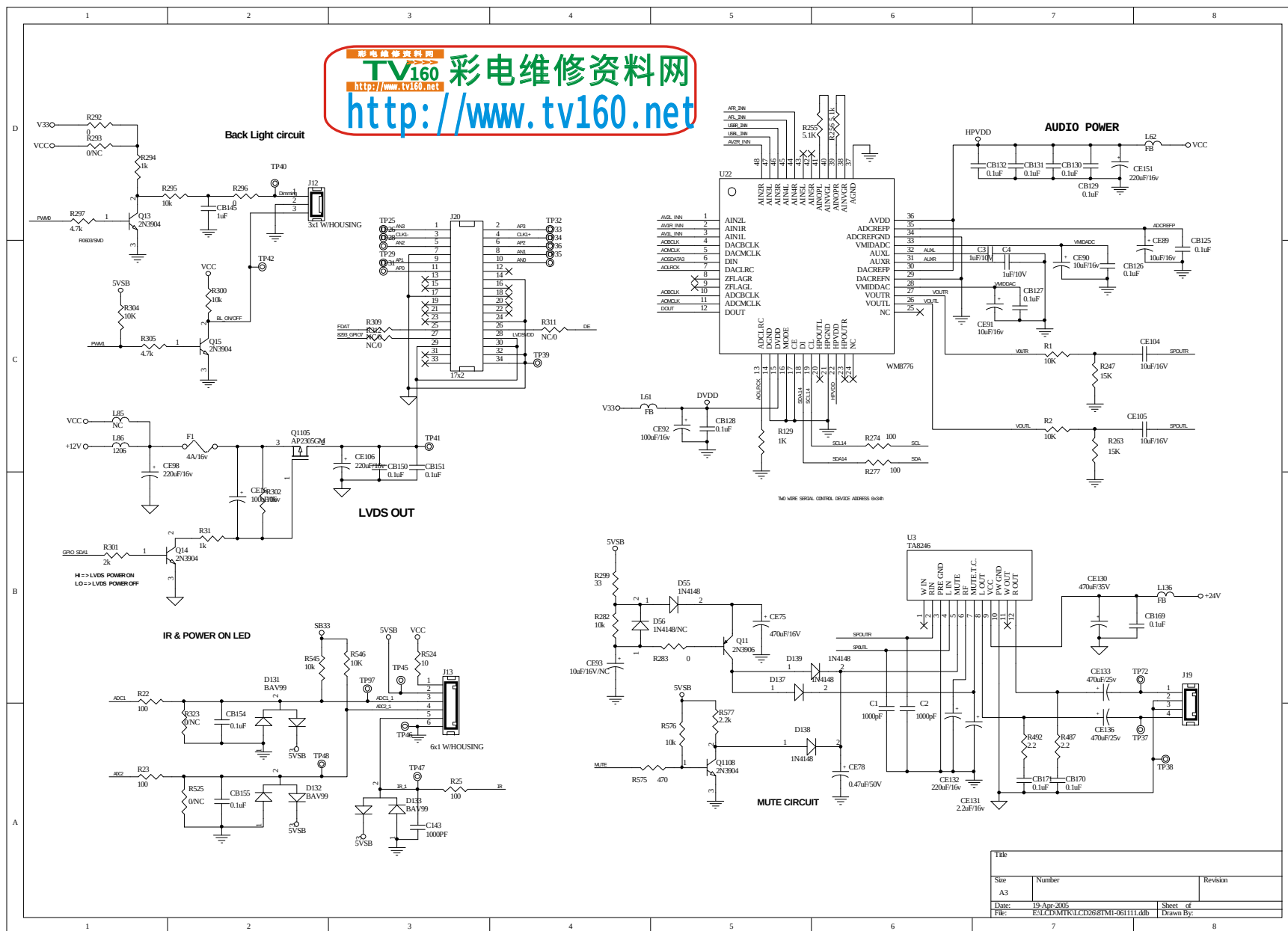


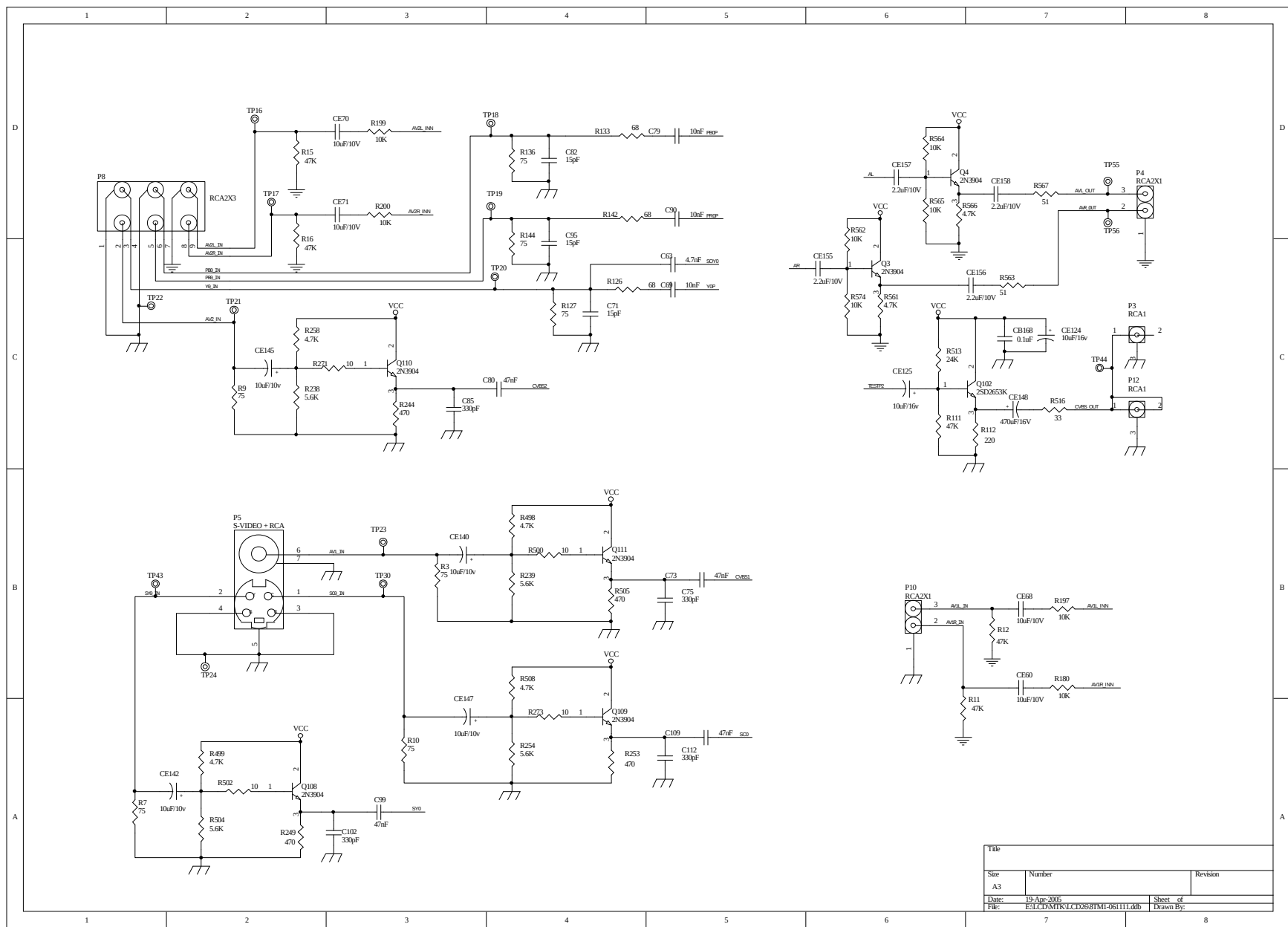
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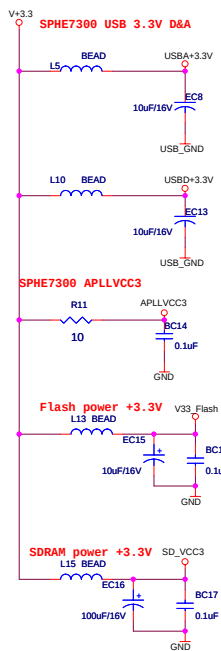
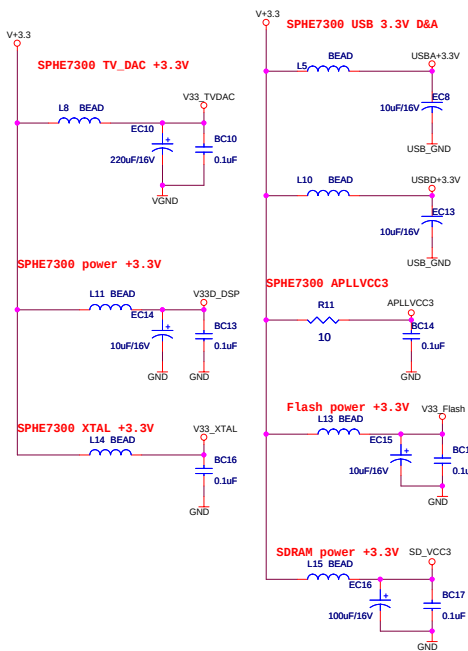
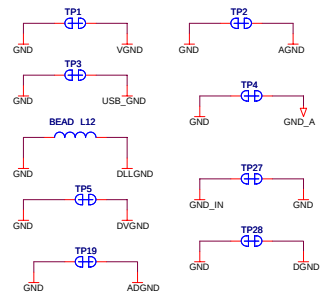
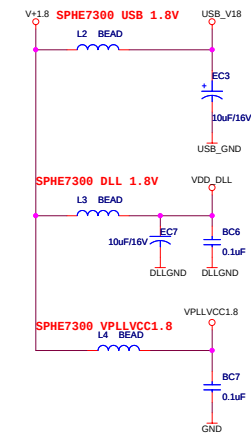
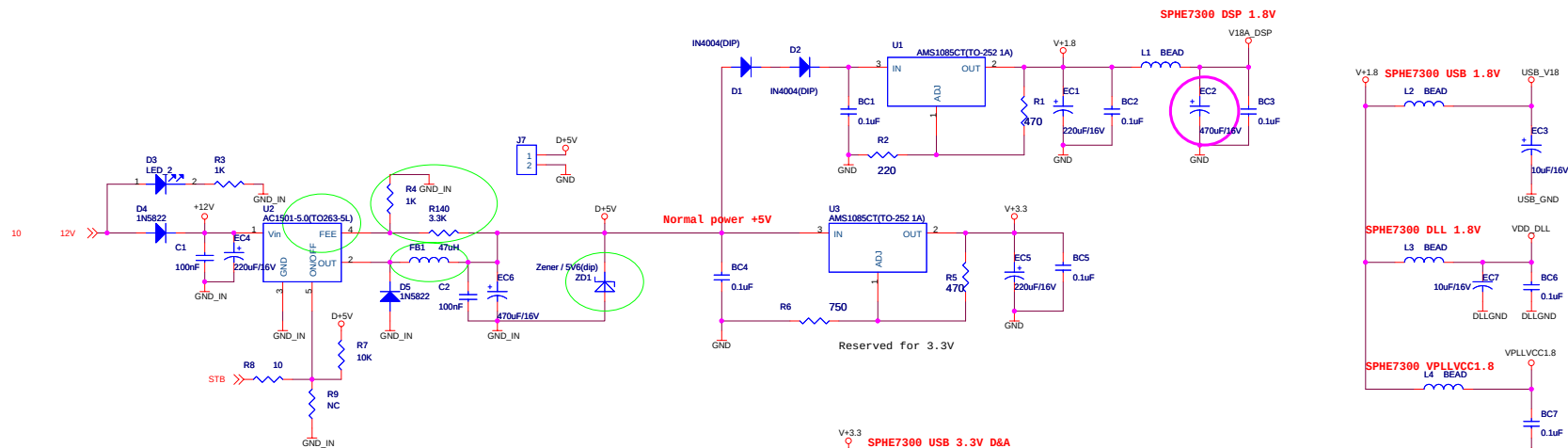


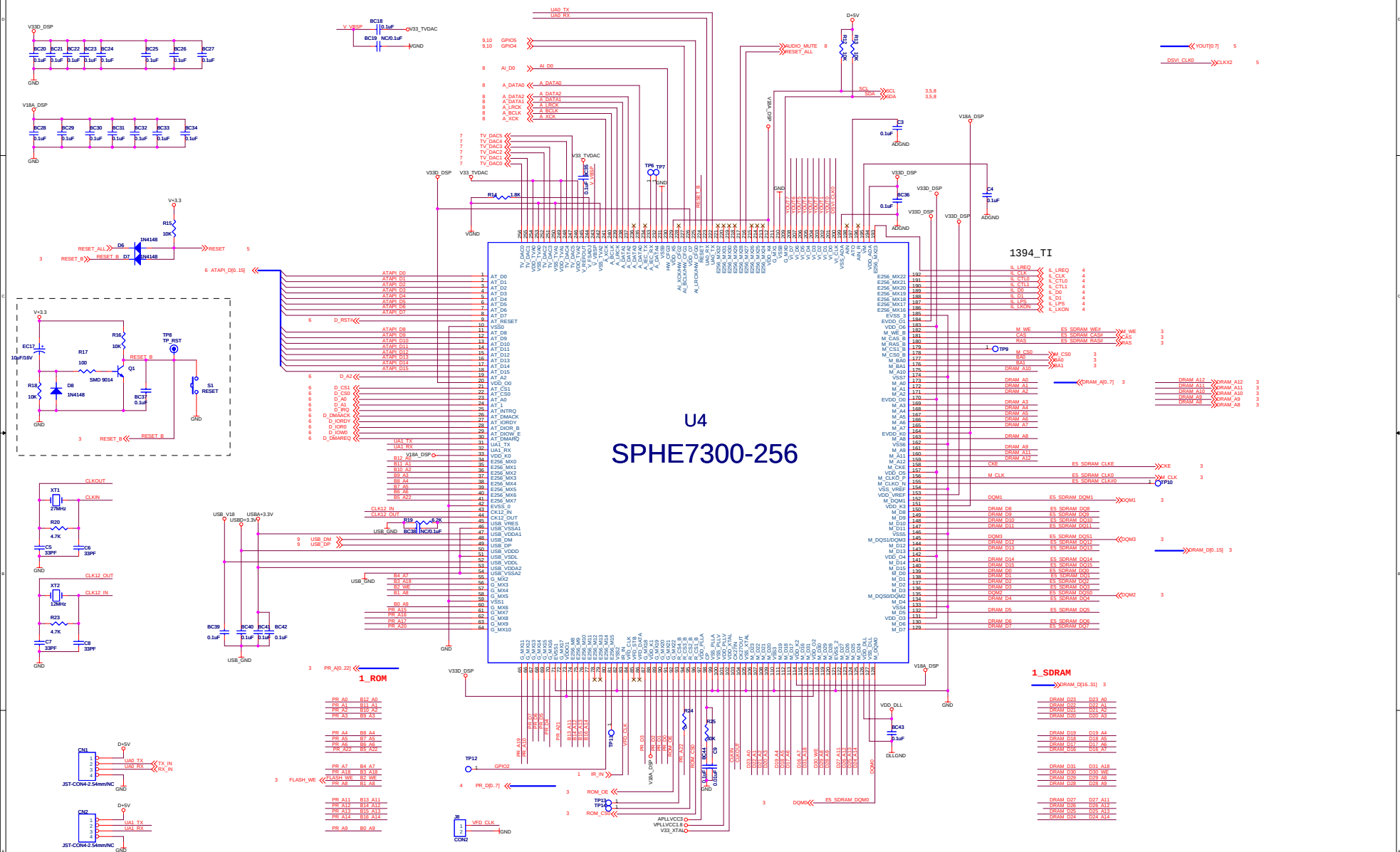


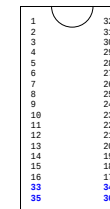


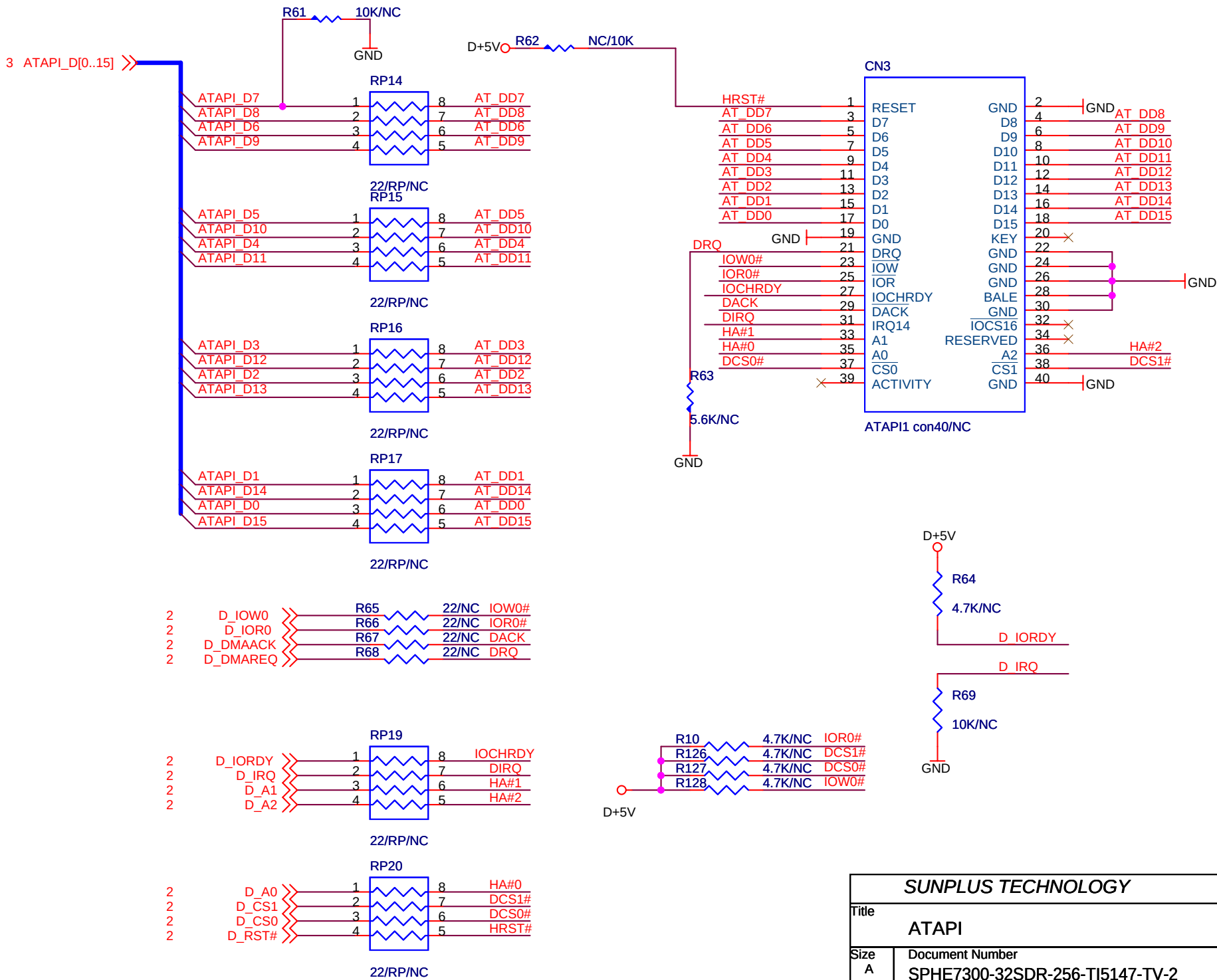




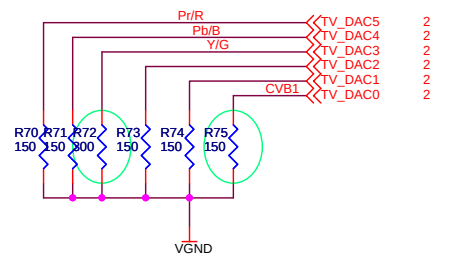
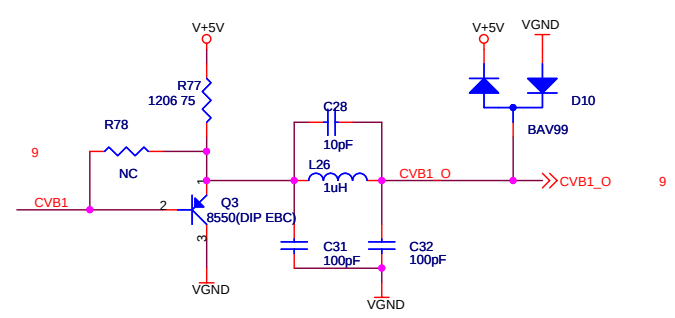
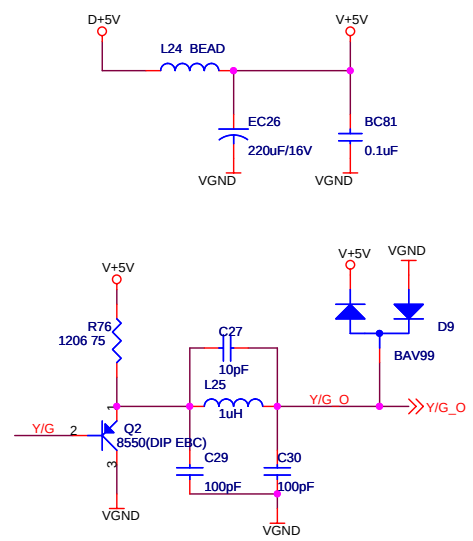




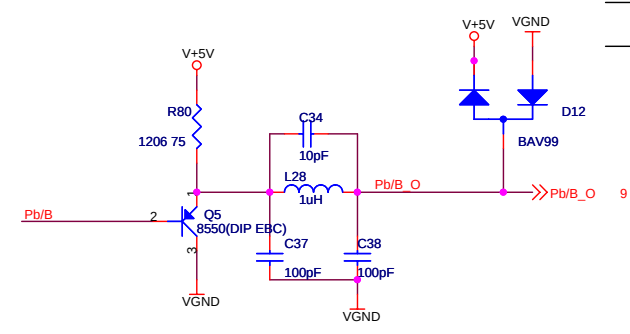
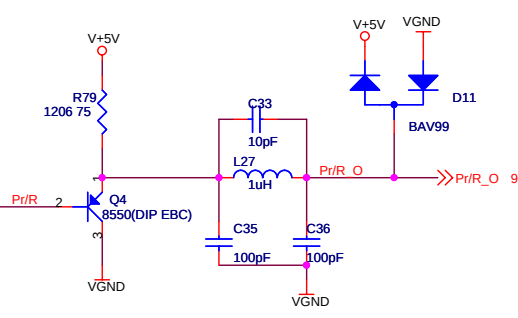


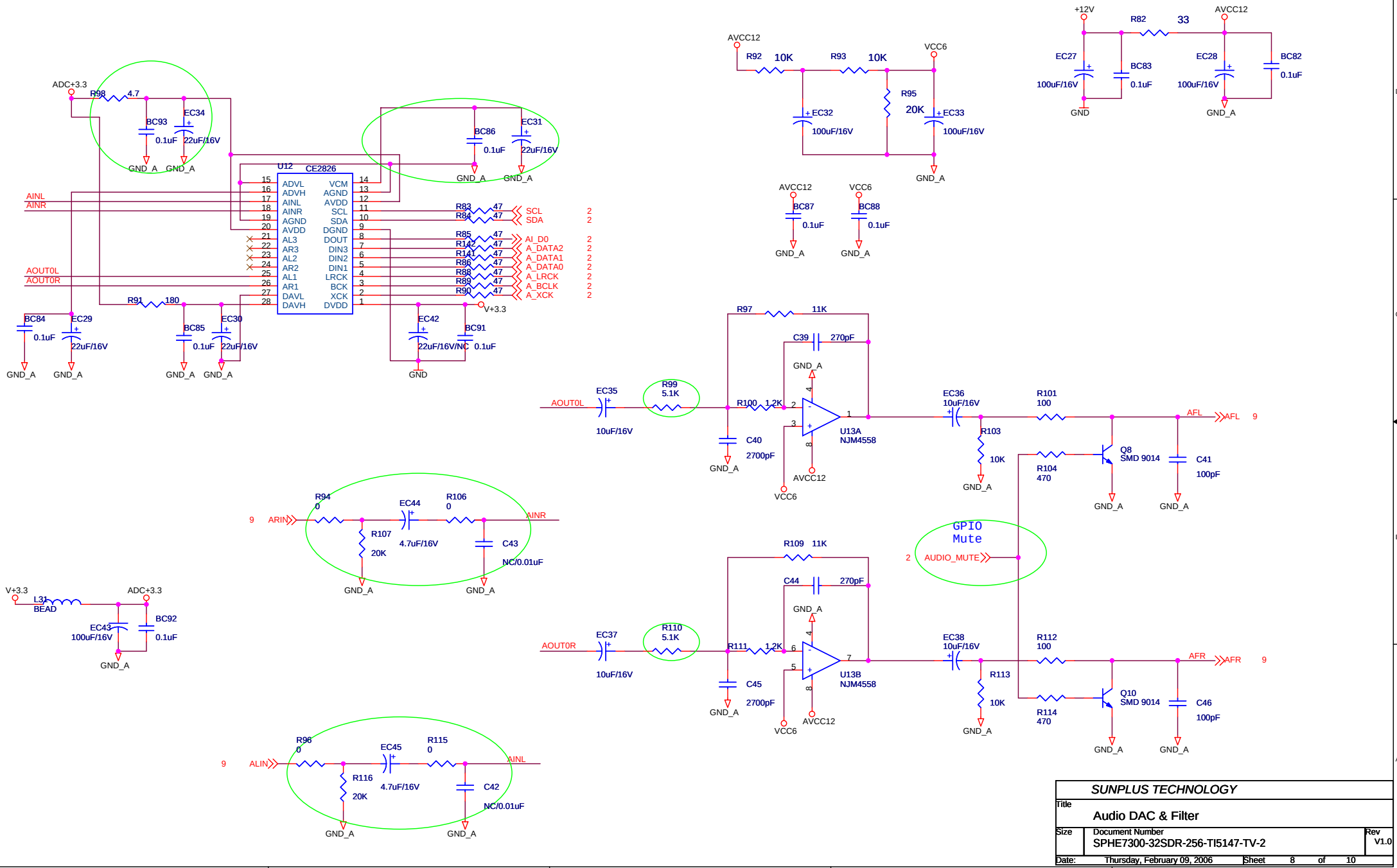


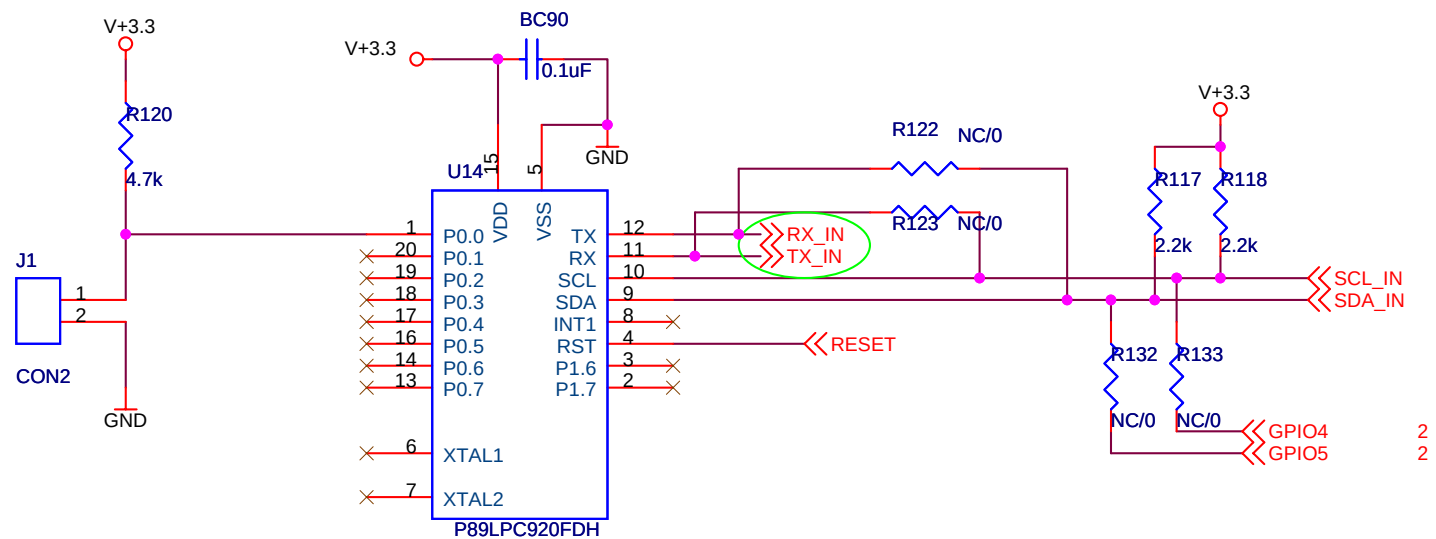
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Size	Document Number	Rev
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Date:	Thursday, February 09, 2006	Sheet 6 of 10



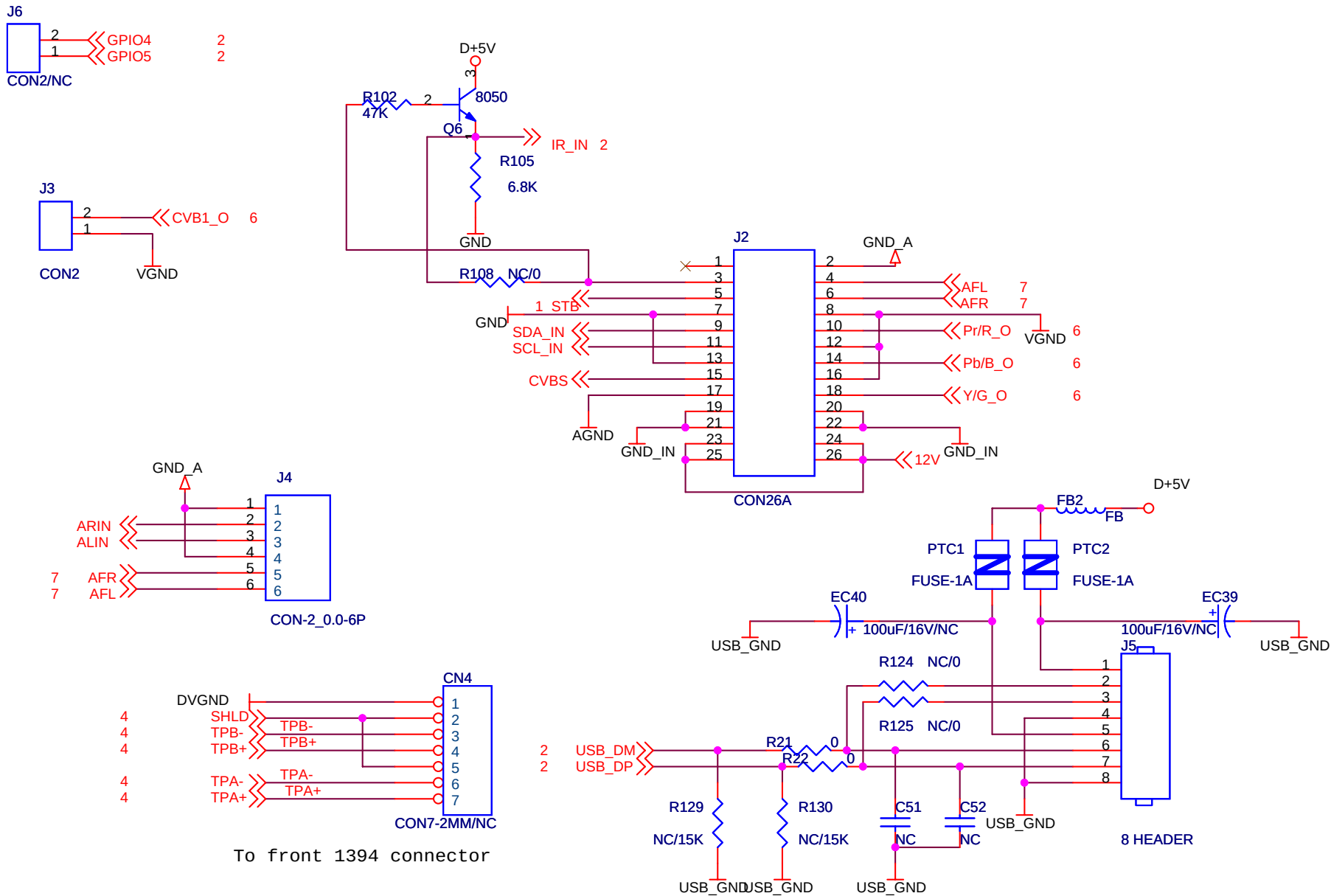
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CVB1_0(DAC0)	CVB Out
Y/G_0 (DAC3)	跌纏だ祇 Y ー RGB R ー Y(TV)
Pb/B_0 (DAC4)	跌纏だ祇 Pb ー RGB B ー U
Pr/R_0 (DAC5)	跌纏だ祇 Pr ー RGB R ー V







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USB & MCU			
Size	Document Number		Rev
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To front 1394 connector

SUNPLUS TECHNOLOGY		
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Connector		
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	SPHE7300-32SDR-256-TI5147-TV-2	V1.0
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2006.01.21

SPHE7300-32SDR-256-TI5147-TV-1 to SPHE7300-32SDR-256-TI5147-TV-2

1.page8:modify ce2826 13 15 19 pin;del HW mute;modify Audio input

2.page1:DC-DC use AP1501 and lm2576,add 5.6V regulator

3.page9:del 11.0592MHz crystal;modify MCU rx,tx;del USB power ctrl

4.page4,6:all element NC

5.modify some element encapsulation

2006.02.09

page10:add IR dynatron

SUNPLUS TECHNOLOGY

Title

change list

Size

Document Number

SPHE7300-32SDR-256-TI5147-TV-2

Rev

V1.0

Date:

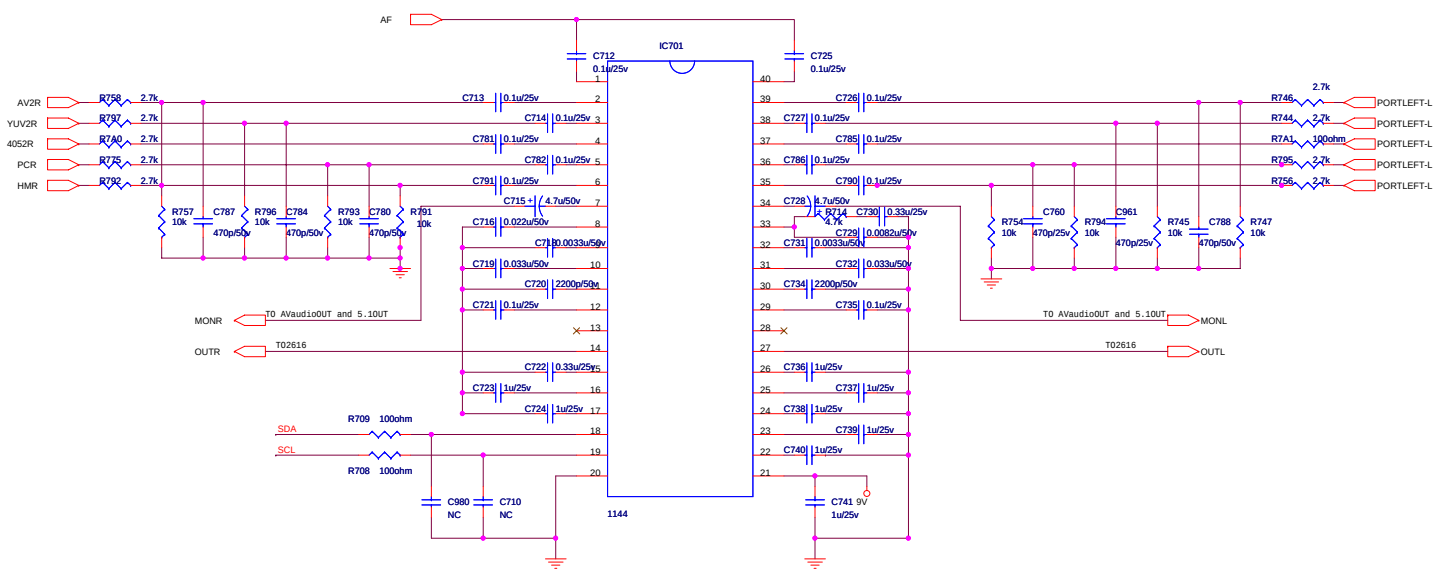
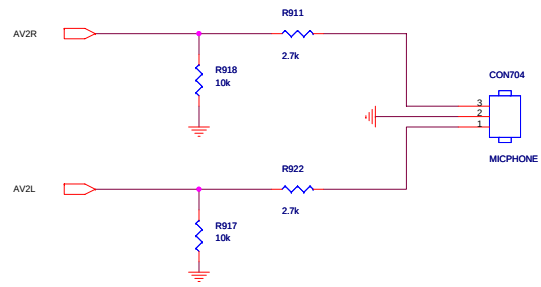
Thursday, February 09, 2006

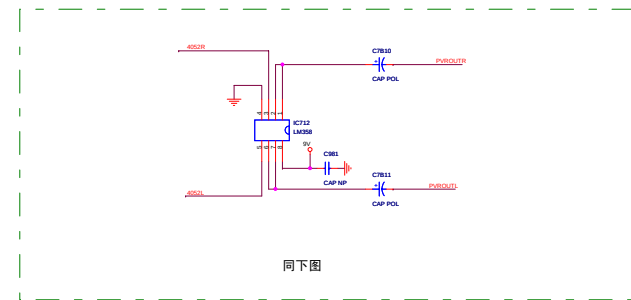
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11

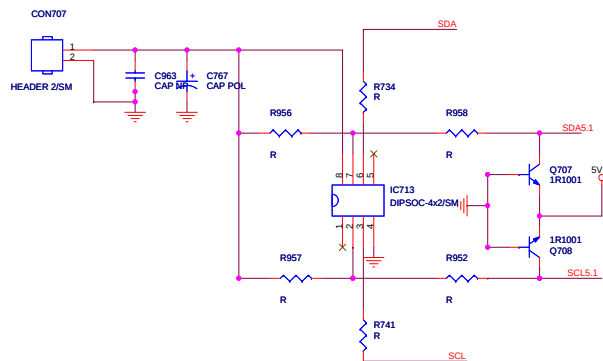
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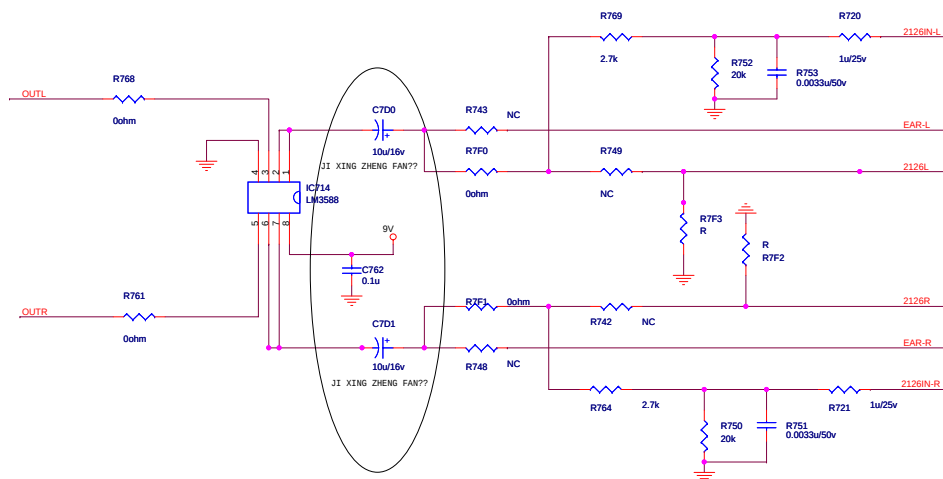
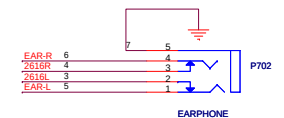
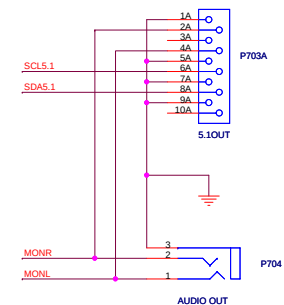


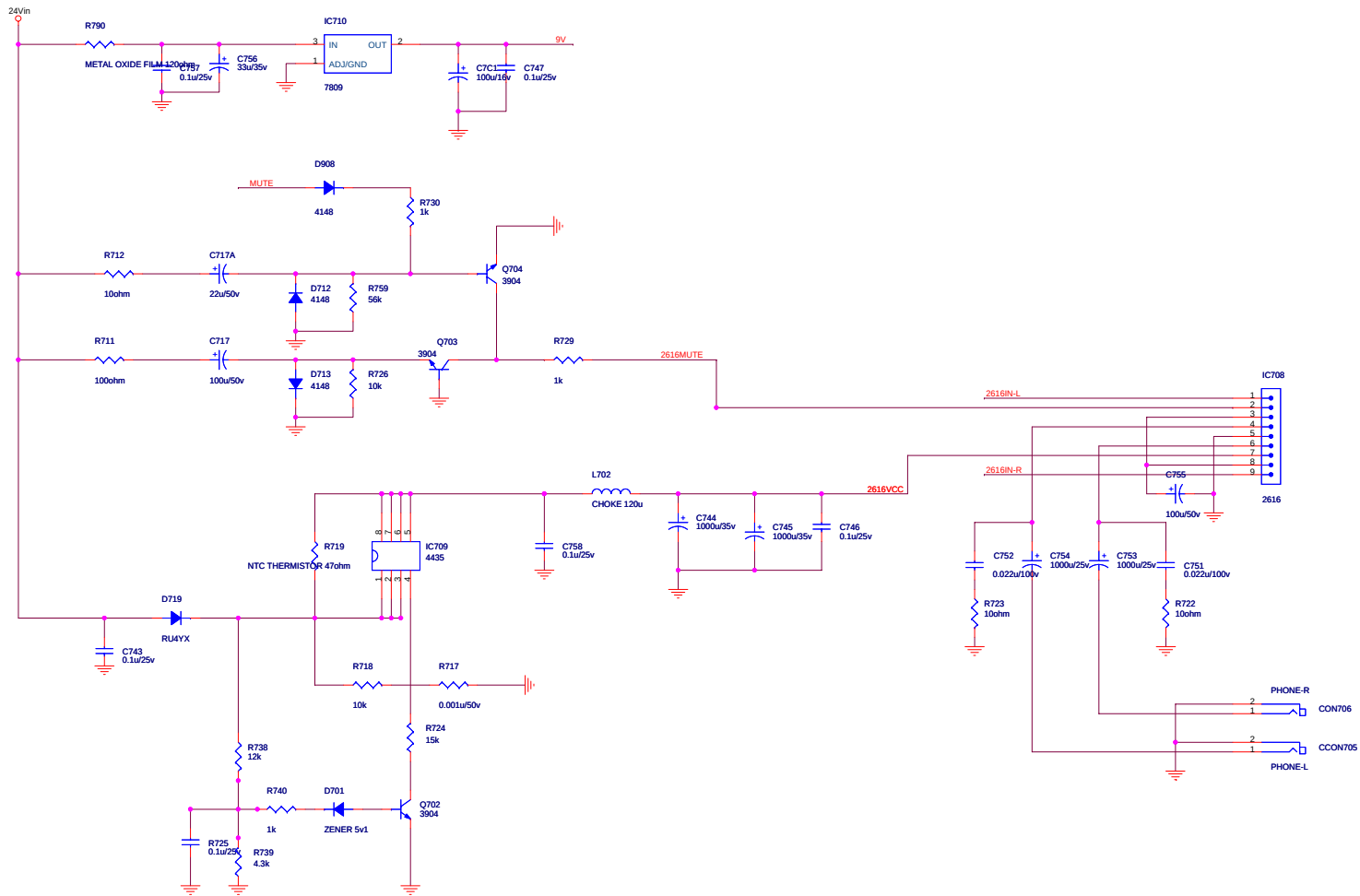


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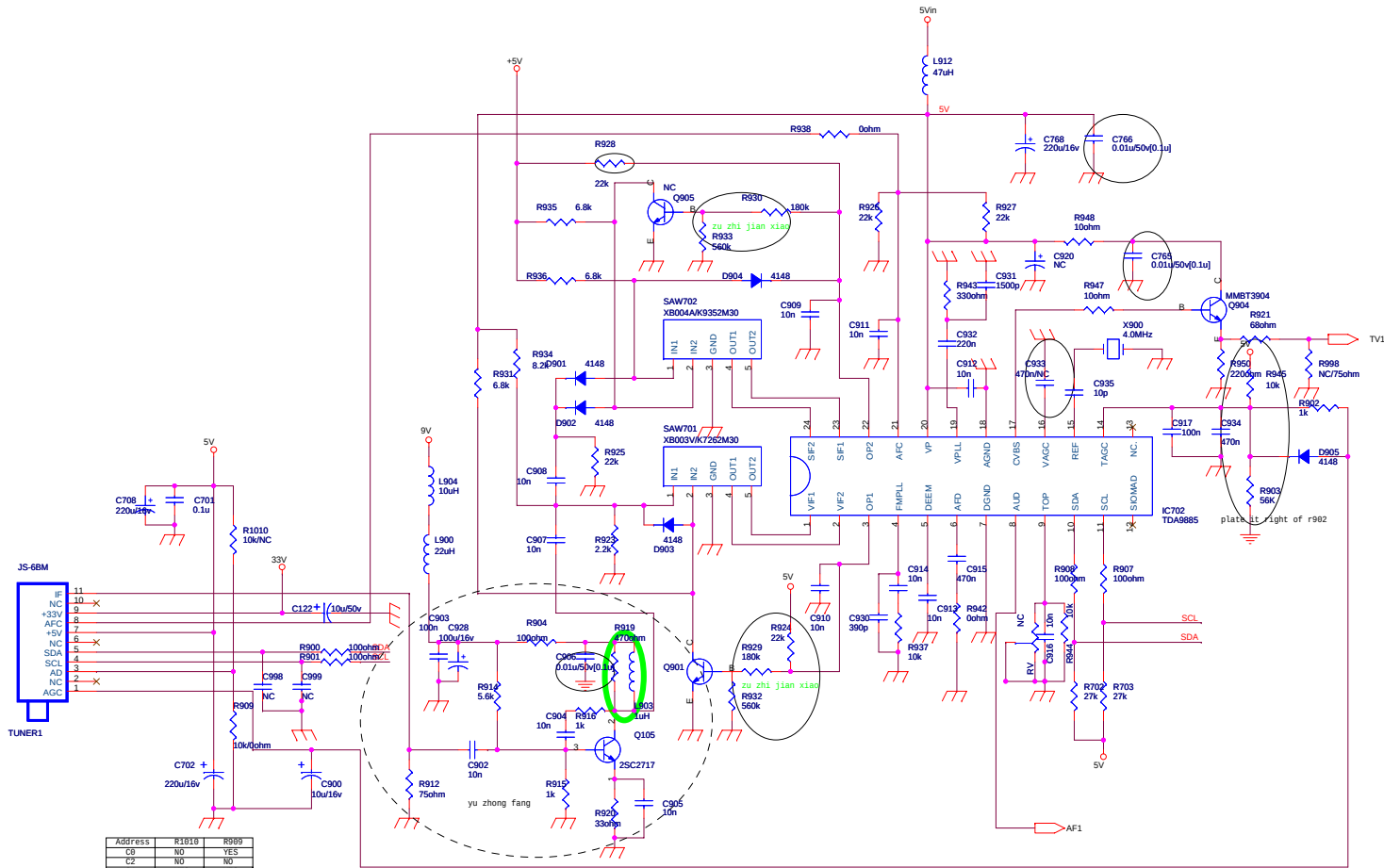
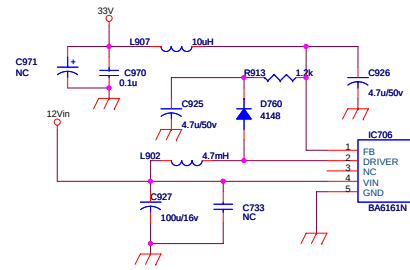
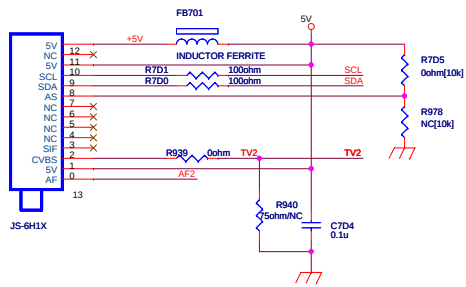


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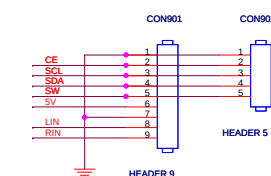
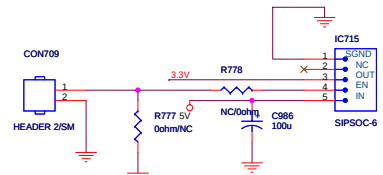
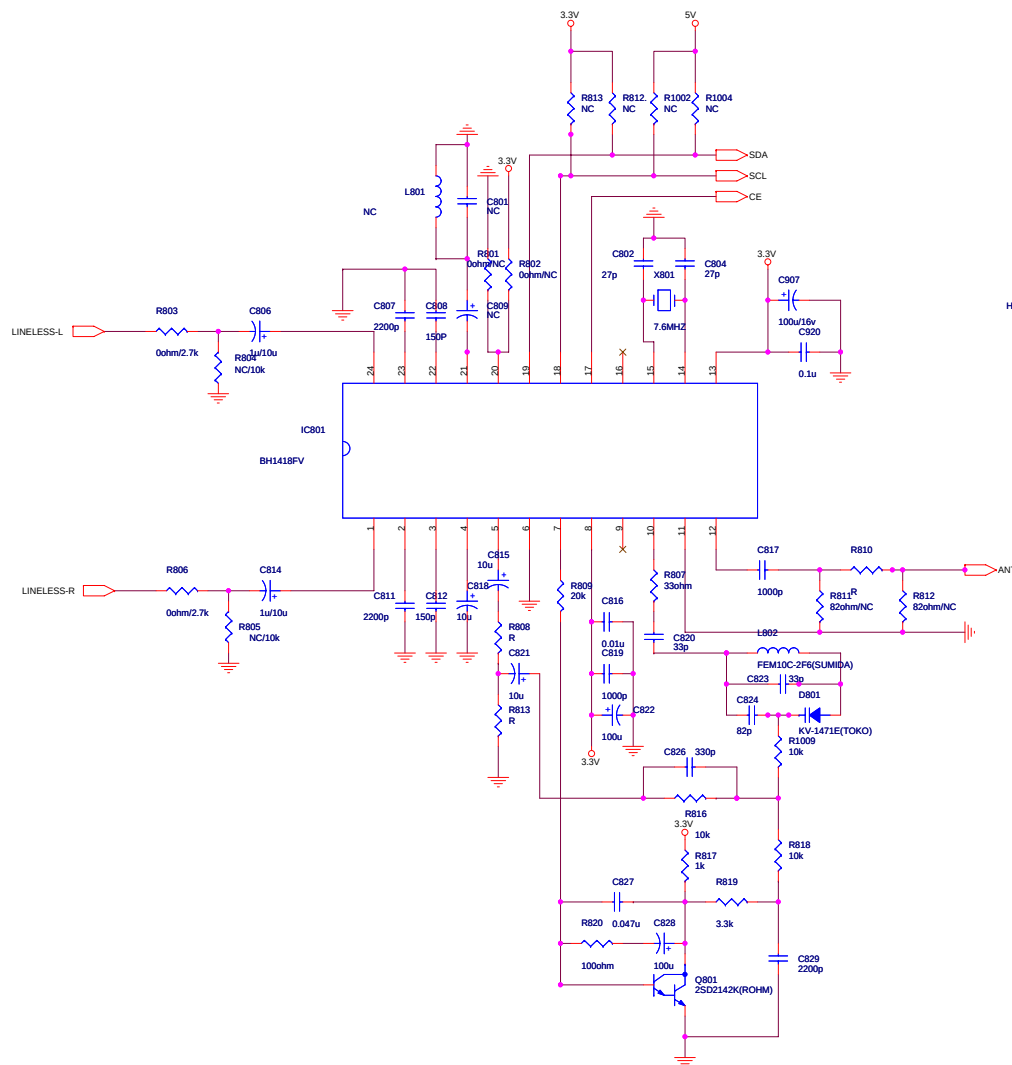


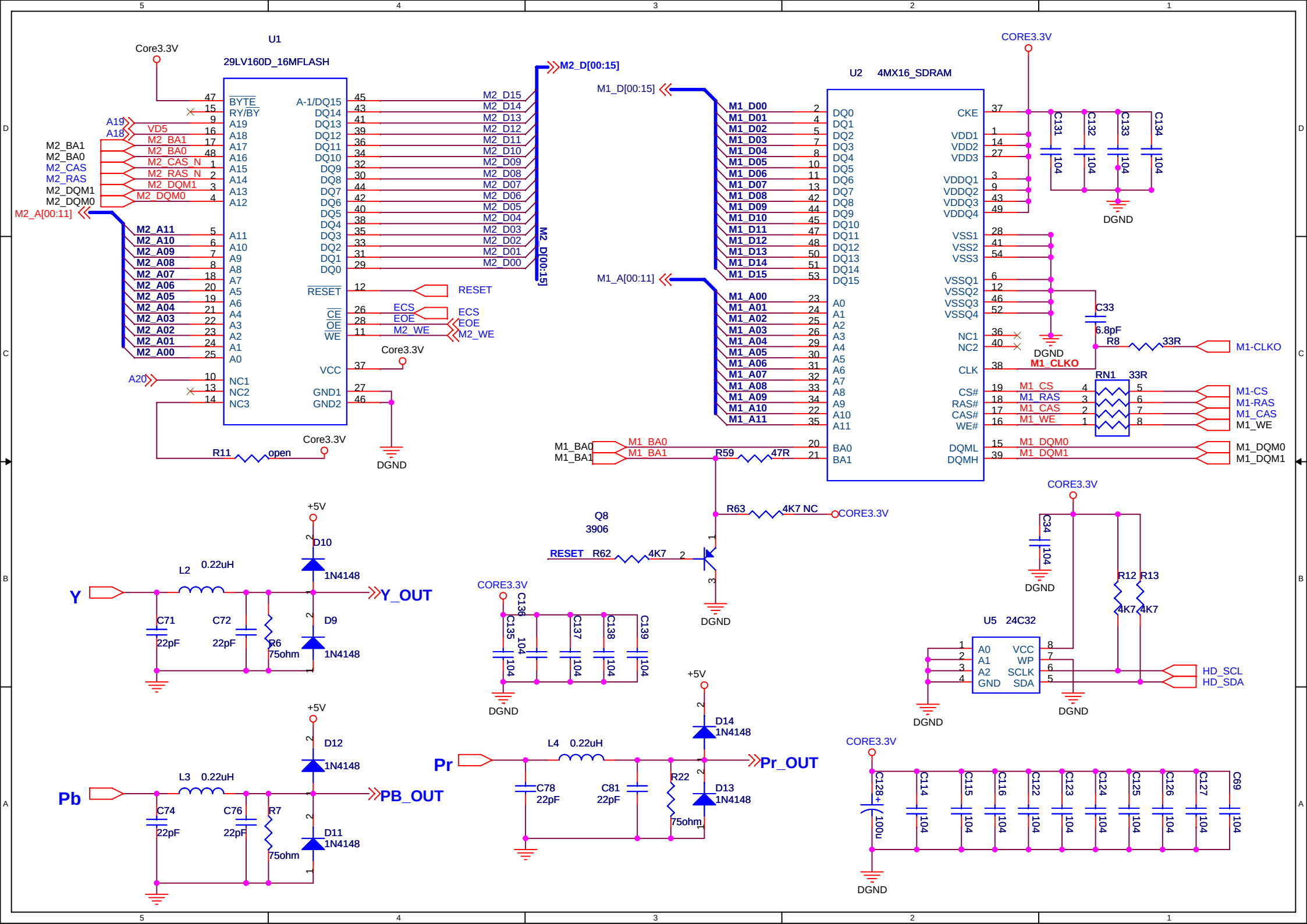


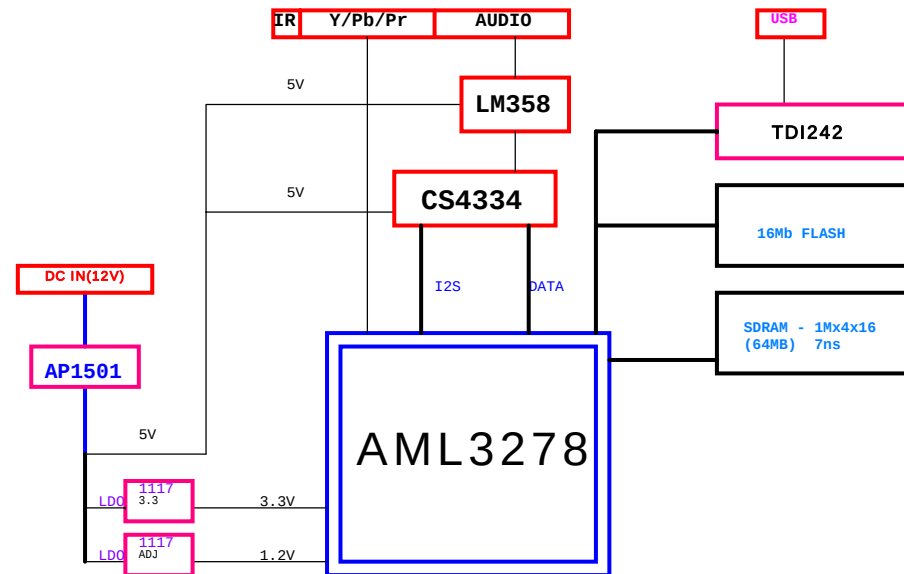
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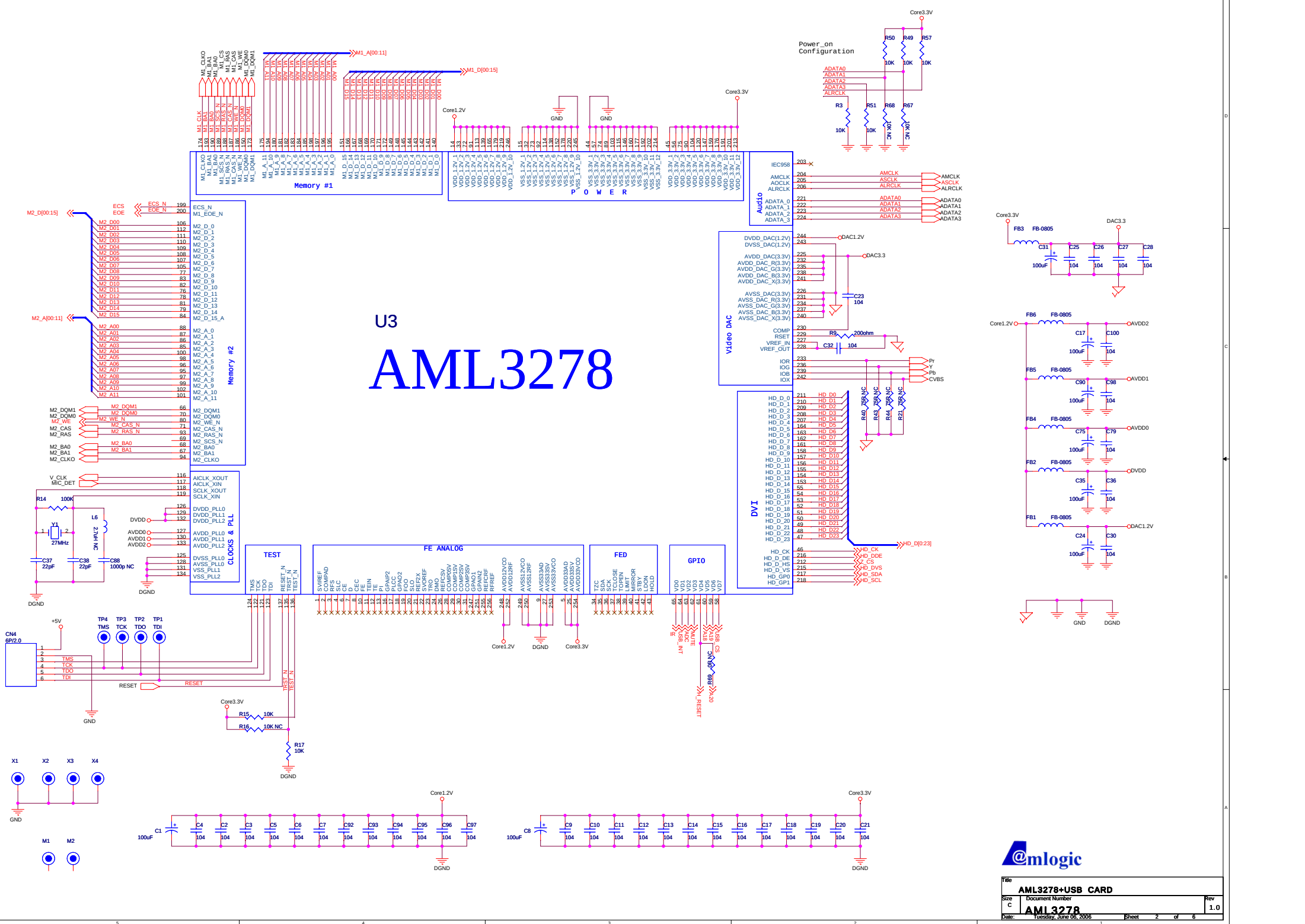
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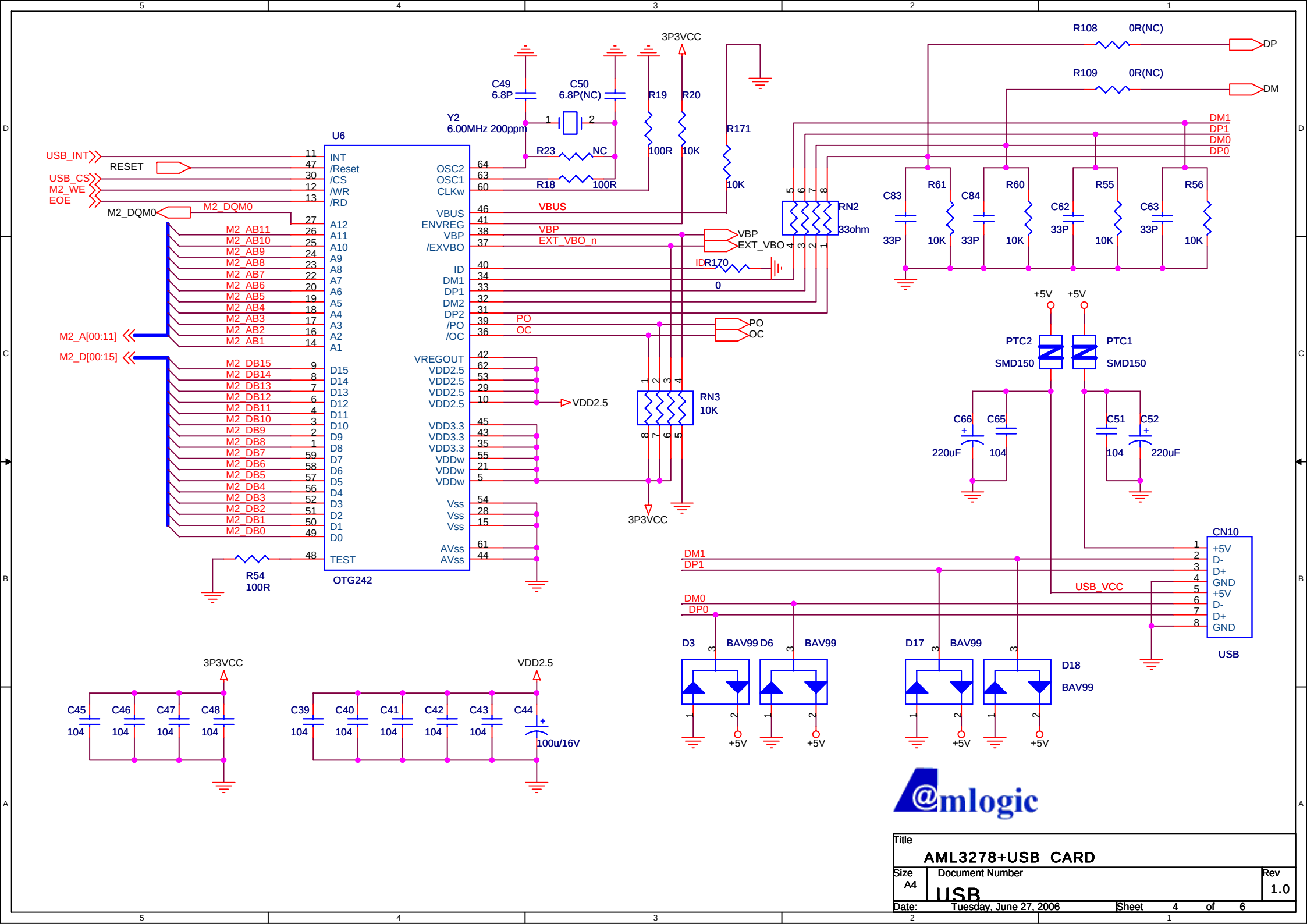
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Size	Document Number	Rev
A3	3278+242	01
Date:	Tuesday, June 27, 2006	Sheet 1 of 8

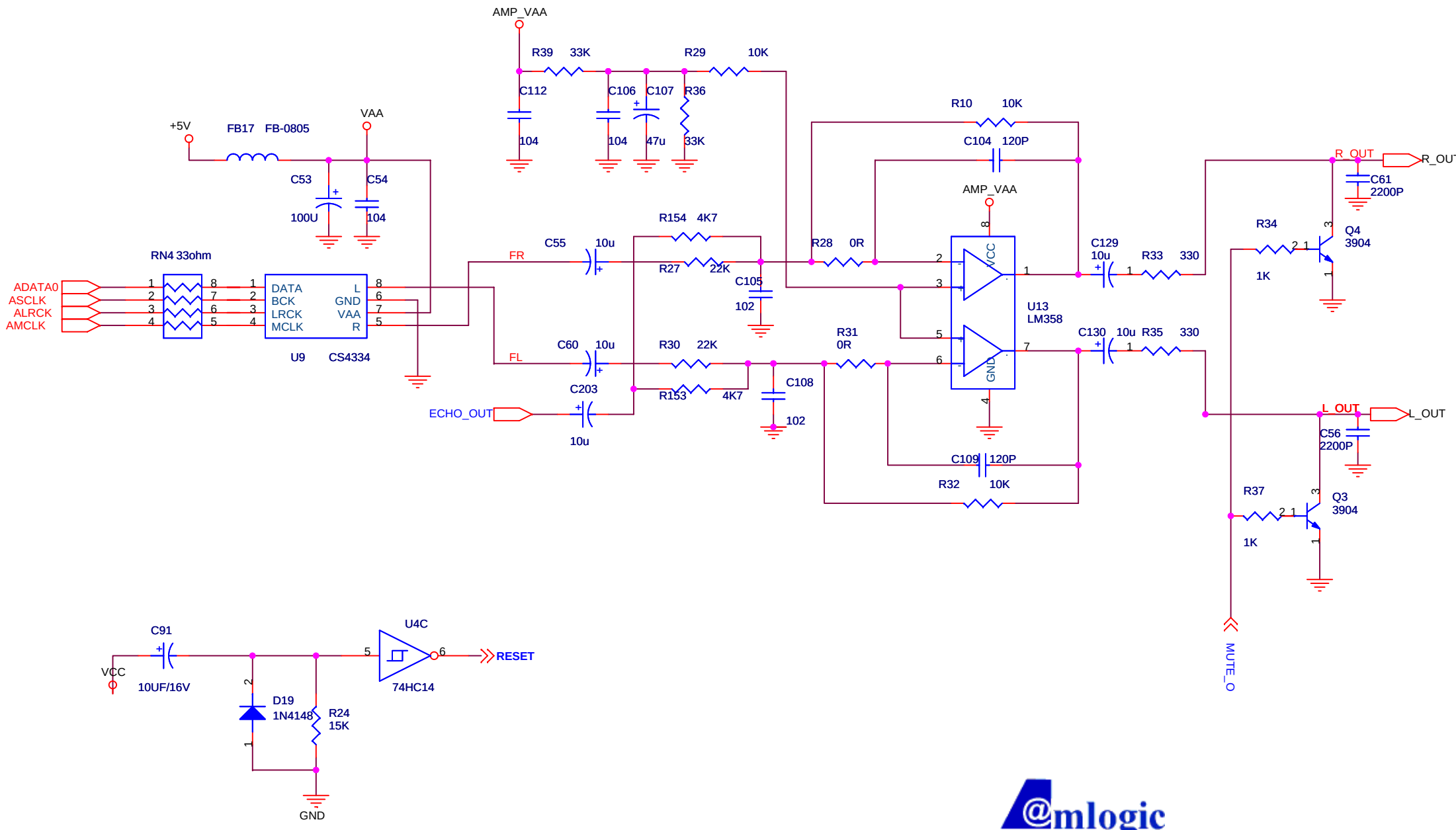


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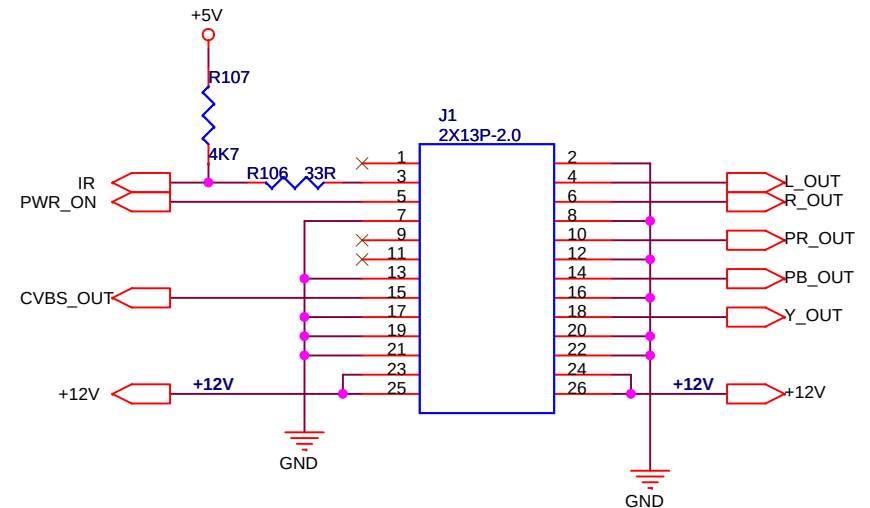
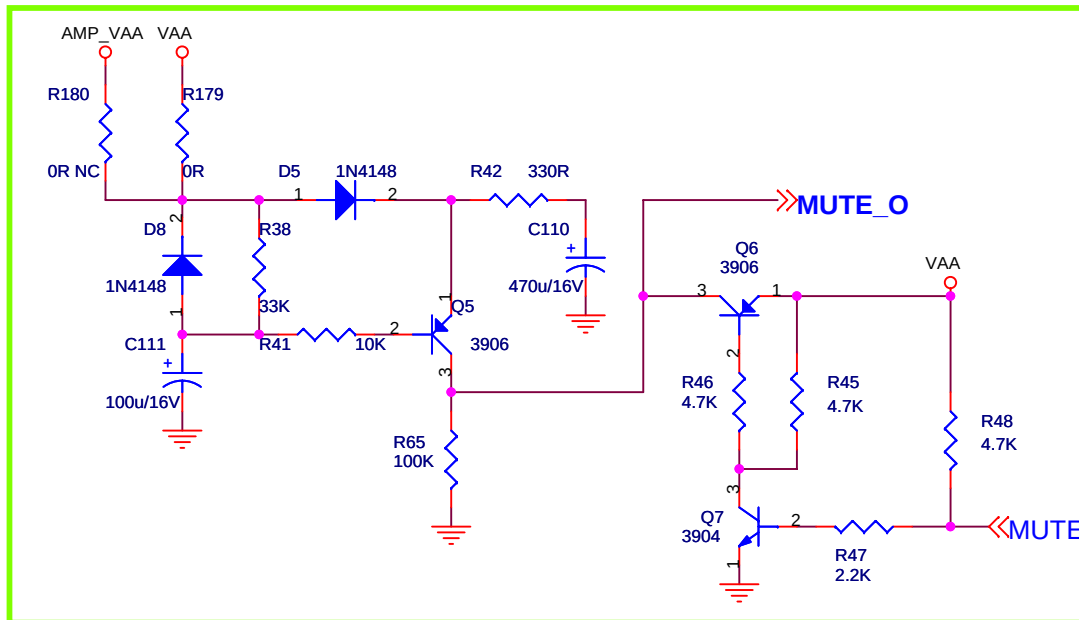
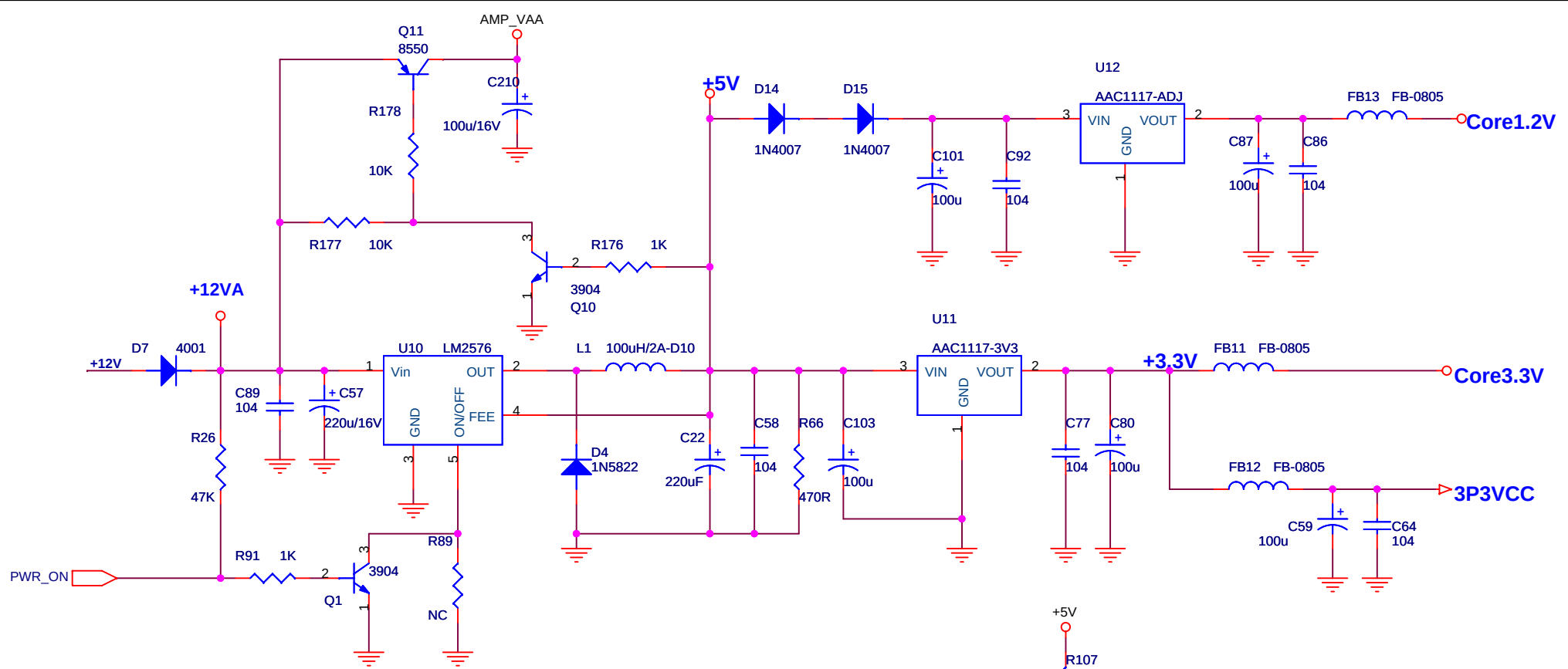
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A4	AUDIO&VIDEO	1.0
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