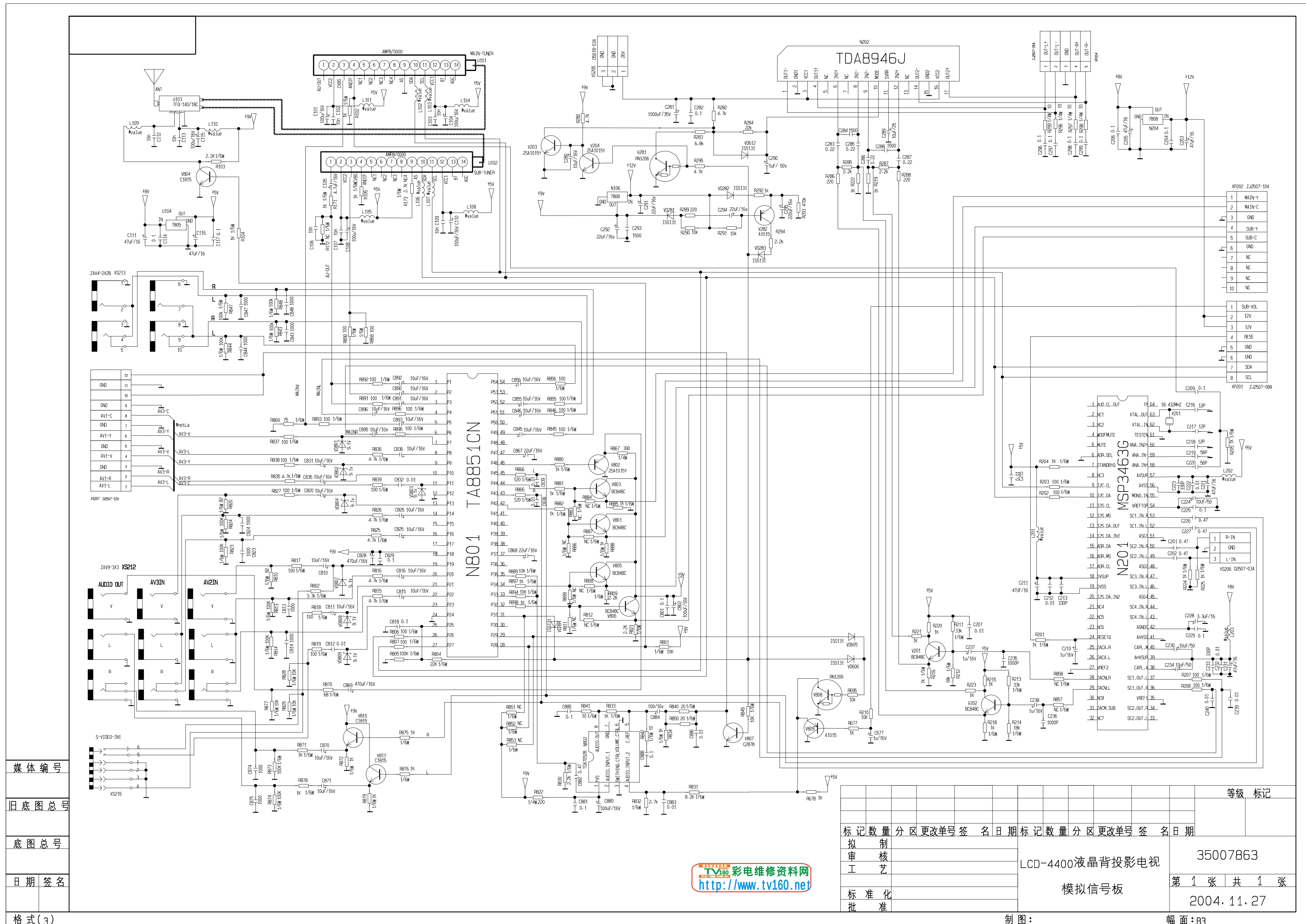
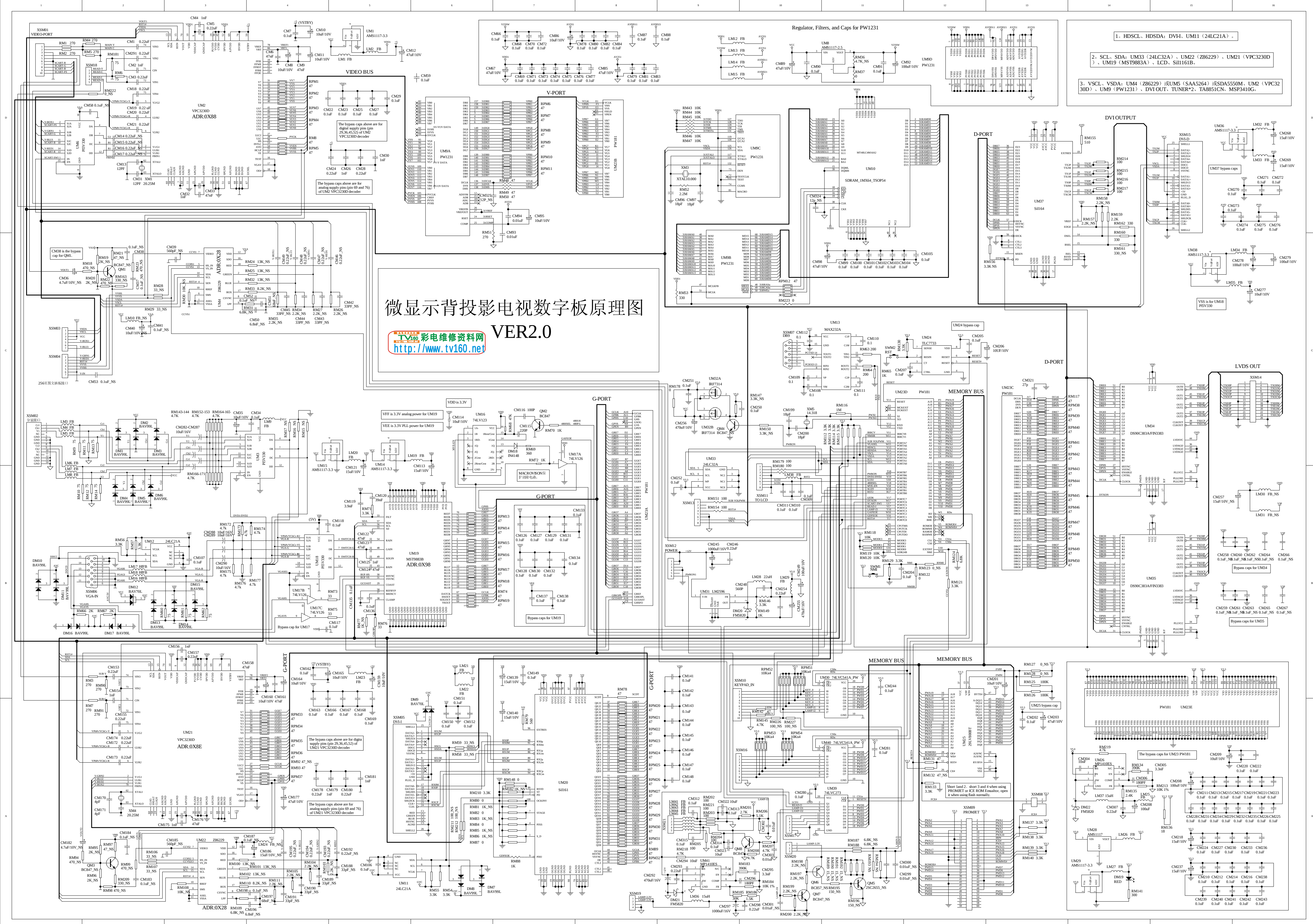






				MAY 22, 2003																									
				REVISION: B2																									
				HISTORY																									
				FIRST RELEASE																									
				10-16-02	1. Added ECN																								
				11-1-02	2. Fixed VGA lines problem REV B1																								
				2-26-03	3. Remapped memory module pin assign, added expansion conn, and S-Video (Component) conn. REV B2																								
				3-31-03	4. Added ECN. Isolated HCS3# and HIORDY. Added VFD connector, ADC for MIC, video in/out connector.																								
HORNET																													
ES6420																													
<table><tr><td>LCSx#</td><td>FUNCTION</td></tr><tr><td>0</td><td>SRAM</td></tr><tr><td>1</td><td>I/O</td></tr><tr><td>2</td><td>FLASH</td></tr><tr><td>3</td><td>ROM/FLASH</td></tr></table>						LCSx#	FUNCTION	0	SRAM	1	I/O	2	FLASH	3	ROM/FLASH														
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THE INFORMATION HAS BEEN CHECKED AND IS BELIEVED TO BE RELIABLE. HOWEVER, NO RESPONSIBILITY IS ASSUMED FOR INNACCURACIES. CIRCUIT DIAGRAMS ARE PROVIDED AS A MEANS OF ILLUSTRATING TYPICAL APPLICATIONS; CONSEQUENTLY COMPLETE INFORMATION FOR CONSTRUCTION PURPOSES IS NOT NECESSARILY GIVEN. ESS RESERVES THE RIGHT TO MAKE CHANGES AT ANY TIME IN ORDER TO IMPROVE THE DESIGN.																													
KONKA-读卡器																													
Title TITLE PAGE																													
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ES6420

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KONKA-读卡器

Title
ES6420

Size
Document Number
HORNET

Rev
B2

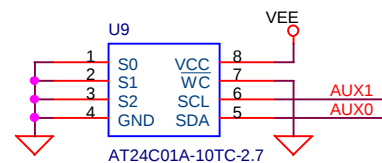
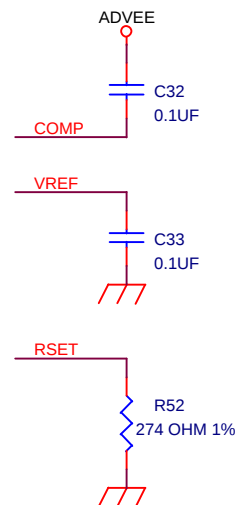
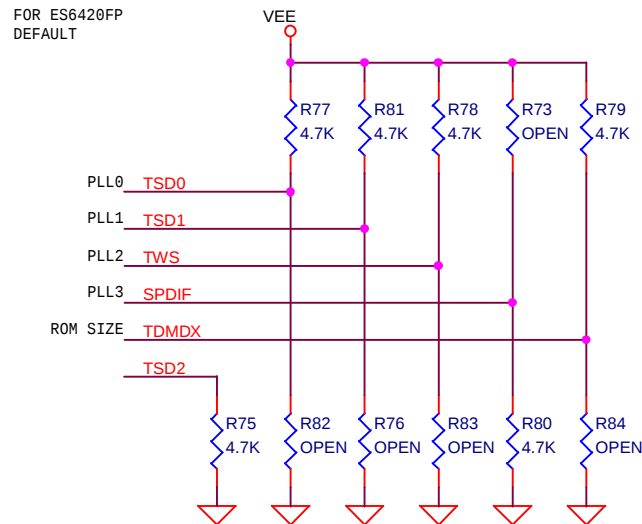
Date: Monday, November 29, 2004

Sheet 2 of 7

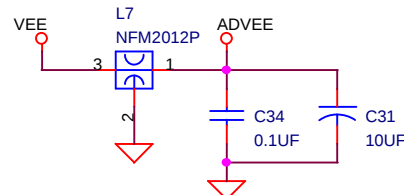
PLL2	PLL1	PLL0	MULT	FREQ.
0	0	0	DEFAULT	DEFAULT
1	1	1	4.25X	114.75
			4.0X	108

FOR ES6420FP
DEFAULT

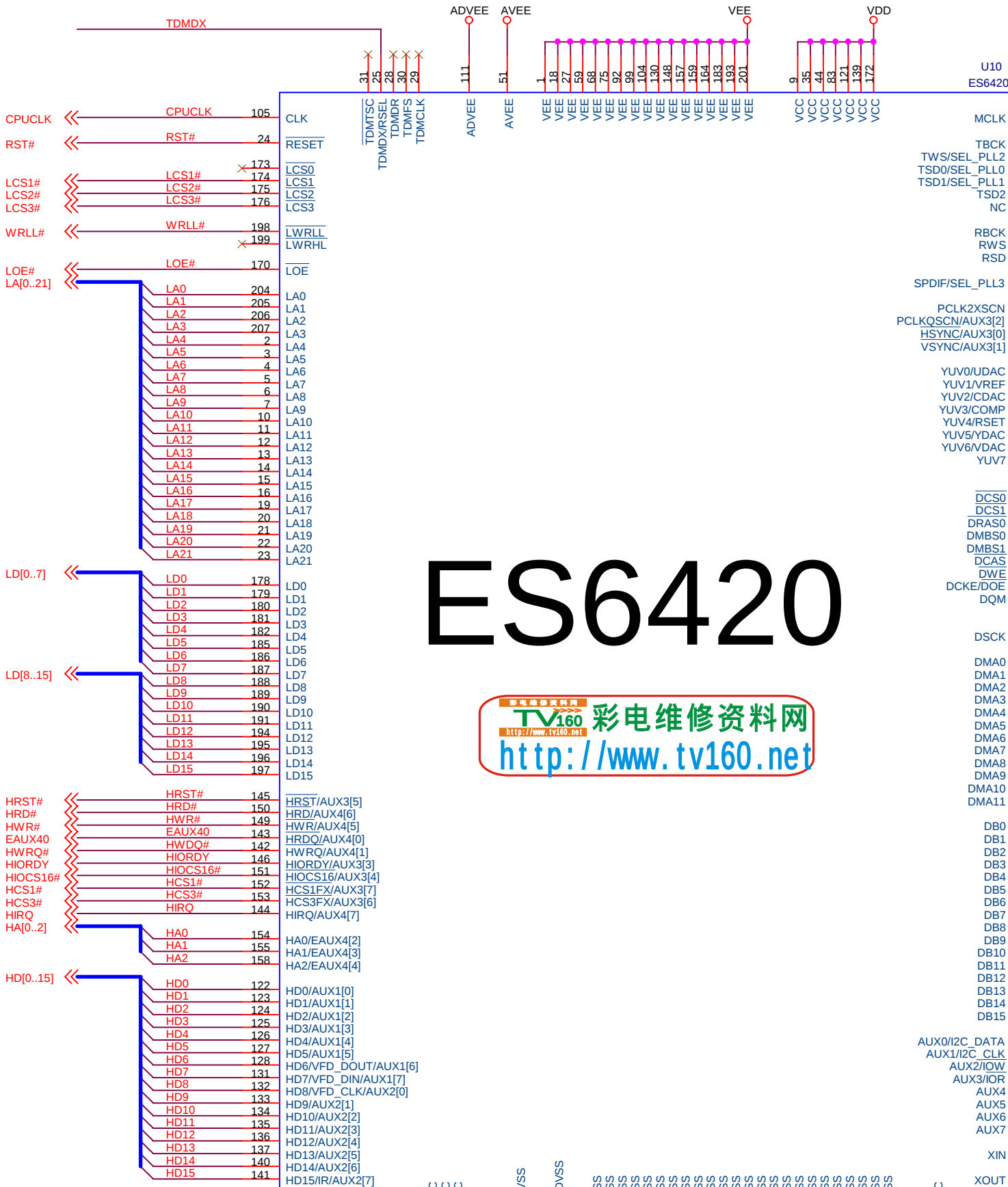
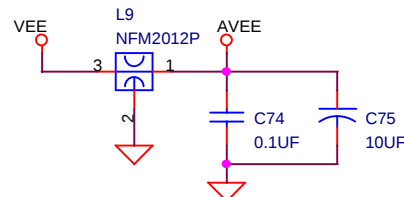
PLL3	CLK SOURCE
0	27 MHZ
1	33.3 MHZ
RSEL	ROM SIZE
0	16-BIT
1	8-BIT

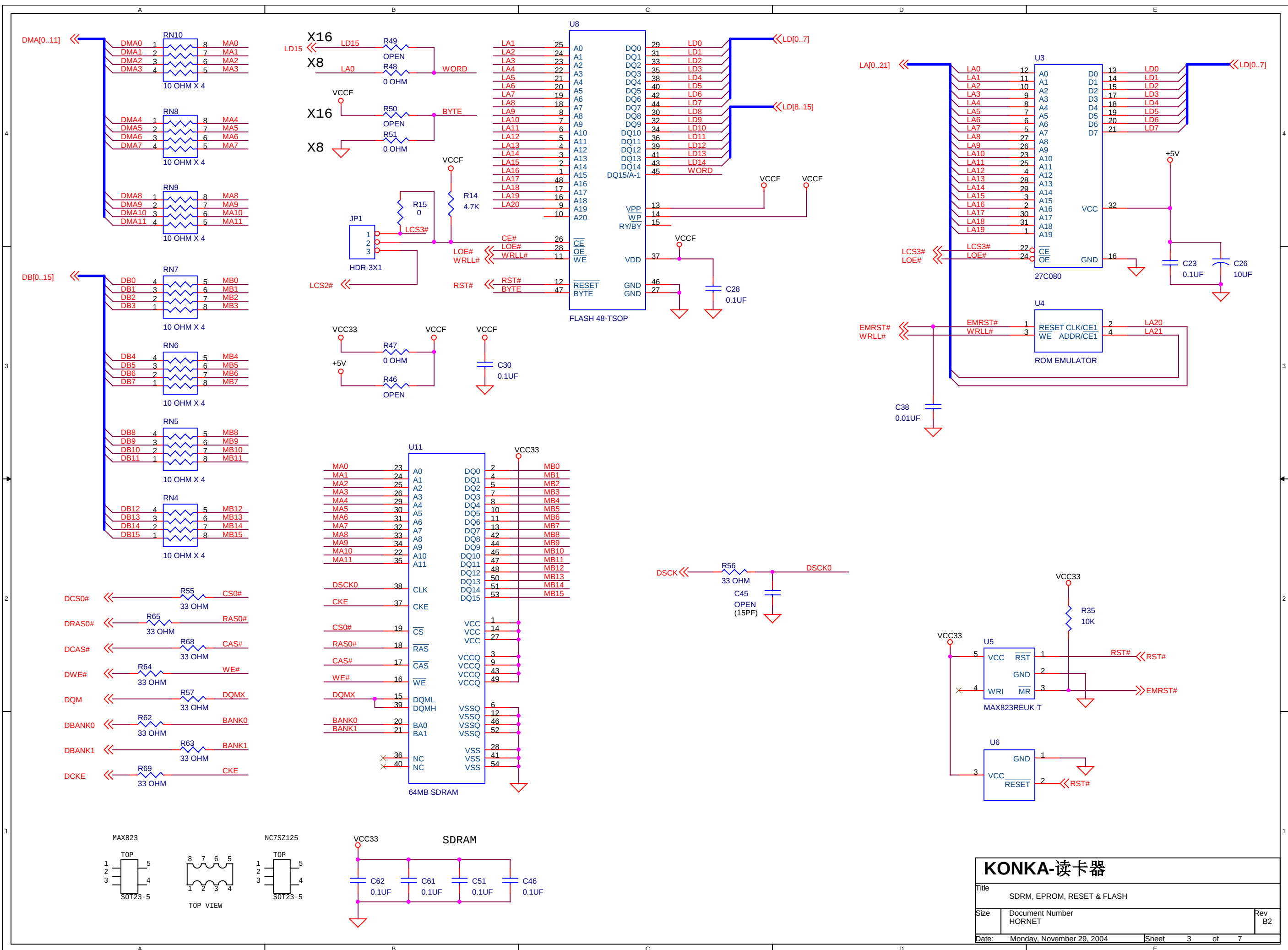


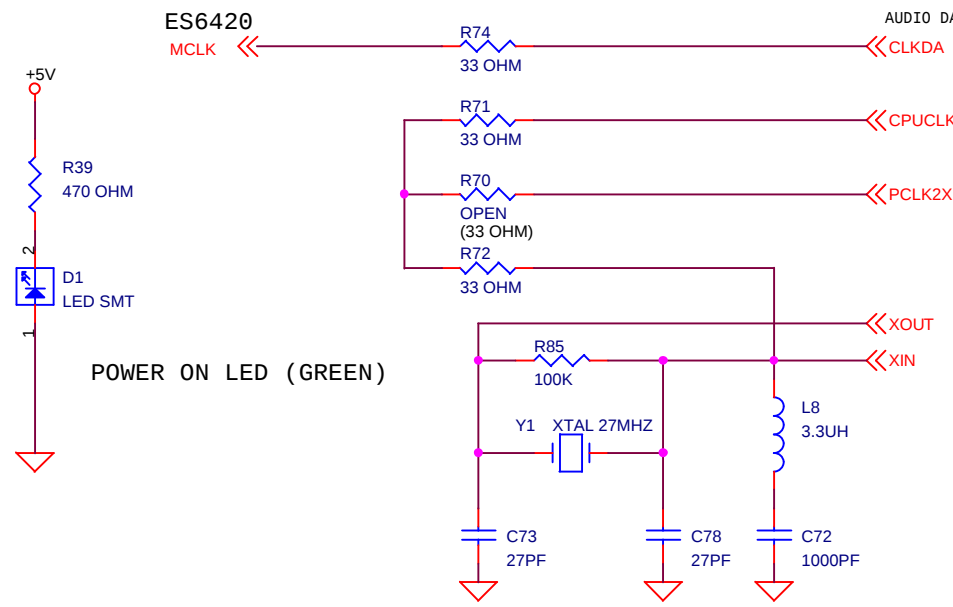
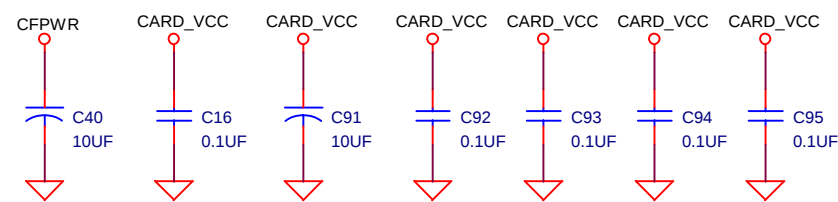
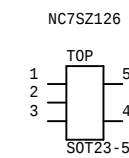
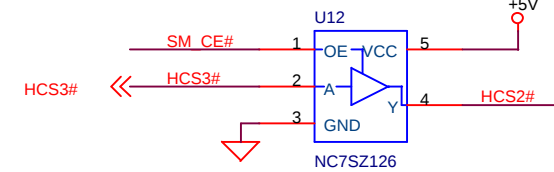
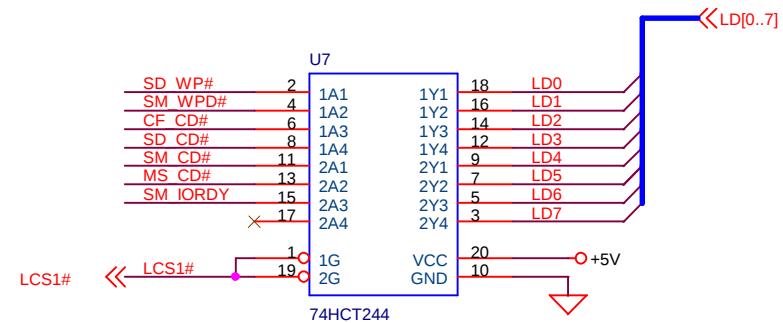
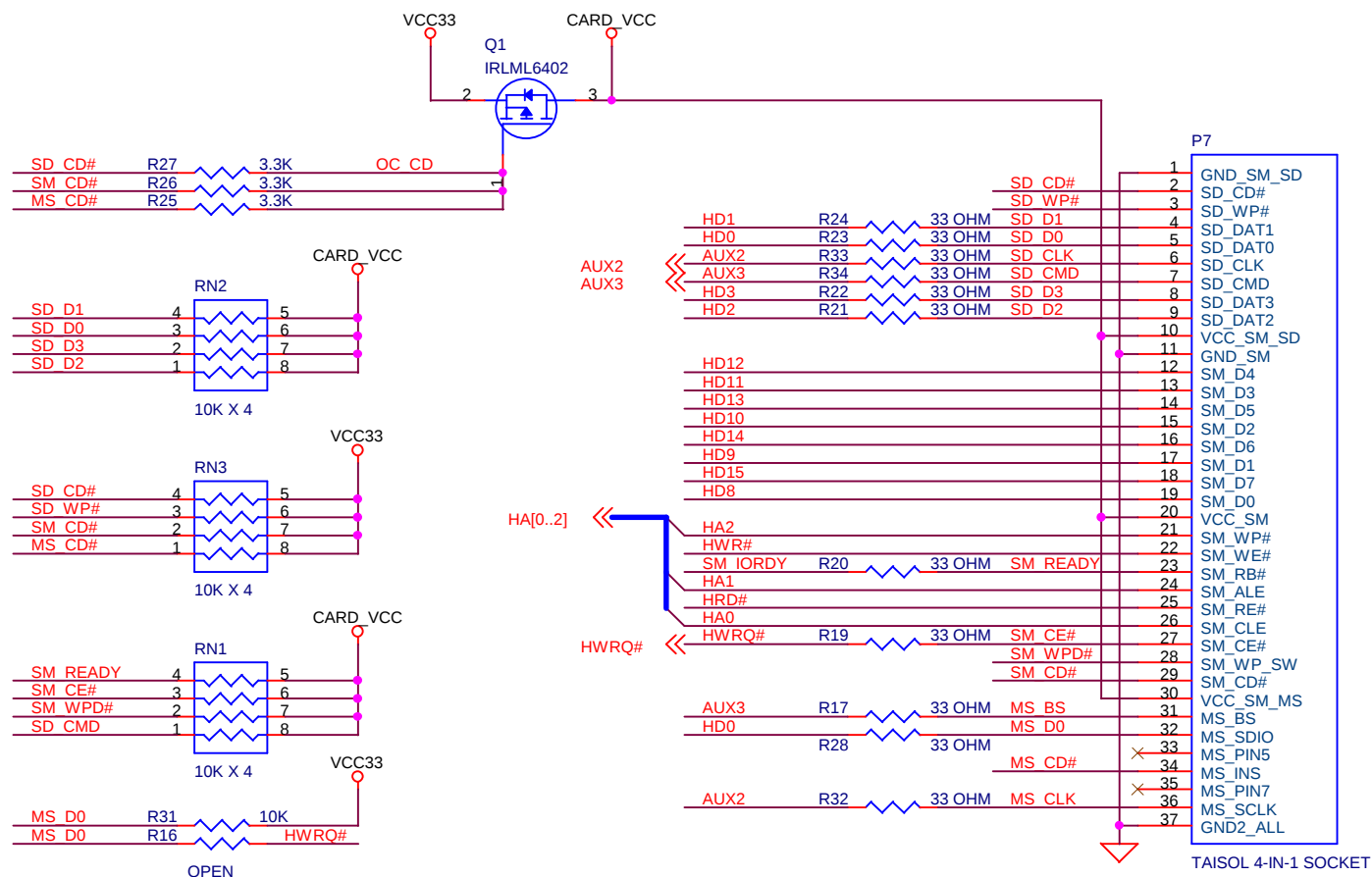
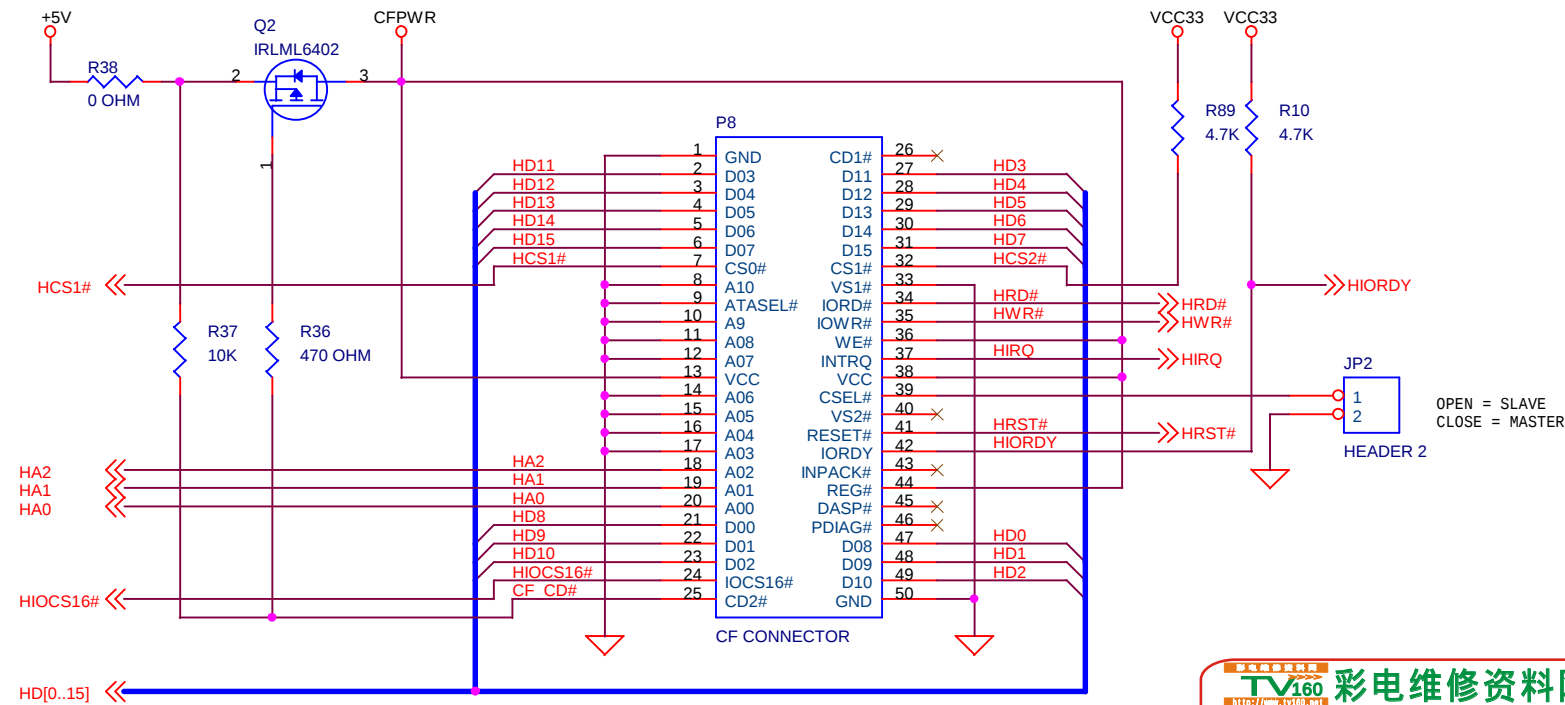
VIDEO DAC POWER



PLL POWER







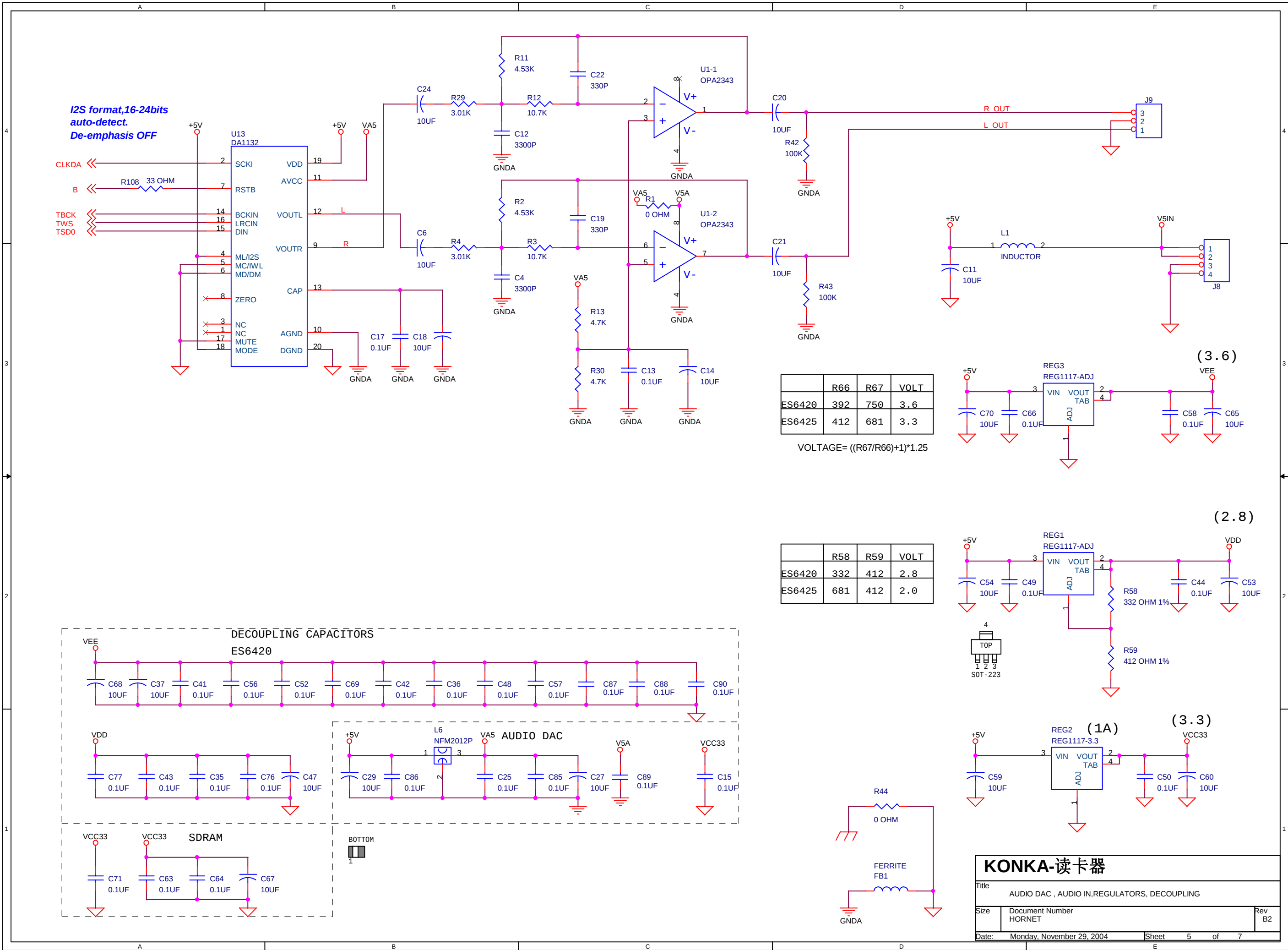
KONKA-读卡器

Title
CLOCKS, MEMCARD-MS,CF,SM, SD

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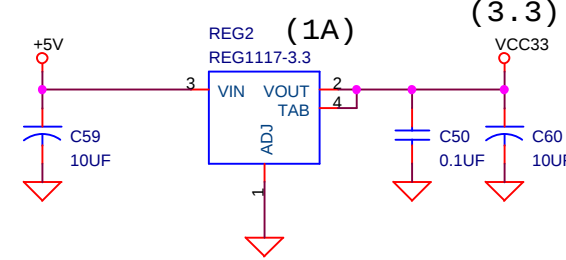
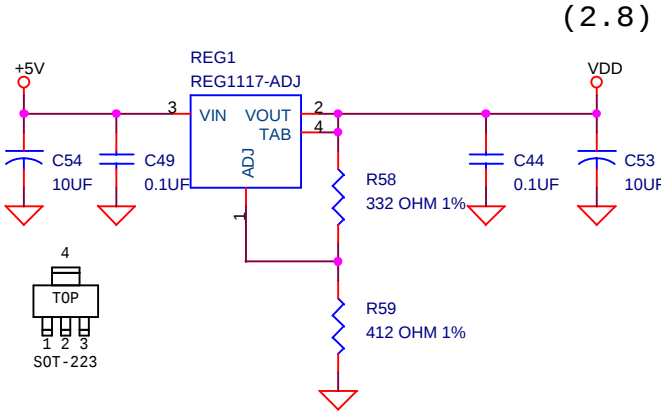
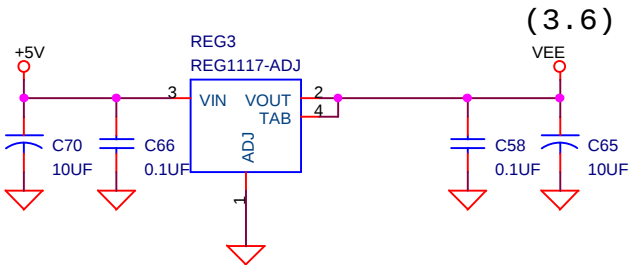
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	R66	R67	VOLT
ES6420	392	750	3.6
ES6425	412	681	3.3

VOLTAGE= ((R67/R66)+1)*1.25

	R58	R59	VOLT
ES6420	332	412	2.8
ES6425	681	412	2.0



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Title AUDIO DAC , AUDIO IN,REGULATORS, DECOUPLING

Size Document Number

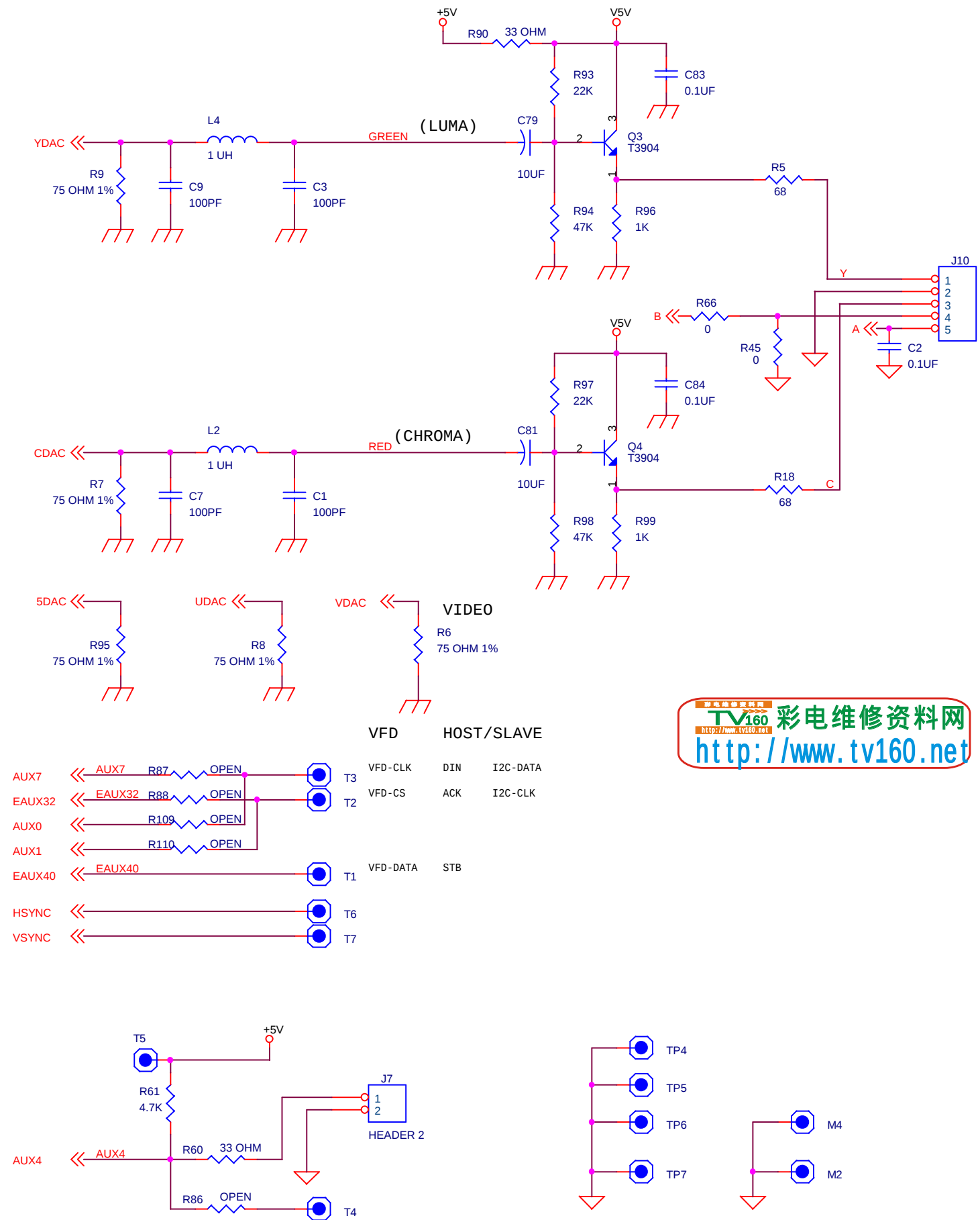
HORNET

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Title
CONNECTOR MEMORY MAP TABLE

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MODULE PIN ASSIGNMENT LIST								
ES6420 Pin #	GPIO	DESCRIPTION	ATAPI/CF	Smart Media	Memory Stick	SD/MMC	USB	VFD
160	AUX0	12C-DATA (I/O)						
161	AUX1	12C-CLOCK (O)						
162	AUX2				MS_SCLK (O)	SD_CLK (O)	USB_WR# (O)	
165	AUX3	DAC-ML (O)			MS_BS (O)	SD_CMD (O)	USB_RD# (O)	
166	AUX4							IR (I)
167	AUX5	DAC-MC (O)						
168	AUX6	DAC-MD (O)						
169	AUX7						USB_CS# (O)	CLK(O)
122	EAUX10	HD0 (I/O)	D8 (I/O)		MS_D0 (I/O)	SD_D0 (I/O)		
123	EAUX11	HD1 (I/O)	D9 (I/O)			SD_D1 (I/O)		
124	EAUX12	HD2 (I/O)	D10 (I/O)			SD_D2 (I/O)		
125	EAUX13	HD3 (I/O)	D11 (I/O)			SD_D3 (I/O)		
126	EAUX14	HD4 (I/O)	D12 (I/O)					
127	EAUX15	HD5 (I/O)	D13 (I/O)		MS_D1 (I/O)			
128	EAUX16	HD6 (I/O)	D14 (I/O)		MS_D2 (I/O)			
131	EAUX17	HD7 (I/O)	D15 (I/O)		MS_D3 (I/O)			
132	EAUX20	HD8 (I/O)	D0 (I/O)	SM_D0 (I/O)				
133	EAUX21	HD9 (I/O)	D1 (I/O)	SM_D1 (I/O)				
134	EAUX22	HD10 (I/O)	D2 (I/O)	SM_D2 (I/O)				
135	EAUX23	HD11 (I/O)	D3 (I/O)	SM_D3 (I/O)				
136	EAUX24	HD12 (I/O)	D4 (I/O)	SM_D4 (I/O)				
137	EAUX25	HD13 (I/O)	D5 (I/O)	SM_D5 (I/O)				
140	EAUX26	HD14 (I/O)	D6 (I/O)	SM_D6 (I/O)				
141	EAUX27	HD15 (I/O)	D7 (I/O)	SM_D7 (I/O)				
119	EAUX30	HSYNC (I/O)						
118	EAUX31	VSYNC (I/O)						
117	EAUX32	PCLKQ						CS(O)
146	EAUX33	HIORDY (I)	IORDY (I)					
151	EAUX34	HI0CS16# (I)	I0CS16# (I)					
145	EAUX35	HRST# (O)	RESET (O)					
153	EAUX36	HCS3# (O)	CS3 (O)					
152	EAUX37	HCS1# (O)	CS1 (O)					
143	EAUX40	HRDQ# (I)						DATA(O)
142	EAUX41	HWRQ# (I)		SM_CE# (O)	MS_D0 (I/O) OPT		NRESET# (O)	
154	EAUX42	HA0 (O)	A0 (O)	SM_CLE (O)			USB_A0 (O)	
155	EAUX43	HA1 (O)	A1 (O)	SM_ALE (O)				
158	EAUX44	HA2 (O)	A2 (O)	SM_WP# (O)				
149	EAUX45	HWR# (O)	IOWR (O)	SM_WR# (O)				
150	EAUX46	HRD# (O)	IORD (O)	SM_RD# (O)				
144	EAUX47	HIRQ (I)	INTRQ (I)					
	LCS1#	LD0				SD_WP (I)		
	LCS1#	LD1			MS_WPSW# (I)			
	LCS1#	LD2	CF_CD# (I)					
	LCS1#	LD3				SD_CD# (I)		
	LCS1#	LD4		SM_CD# (I)				
	LCS1#	LD5			MS_CD (I)			
	LCS1#	LD6		SM_READY (I)				
	LCS1#	LD7						

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Title

CONNECTOR MEMORY MAP TABLE

Size

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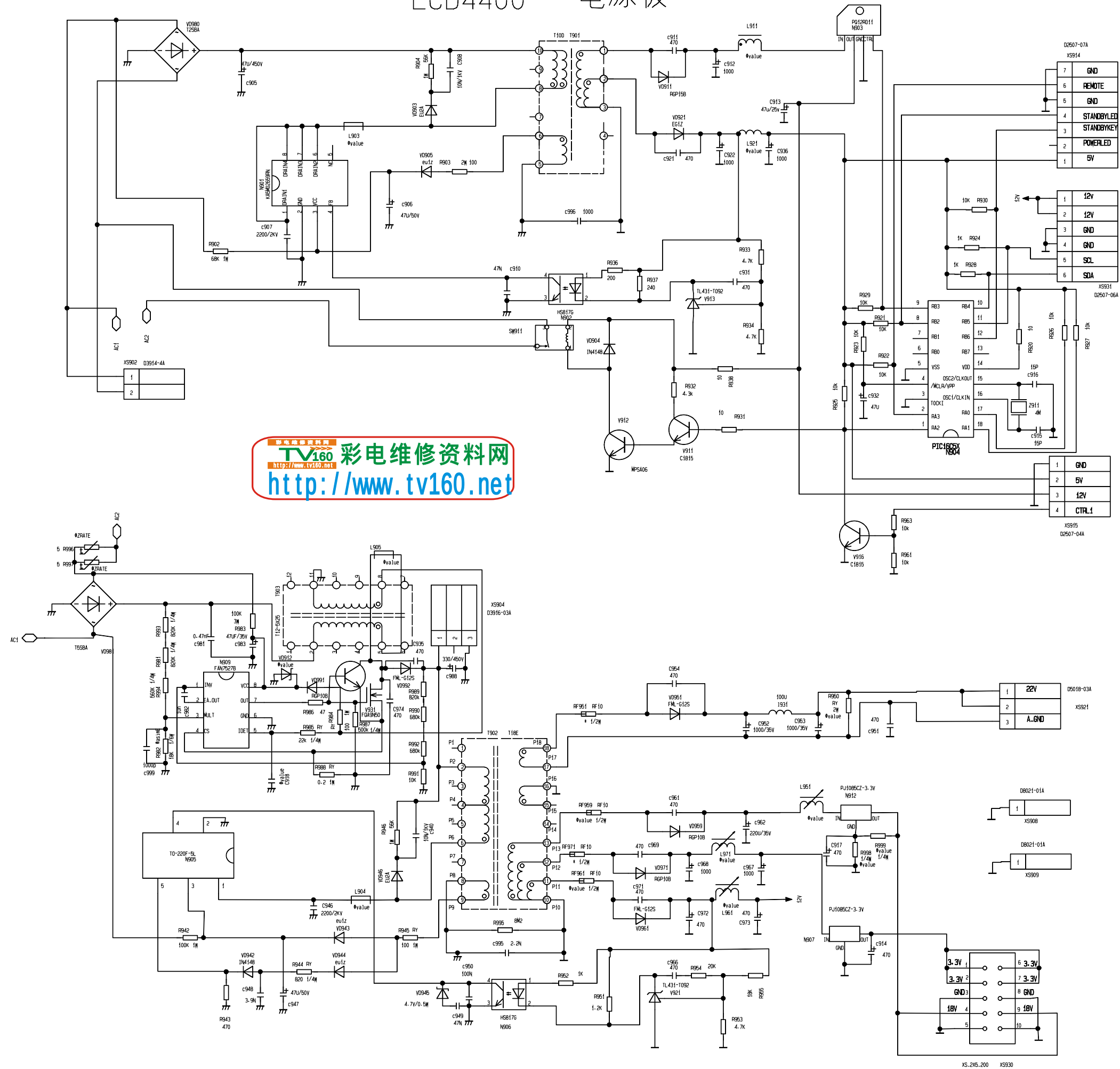
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Rev

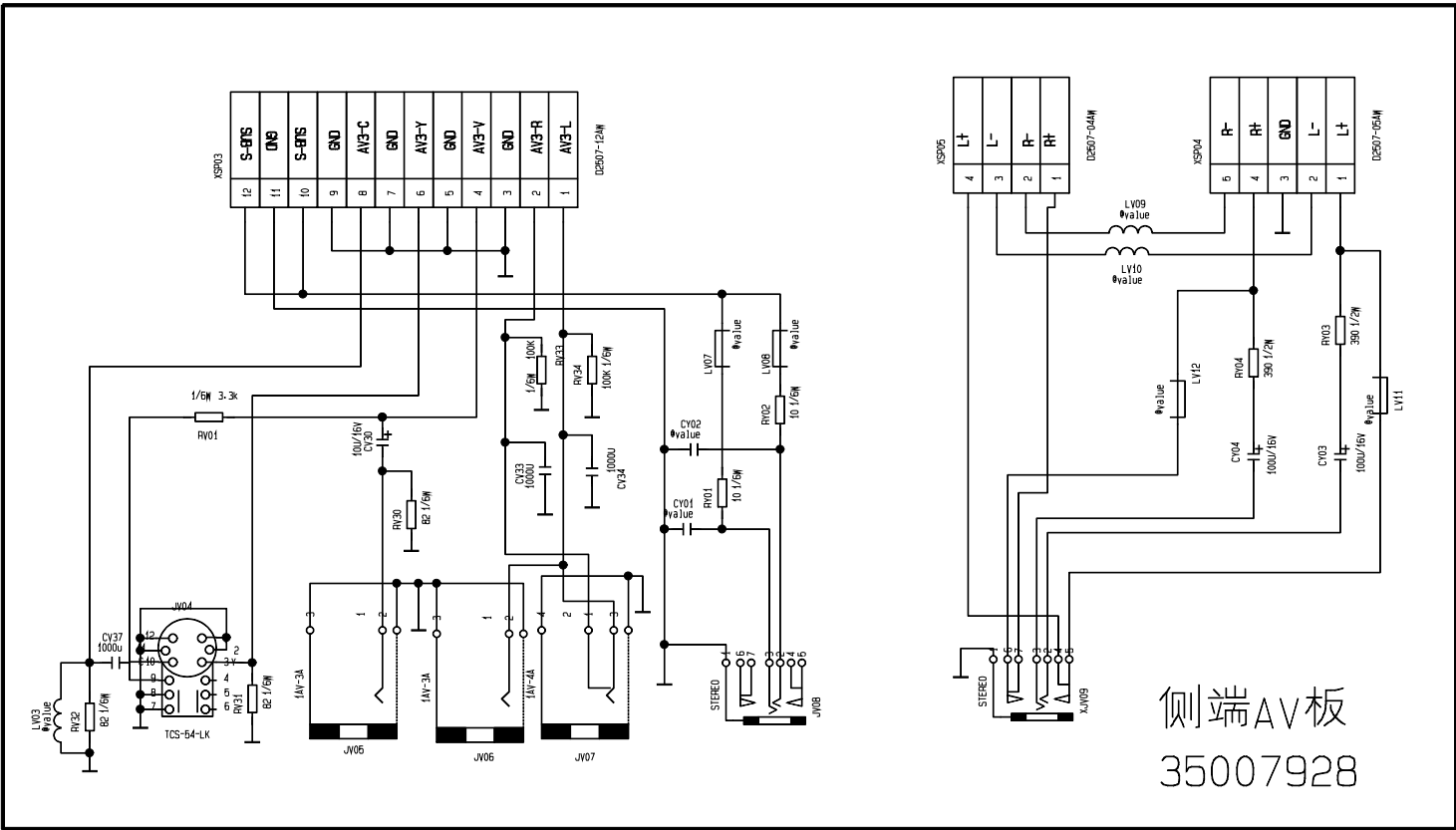
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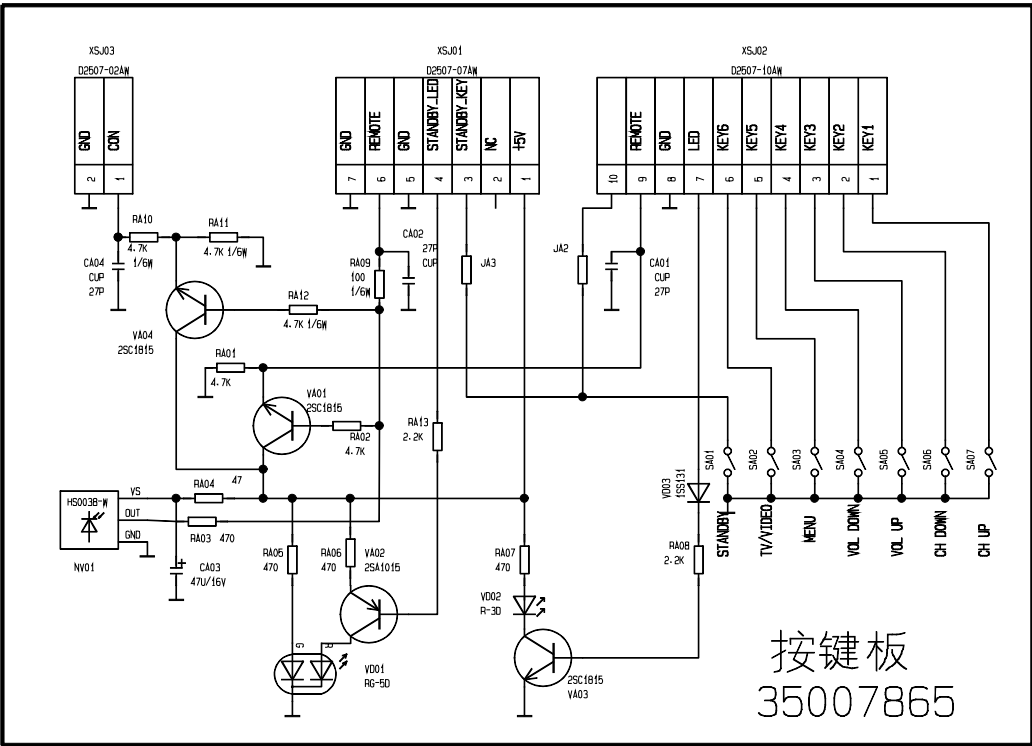


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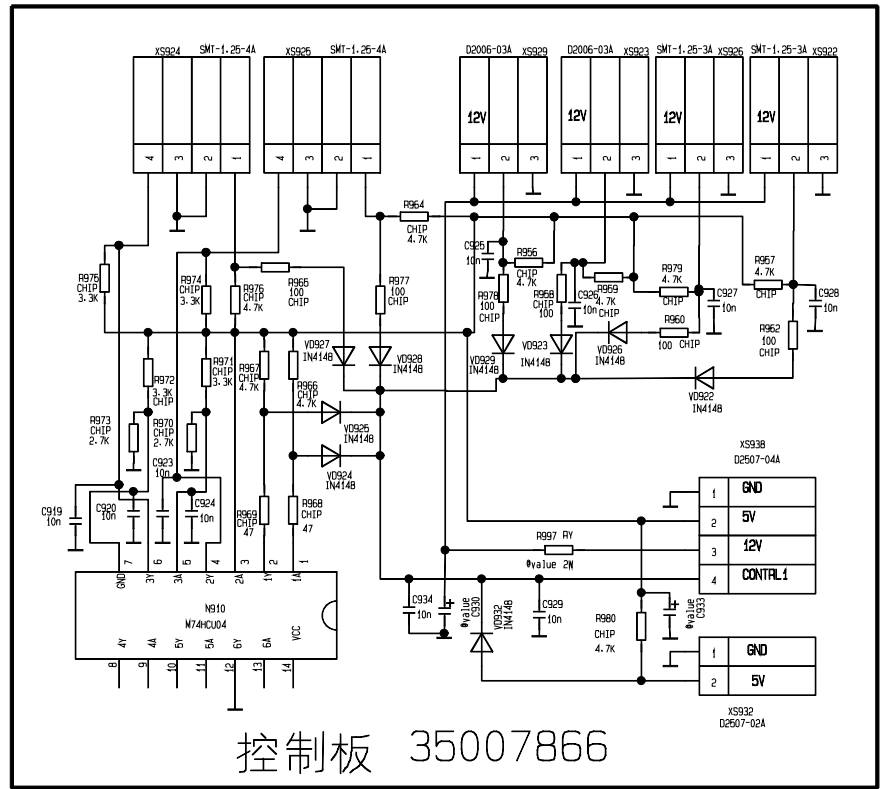
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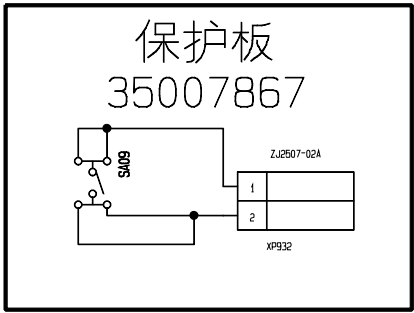
侧端AV板
35007928



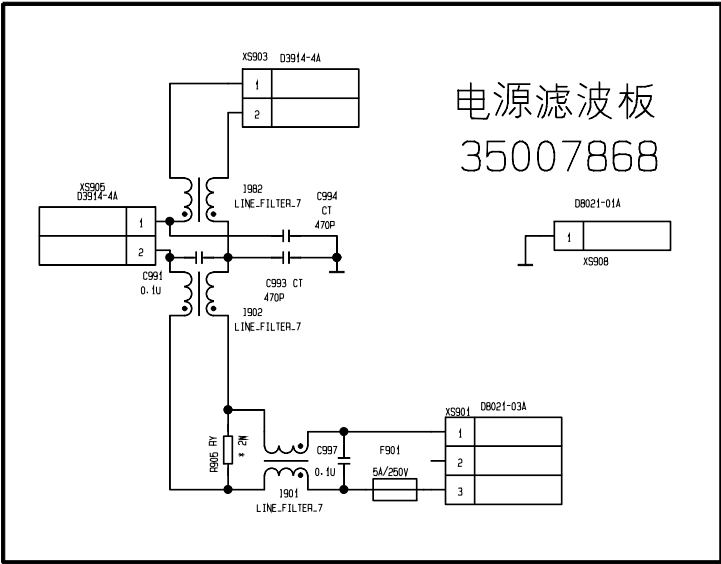
按键板
35007865



控制板 35007866



保护板
35007867



电源滤波板
35007868

等级				标记			
标	记	数量	分	区	更改	单号	签
拟	制						
审	核						
工	艺						
标	准						
批	准						

LCD4400
其它小板原理图

第 张 共 张

制图： 幅面：3

媒体编号

旧底图总号

底图总号

日期 签名

格式(3)