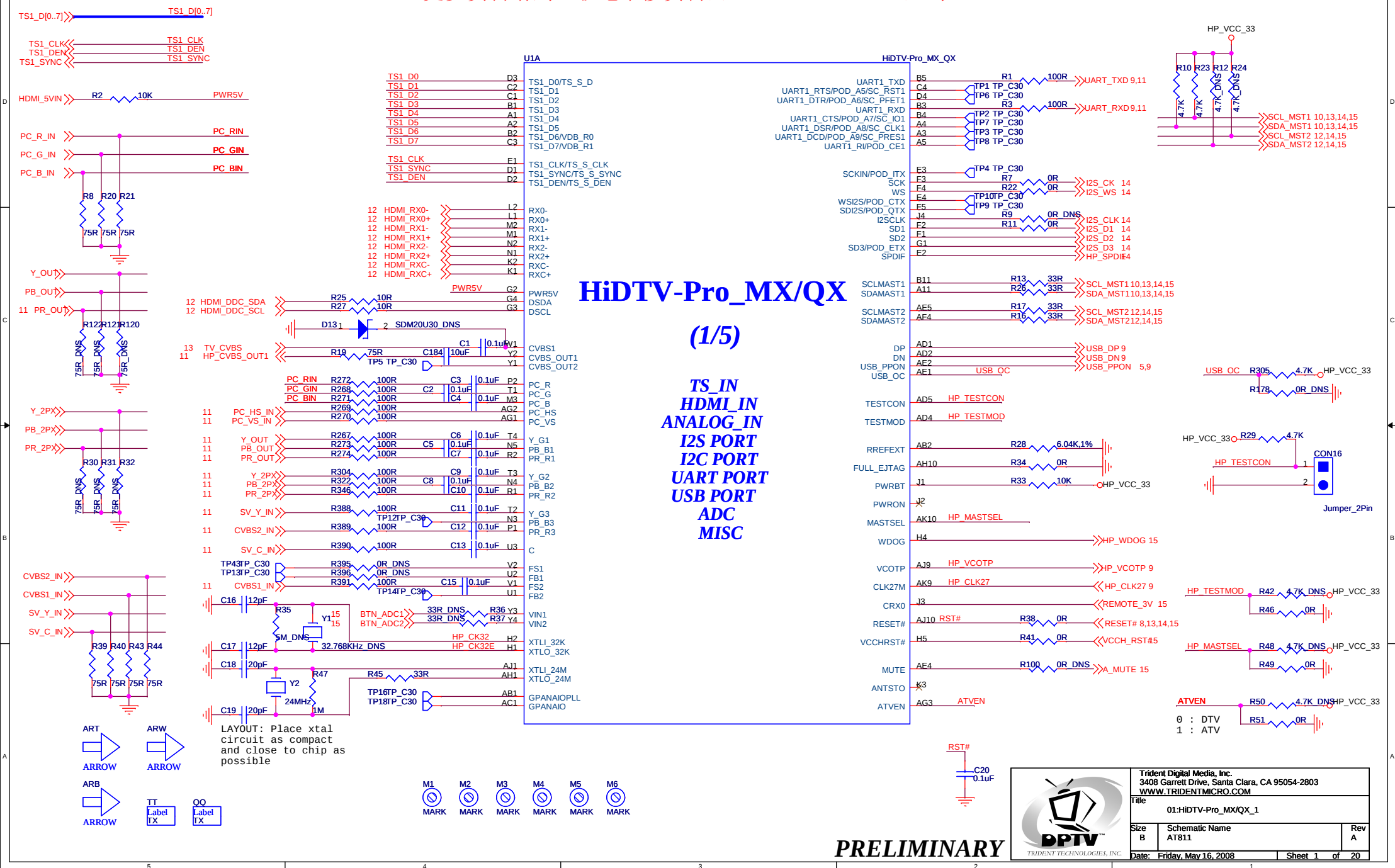
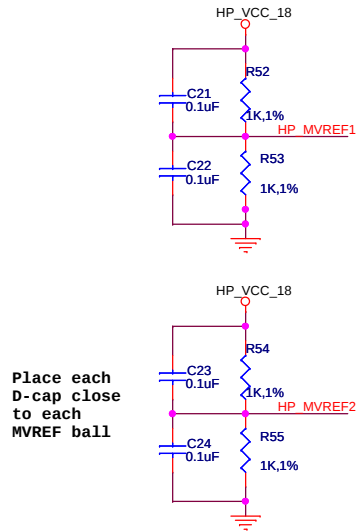






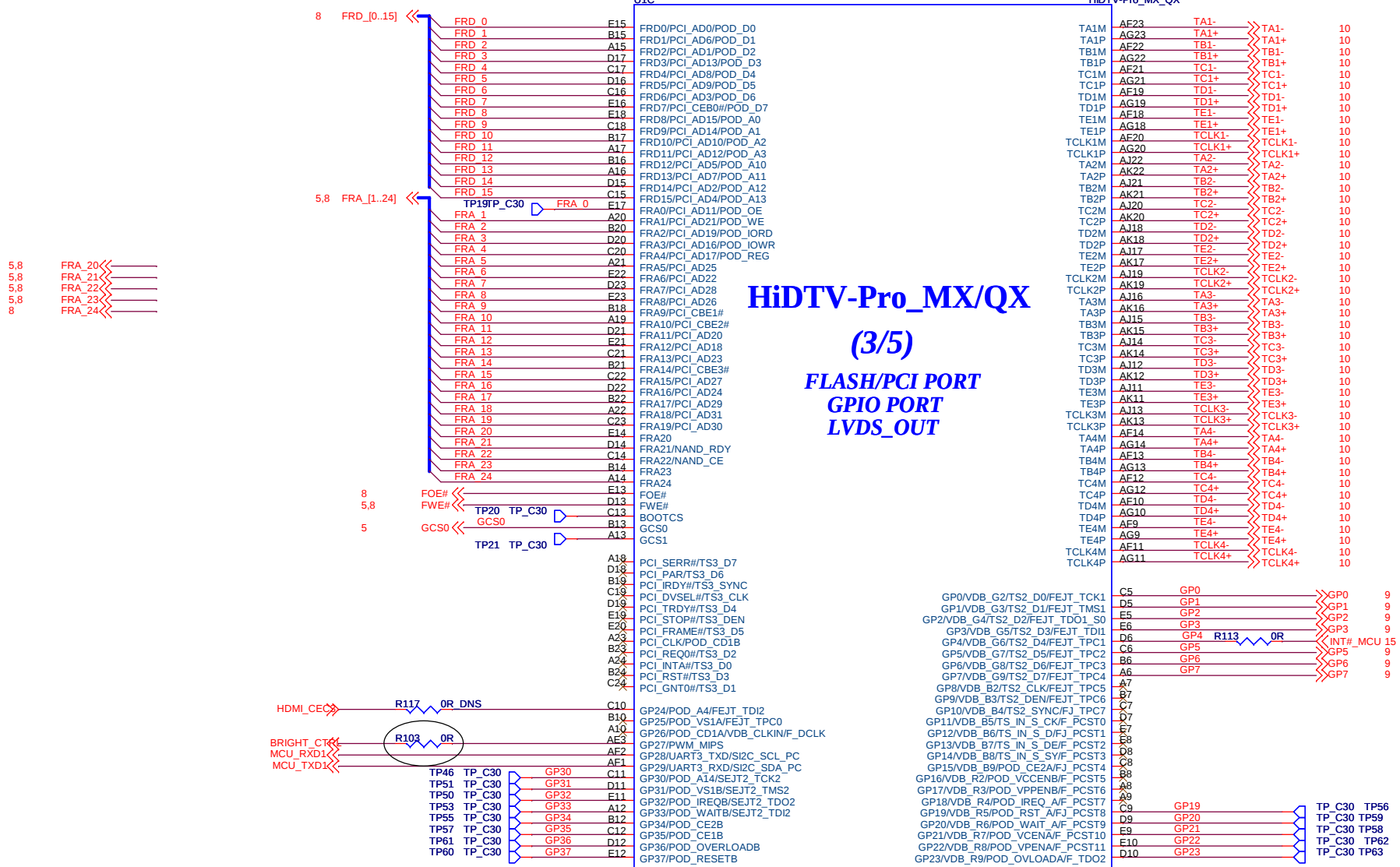


U1B		HiDTV-Pro_MX_QX	
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HP DQS#0	AG28	DQS0#	AJ27
HP DQS1	AA29	DQS1	AH26
HP DQS#1	AA30	DQS1#	MD2
HP DQS2	J29	DQS2	MD3
HP DQS#2	J30	DQS2#	MD4
HP DQS3	D28	DQS3	AF26
HP DQS#3	C28	DQS3#	AE27
HP DQS4	AG29	DQS4	AE25
HP DQS#4	AG30	DQS4#	MD7
HP DQS5	AB28	DQS5	AD28
HP DQS#5	AB29	DQS5#	MD8
HP DQS6	K28	DQS6	AD29
HP DQS#6	J28	DQS6#	AC29
HP DQS7	C29	DQS7	MD9
HP DQS#7	C30	DQS7#	MD10
HP DQM0	AH29	DQM0	MD11
HP DQM1	AB30	DQM1	MD12
HP DQM2	K30	DQM2	MD13
HP DQM3	D29	DQM3	MD14
HP DQM4	AH30	DQM4	MD15
HP DQM5	AB29	DQM5	MD16
HP DQM6	K29	DQM6	MD17
HP DQM7	D30	DQM7	MD18
HP MA0	R28	MAA0	L29
HP MA1	Y29	MAA1	L30
HP MA2	R29	MAA2	H29
HP MA3	Y27	MAA3	H30
HP MA4	P28	MAA4	G28
HP MA5	U26	MAA5	G29
HP MA6	R30	MAA6	F25
HP MA7	Y30	MAA7	F26
HP MA8	N29	MAA8	E26
HP MA9	T29	MAA9	E27
HP MA10	V28	MAA10	D26
HP MA11	N28	MAA11	C27
HP MA12	N30	MAA12/CS1	C26
HP BA0	U27	BA0	B27
HP BA1	U28	BA1	AK28
HP MCLK0	AE30	CLK1	AK29
HP MCLK0#	AD30	CLK1#	AJ29
HP MCLK1	G30	CLK2	AJ30
HP MCLK1#	F30	CLK2#	AE29
HP MVREF1	AK26	MVREF1	AE30
HP MVREF2	A26	MVREF2	AE28
HP CAS#	T28	CAS	AE29
HP RAS#	P30	RAS	AD25
HP WE#	W28	WE	AD26
HP CS0#	U29	CS0	AC26
HP CKE	Y26	CKE	AC27
HP ODT	P29	ODT	Y26
			Y27
			W25
			W26
			M25
			M26
			L26
			L27
			H27
			H26
			G25
			G26
			F28
			F29
			E29
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			A29
			MD63

HiDTV-Pro_MX/QX (2/5) DDR2 INTERFACE

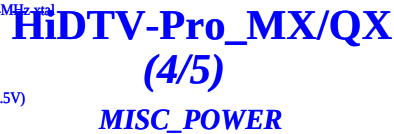
PRELIMINARY

			Trident Digital Media, Inc. 3408 Garrett Drive, Santa Clara, CA 95054-2803 WWW.TRIDENTMICRO.COM		
			Title 02:HiDTV-Pro_MX/QX_2		
Size B	Schematic Name AT811				Rev A
Date:	Tuesday, March 18, 2008	Sheet	2	of	20



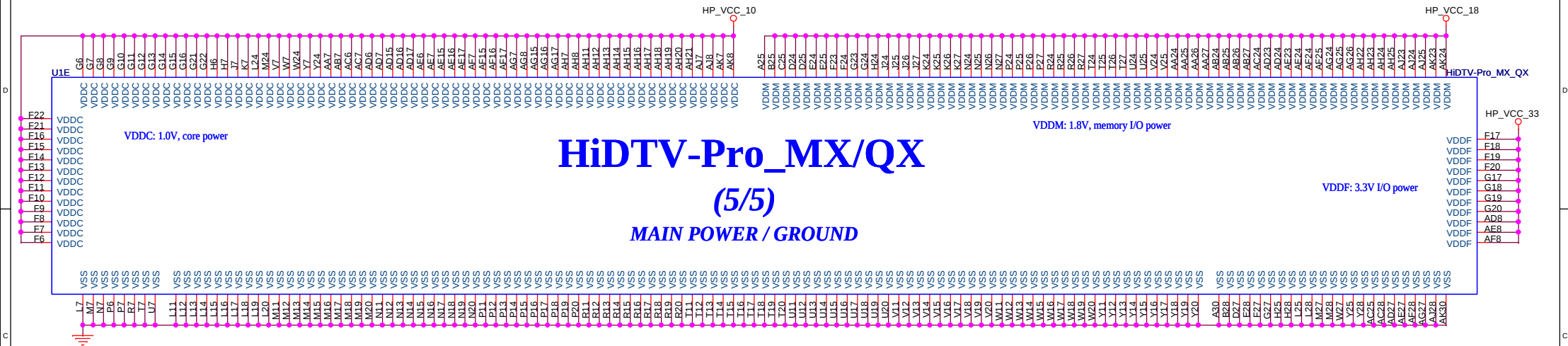
PRELIMINARY

			Trident Digital Media, Inc. 3408 Garrett Drive, Santa Clara, CA 95054-2803 WWW.TRIDENTMICRO.COM		
Title 03:HiDTV-Pro_MX/QX_3					
Size	Schematic Name				Rev
B	AT811				A
Date:	Thursday, March 20, 2008		Sheet	3	of 20

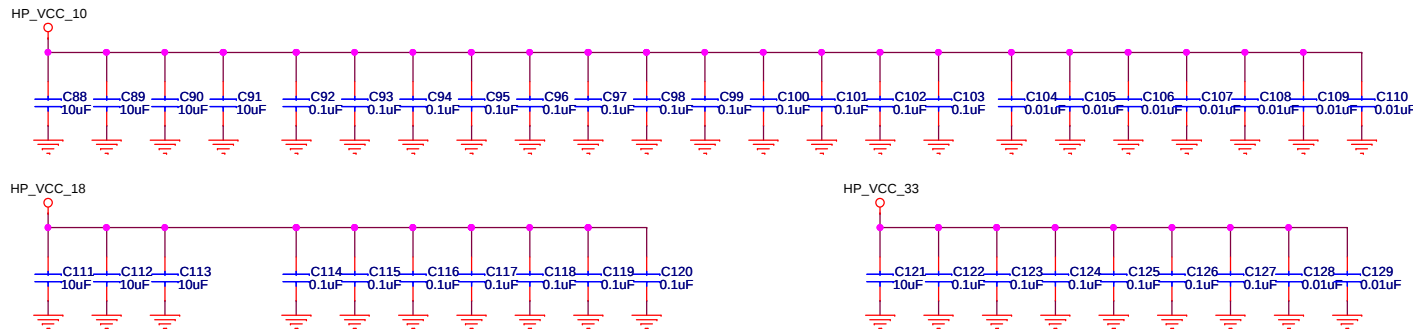
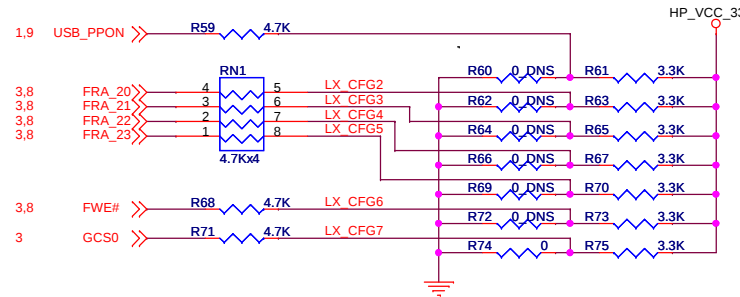


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http://www.tv160.net

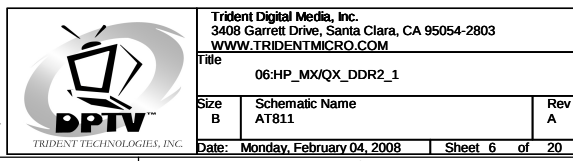


No	PIN	Configure Pin	Description & Setting
1	USB_PPON	ROMSIZE	boot rom width, 0 : 8bit 1 : 16bit
2	GCS0	EMA_CONFIG	Default: 0
3	FWE	FLASH_EPCI_EN	0: PCI 1: FLASH, default connect to 0
4	FRA20	CONM_SN	0: slaver 1: master, default connect to 1
5	FRA21	ENDIAN_SEL[0]	
6	FRA22	ENDIAN_SEL[1]	Default: 2'b00
7	FRA23	I2C_M8051_SEL	1: To enable 8051 master/slaver 0: Disable 8051 master/slaver

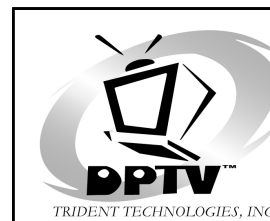
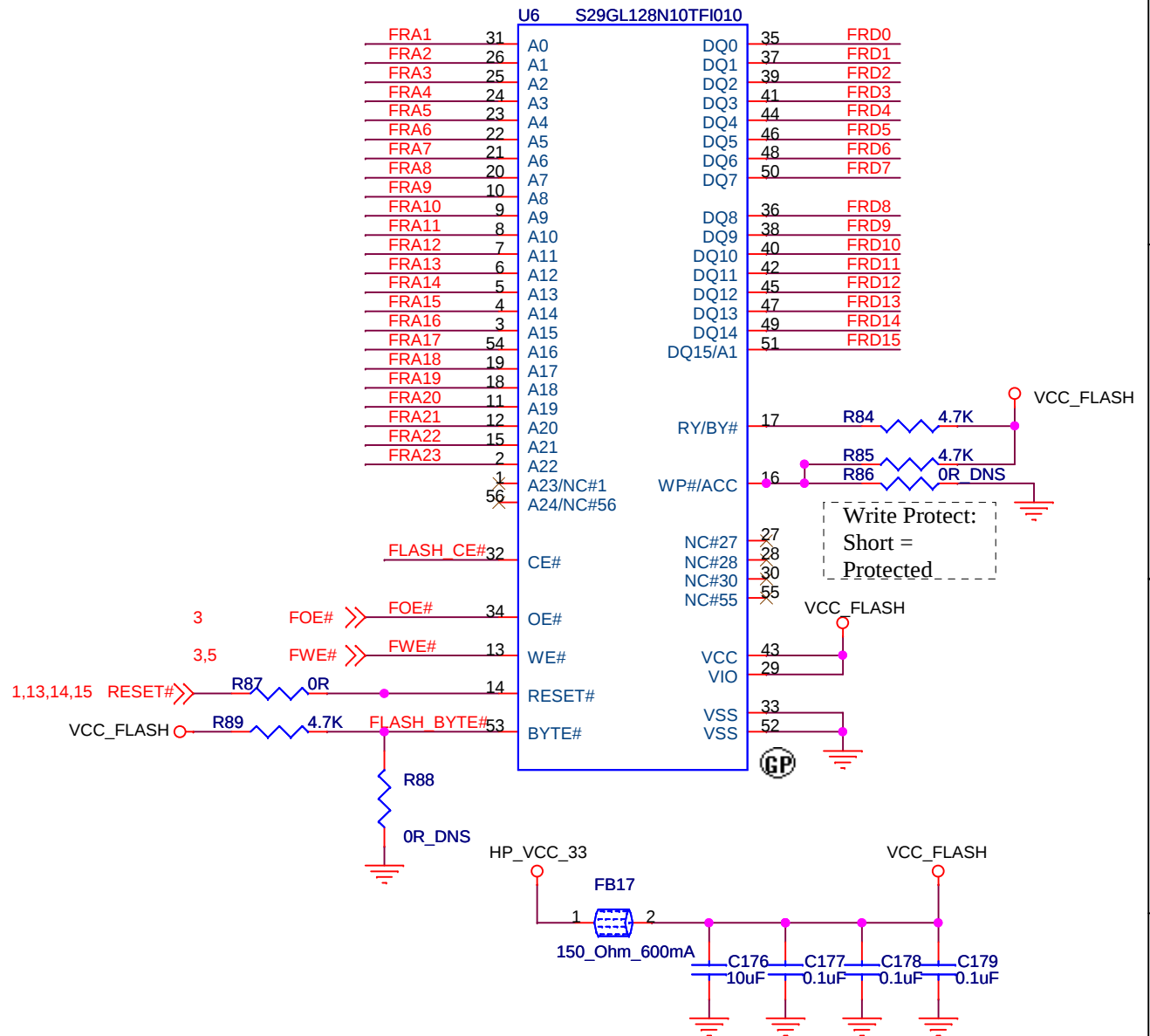
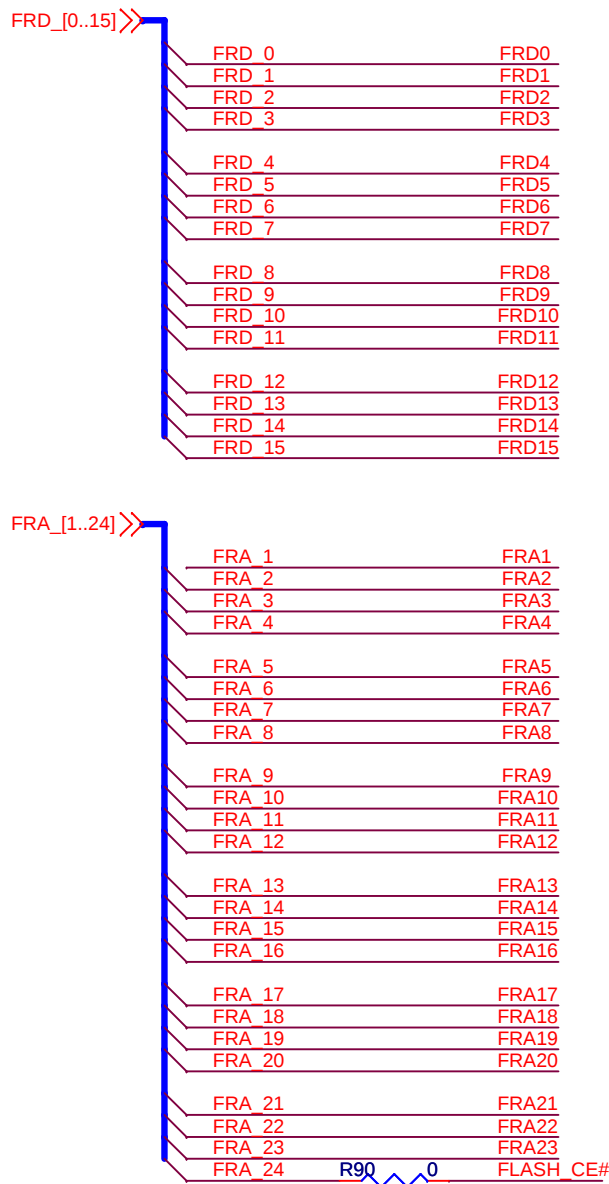


PRELIMINARY

			Trident Digital Media, Inc. 3408 Garrett Drive, Santa Clara, CA 95054-2803 WWW.TRIDENTMICRO.COM	
			Title 05:HiDTV-Pro_MX/QX_5	
Size	B	Schematic Name		Rev
		AT811		A
Date:	Tuesday, March 18, 2008	Sheet	5	of 20







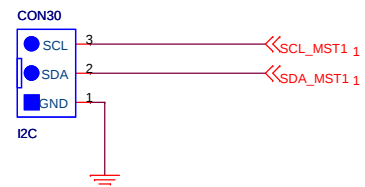
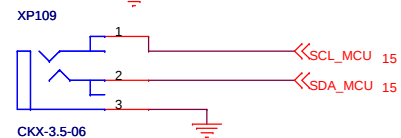
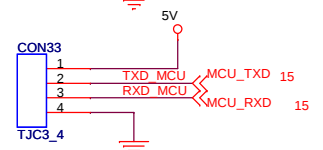
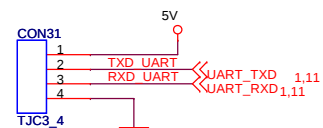
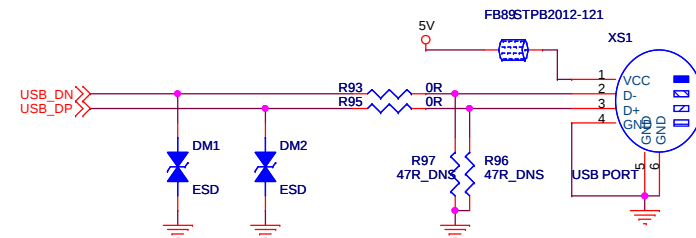
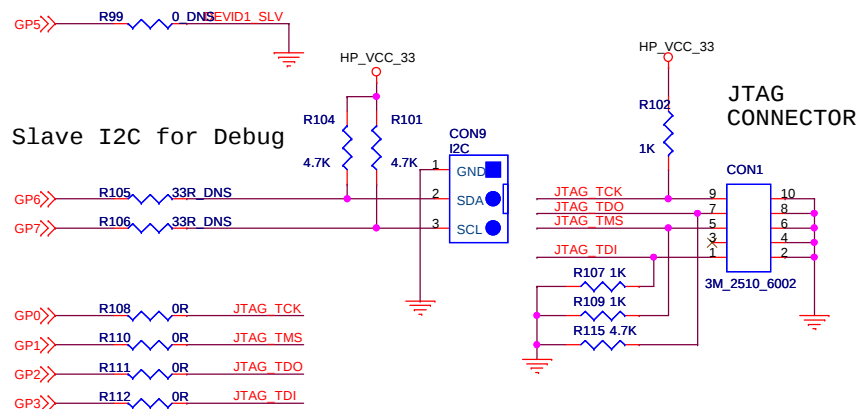
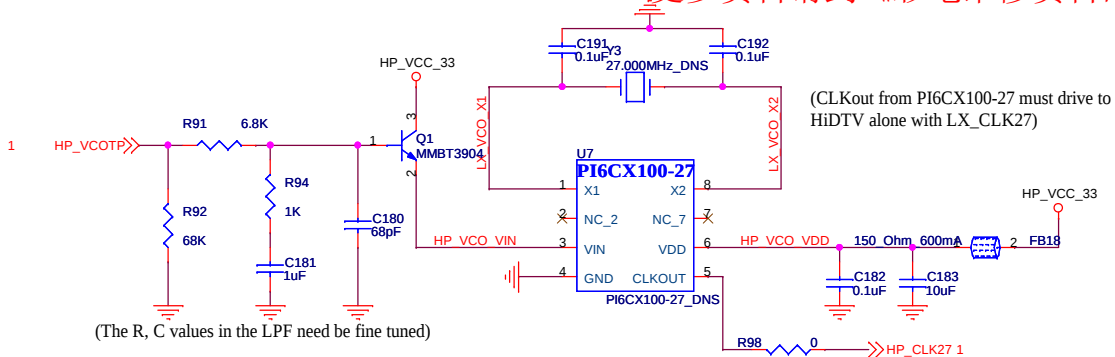
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Title
08:Pro_MX/QX_ROM/FLASH

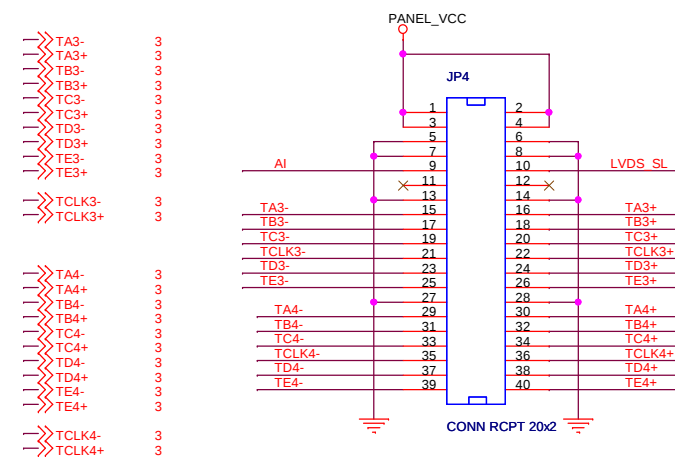
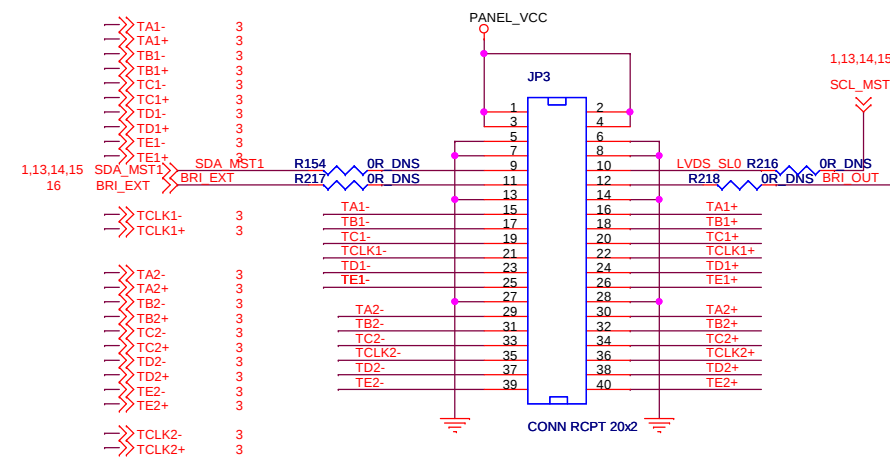
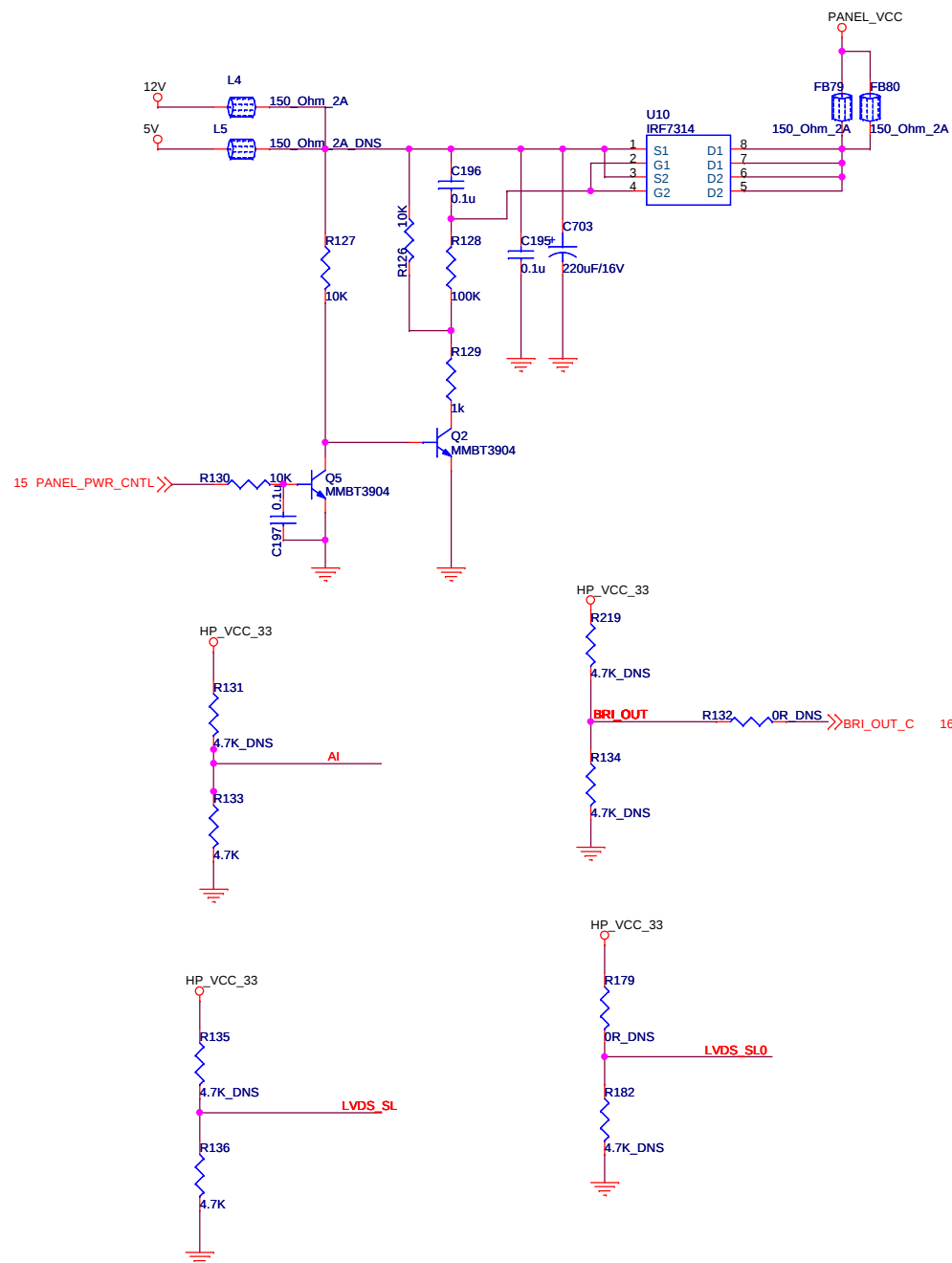
Size
A

Schematic Name
AT811

Date: Thursday, March 20, 2008



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Title			09:Peripheral		
Size	B	Schematic Name	AT811	Rev	A
Date: Thursday, July 03, 2008			Sheet 9 of 20		



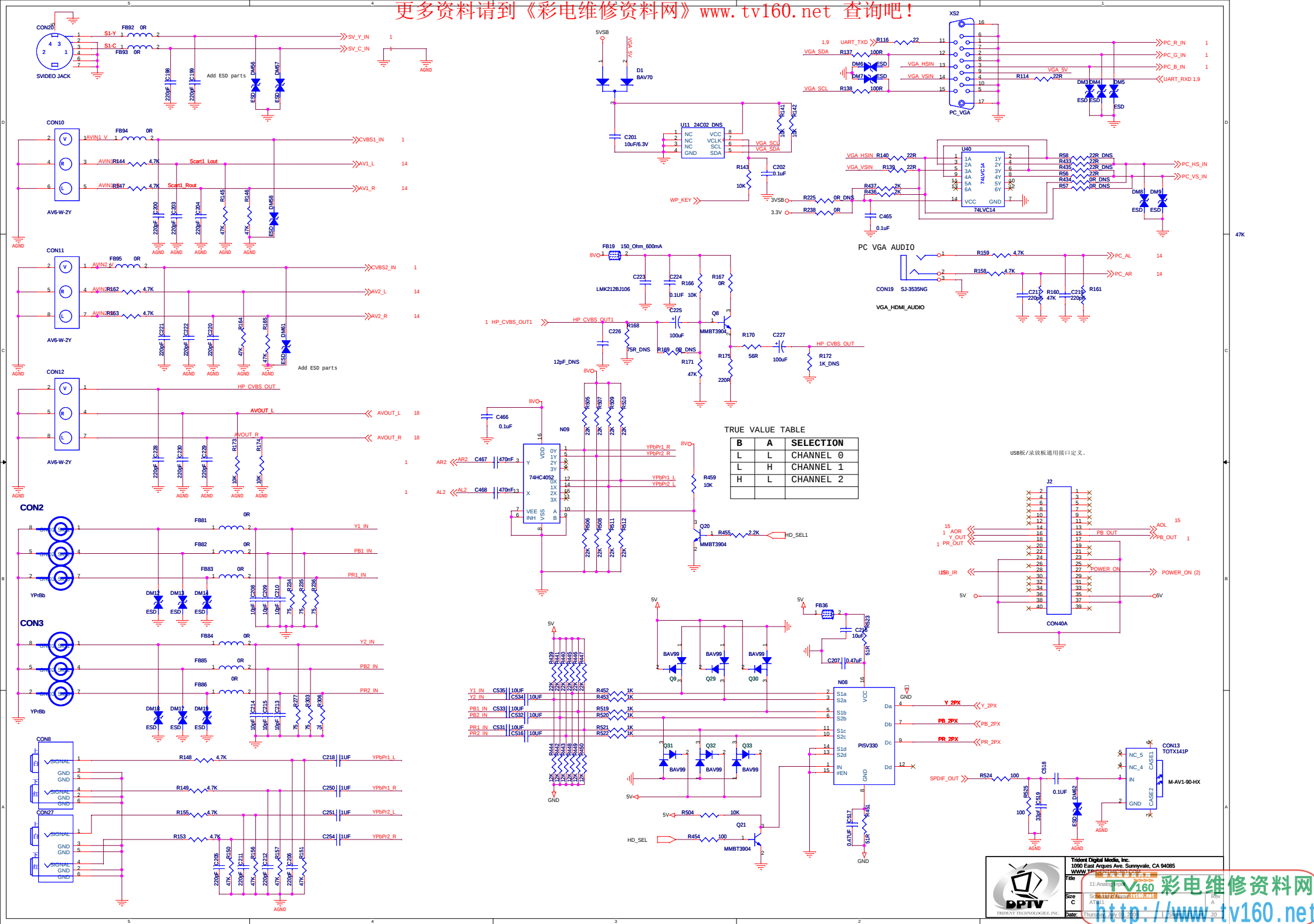
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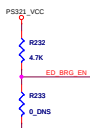
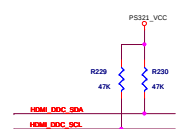
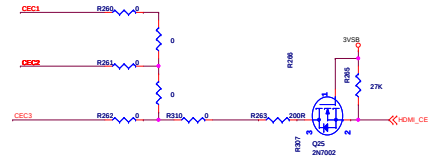
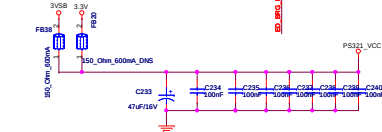
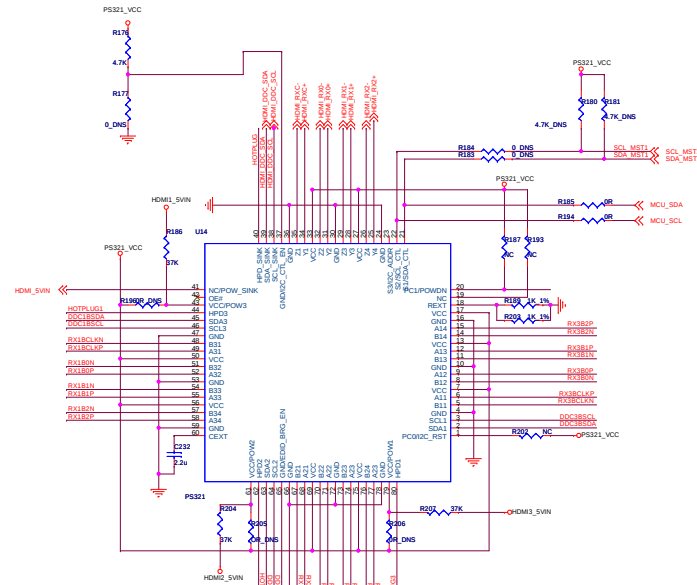
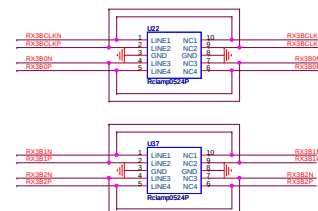
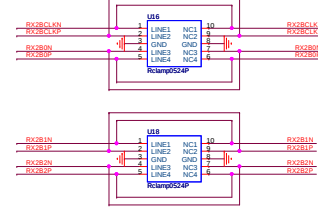
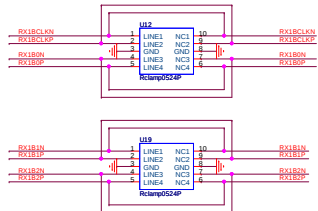
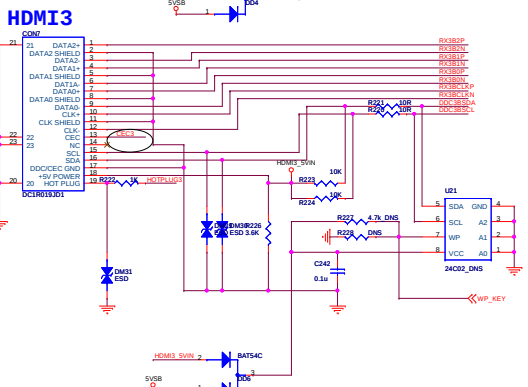
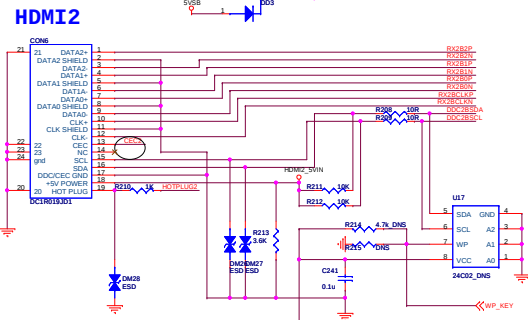
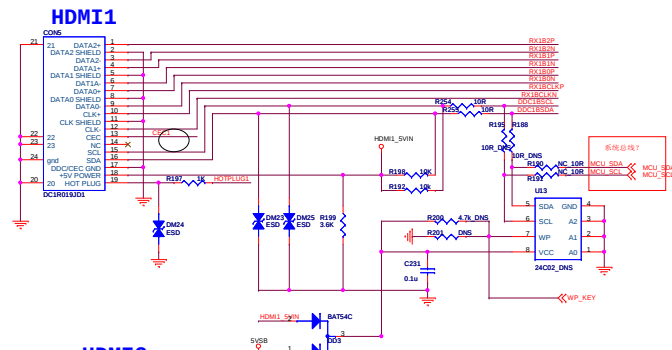
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Title
10:LVDS OUT

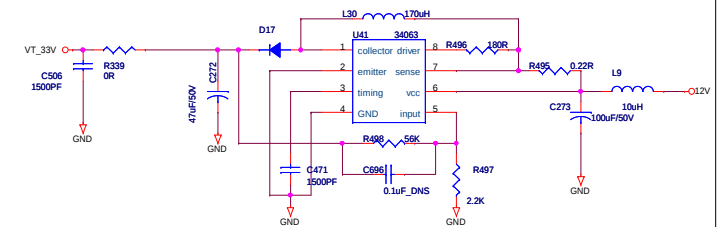
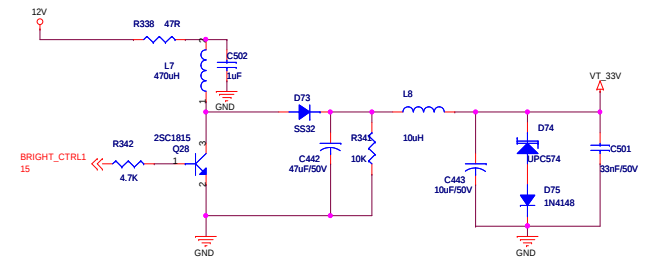
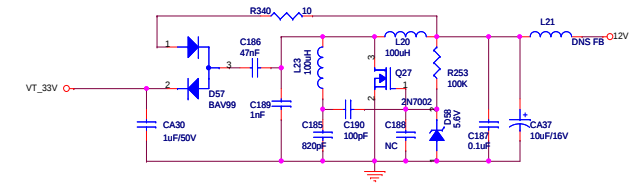
Size B	Schem AT811
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Date: Thursday, July 03, 2008

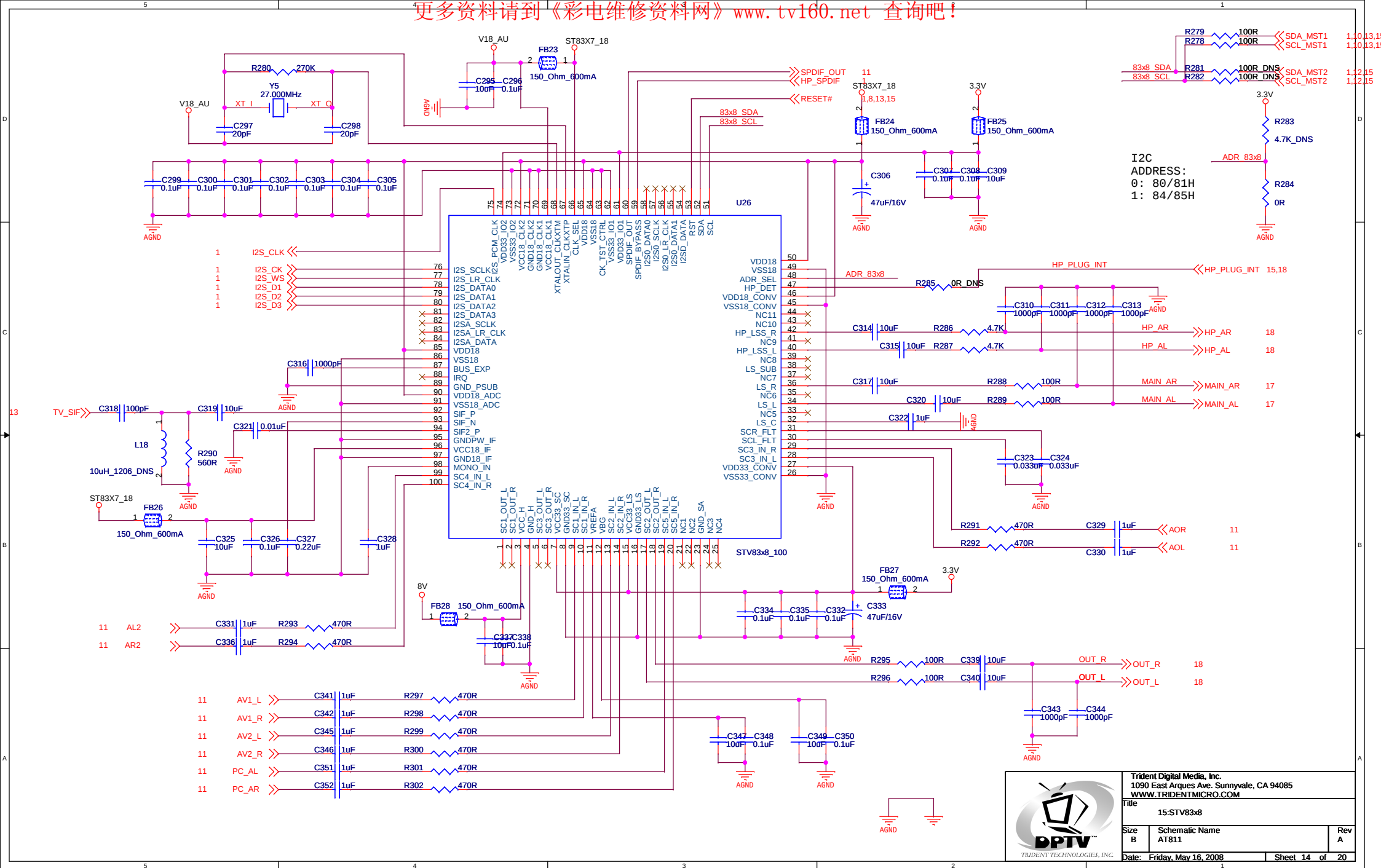
Rev
A

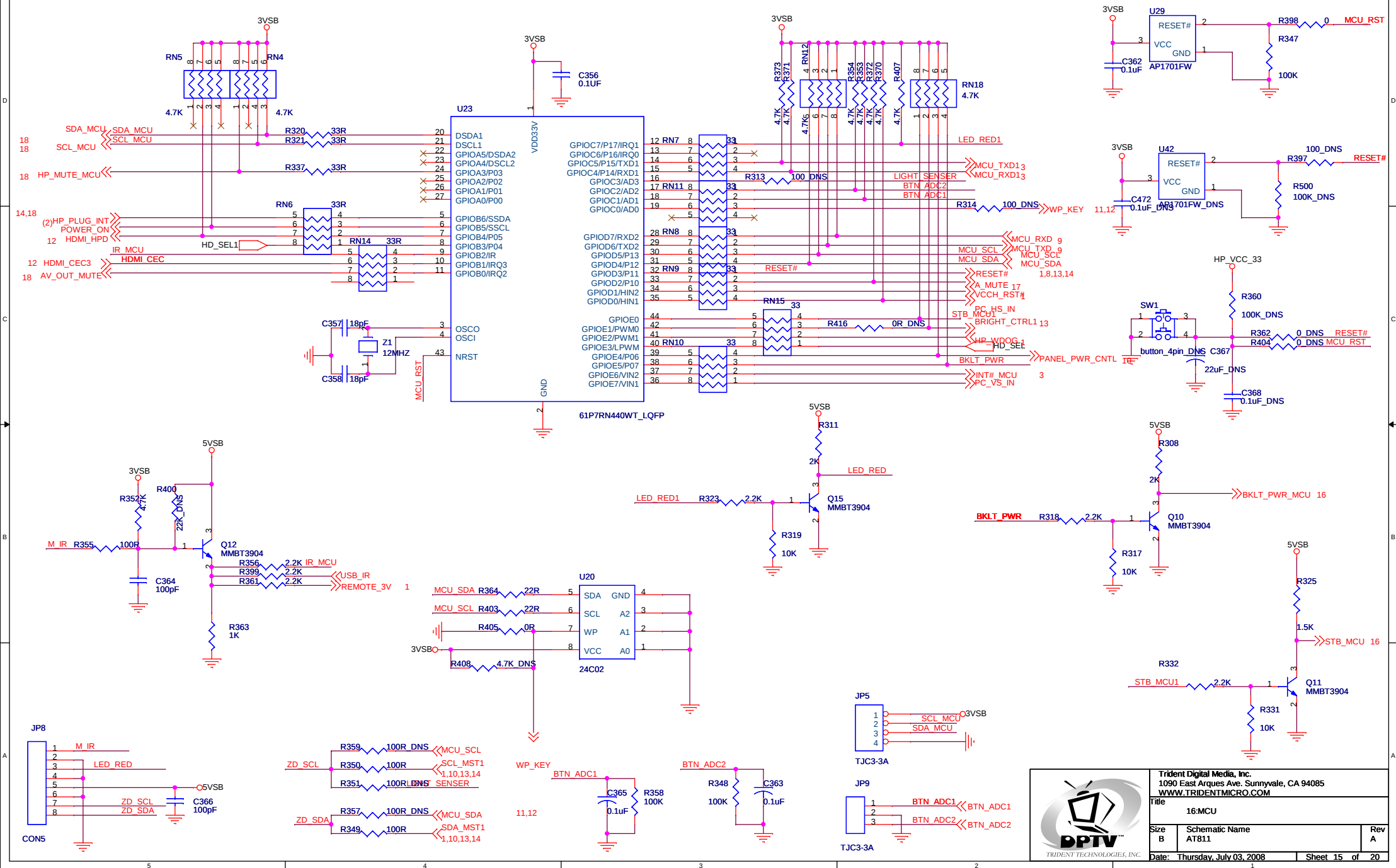




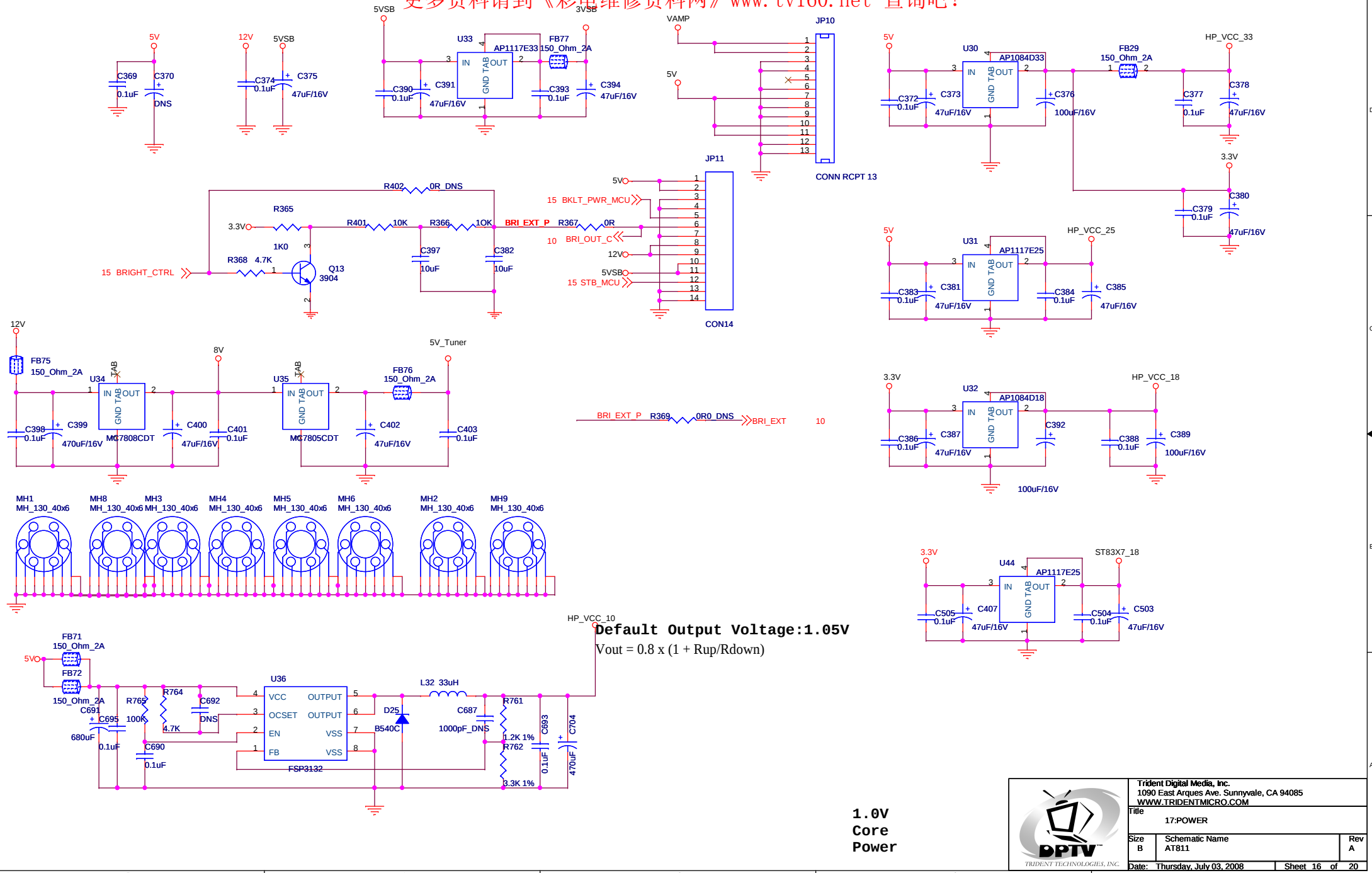


	OP1	OP2
D/K B/G I	H	H
M/N	L	L

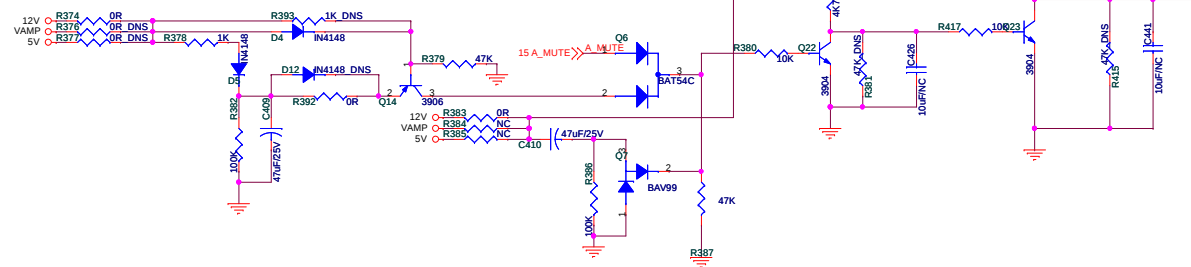




		
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Title: 16:MCU		
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Date: Thursday, July 03, 2008	Sheet 15 of 20	

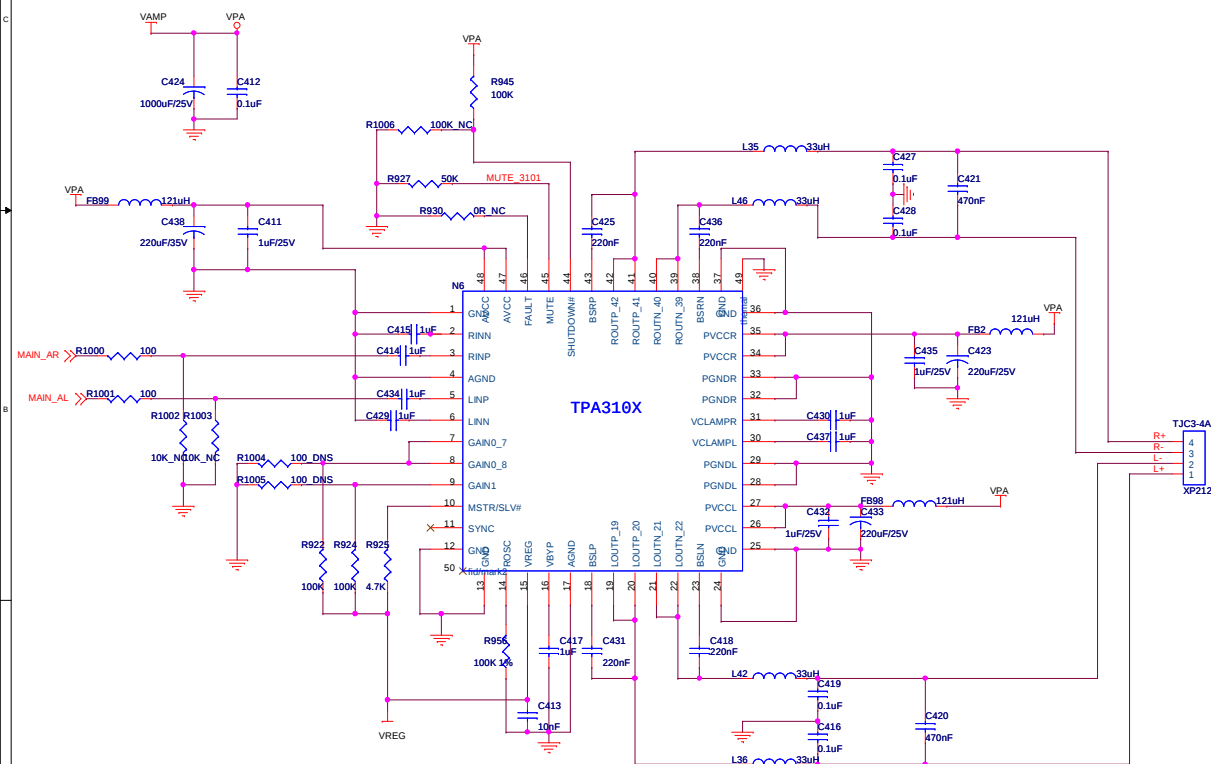


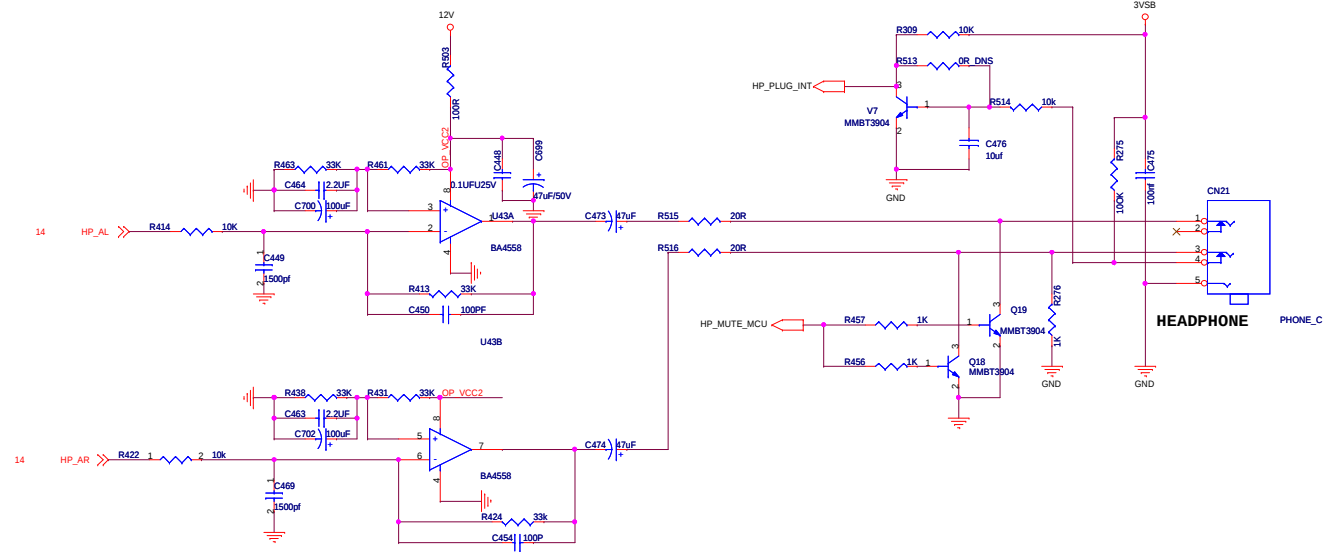
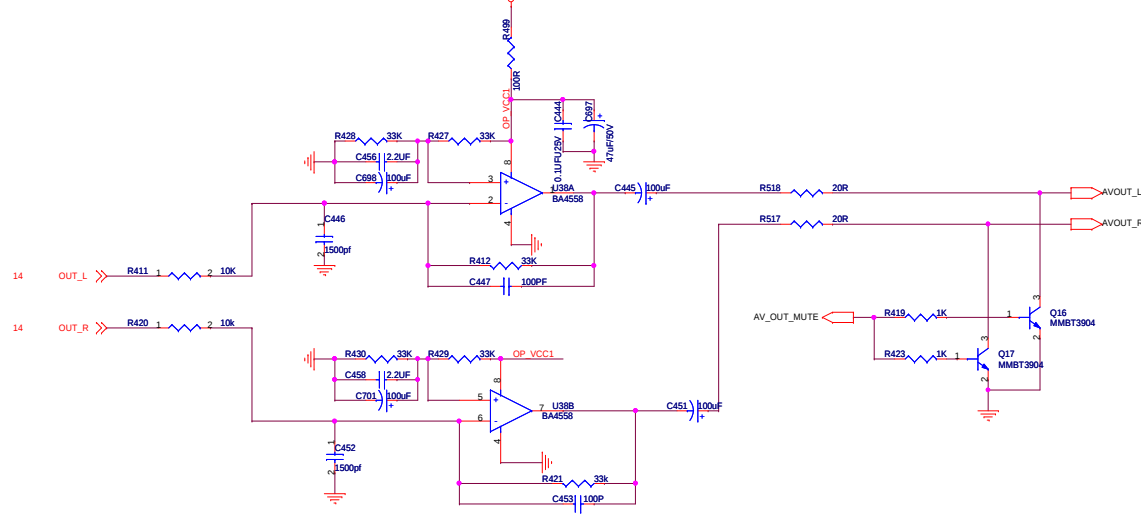
		
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Title: 17:POWER		
Size B	Schematic Name AT811	Rev A
Date: Thursday, July 03, 2008	Sheet 16	of 20

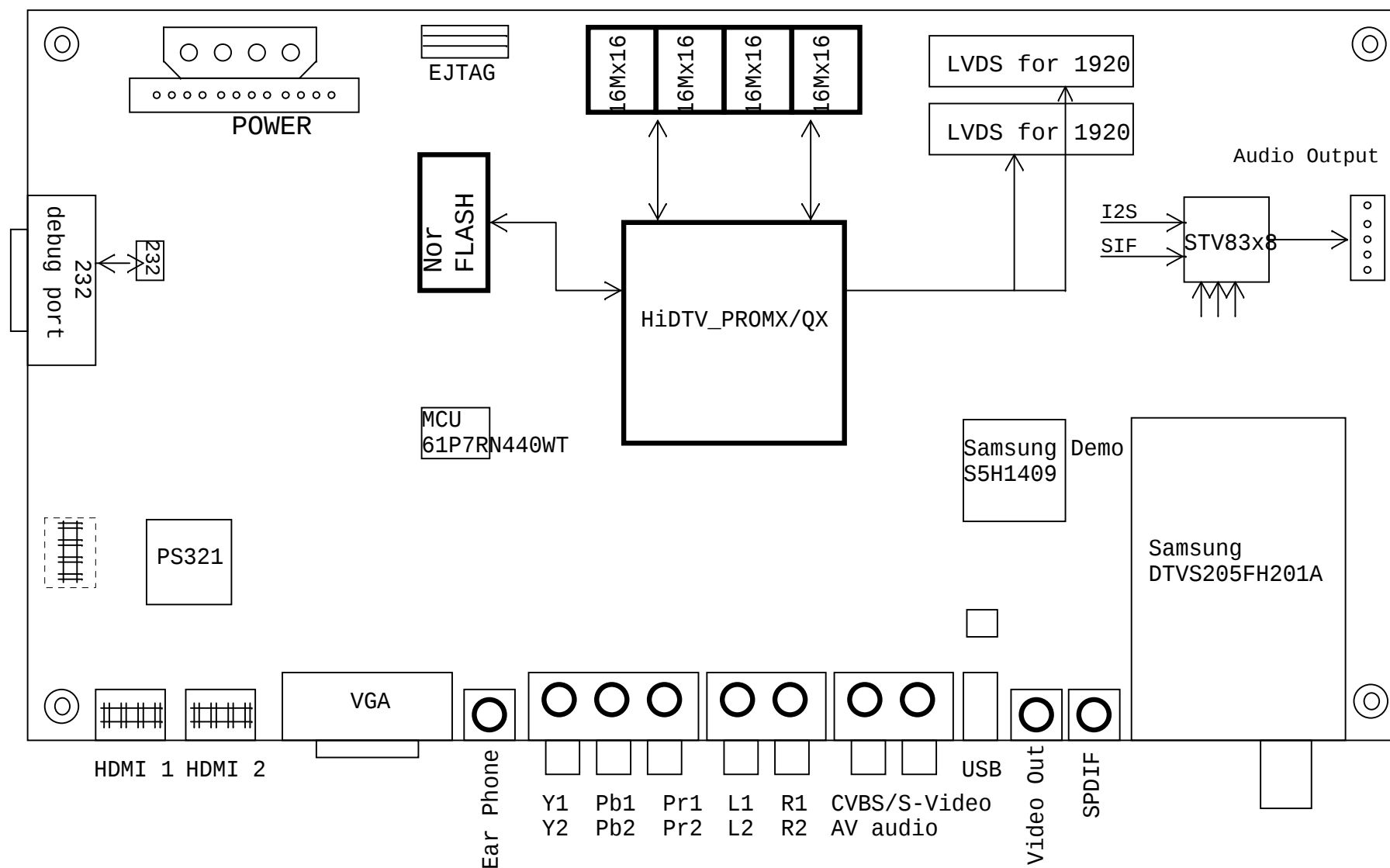


A_MUTE: LOW FOR MUTE

MUTE_3101: HIGH FOR MUTE(>2V)







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Title
19:Block Diagram

Size A Schematic Name
AT8101

Date: Friday, January 18, 2008