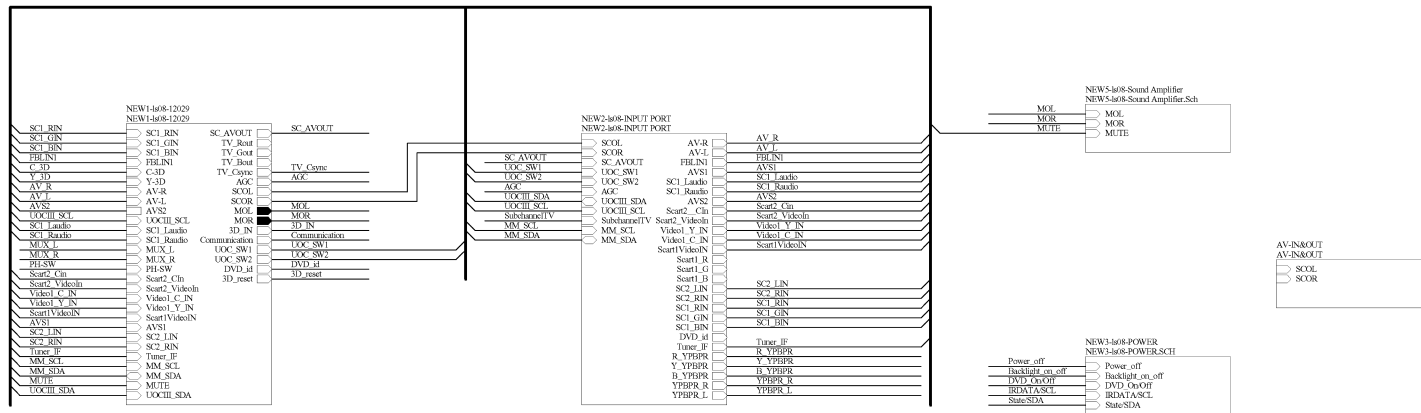
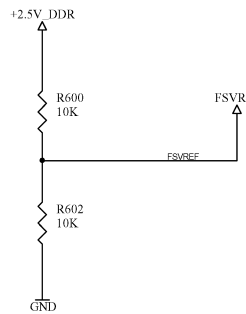
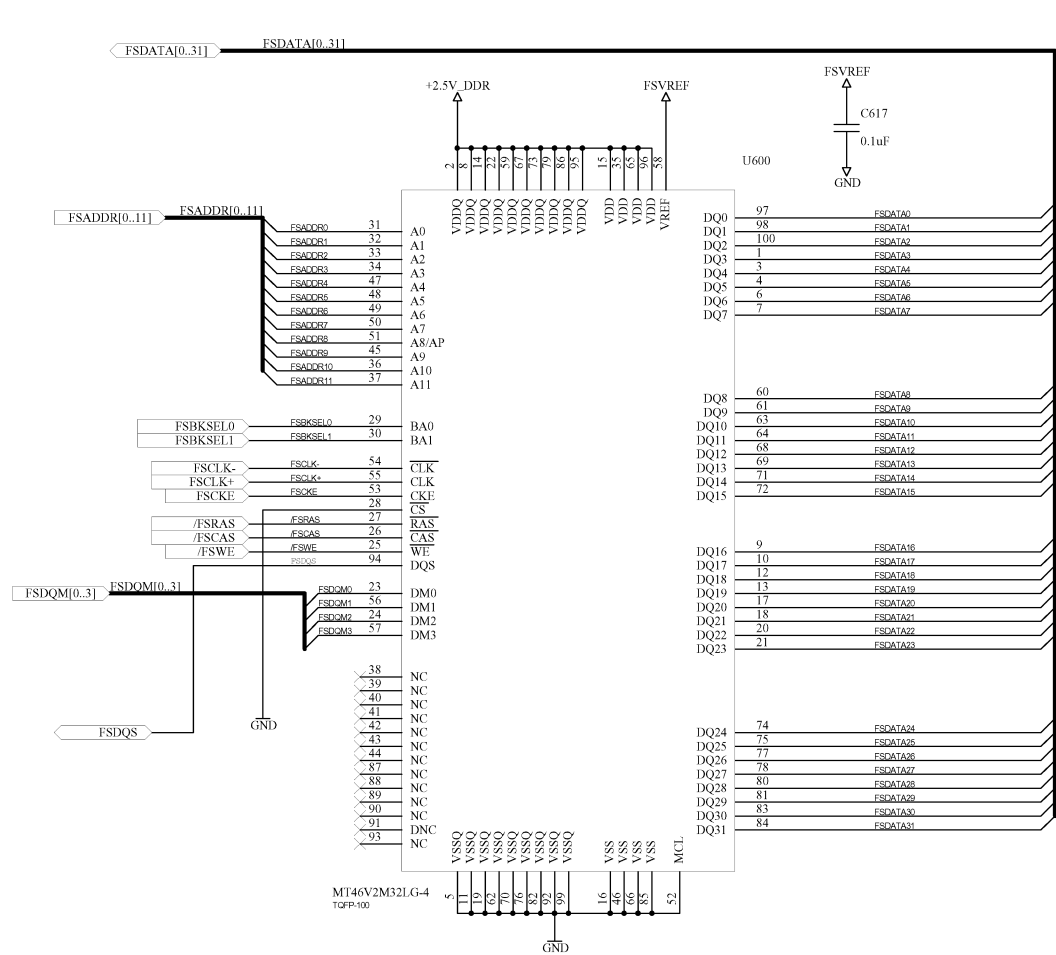
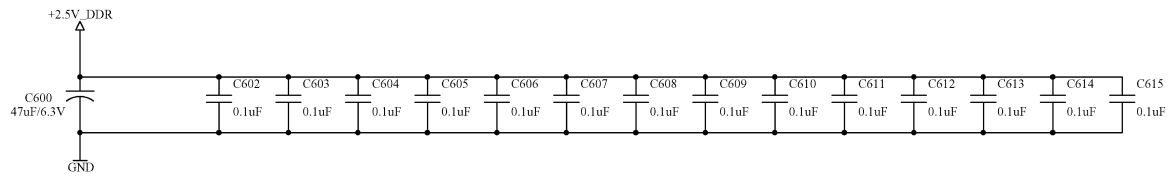
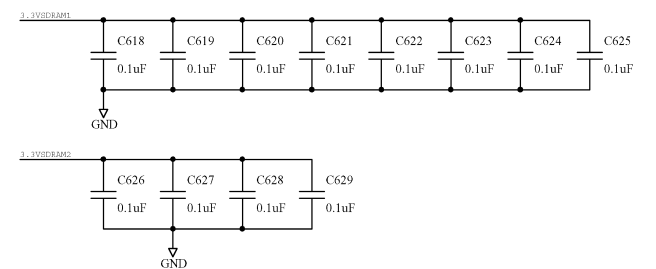
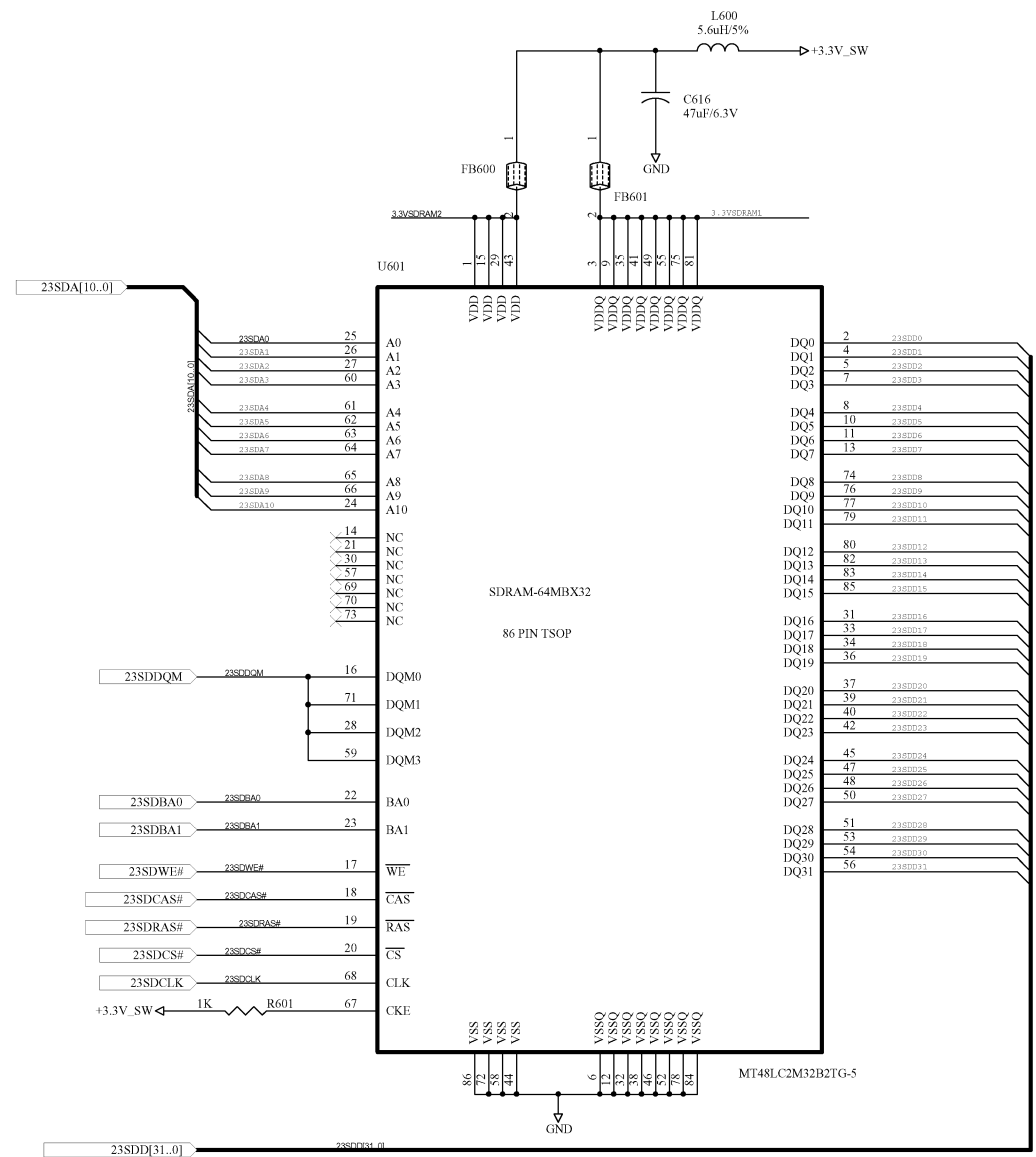
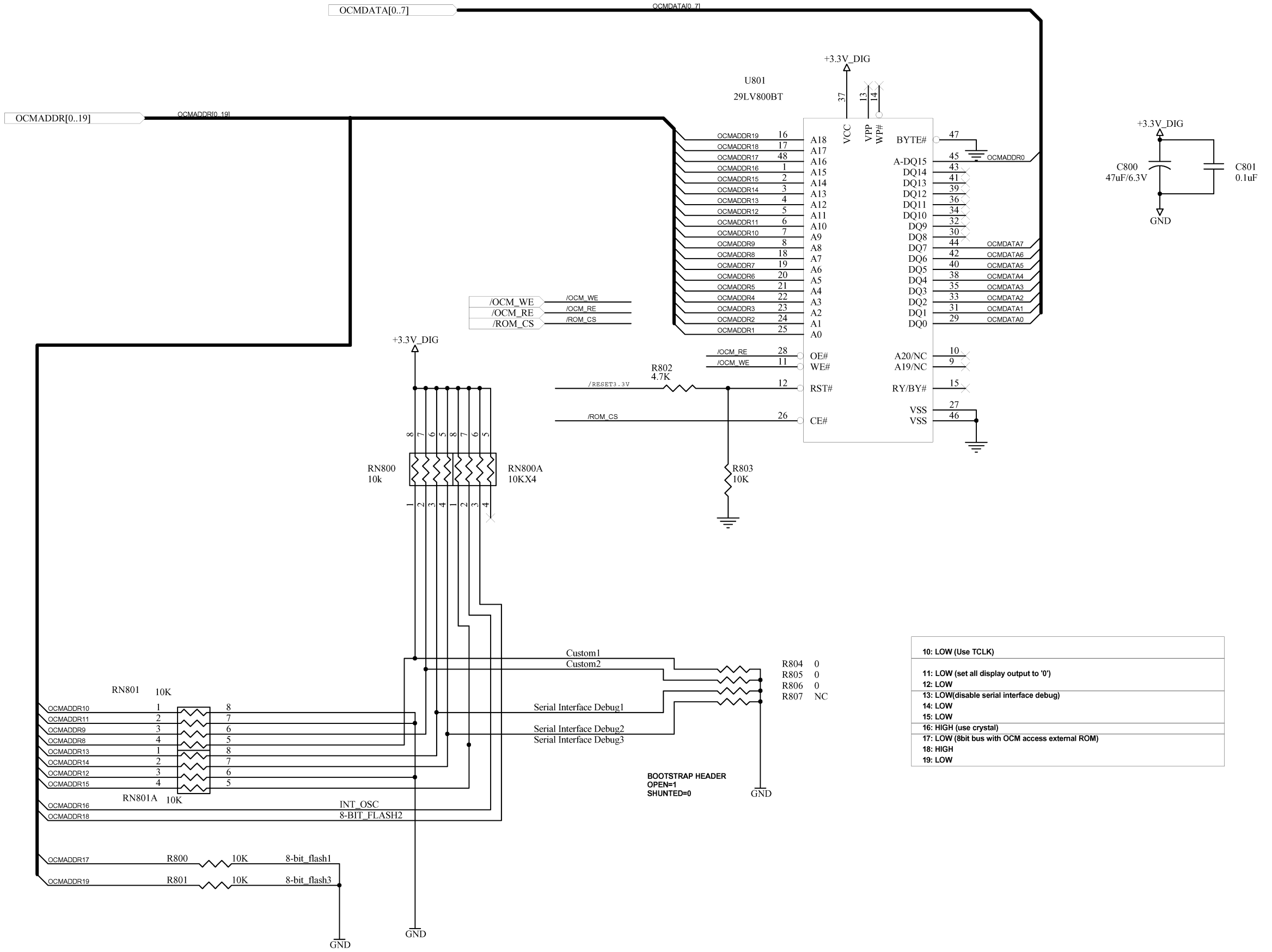


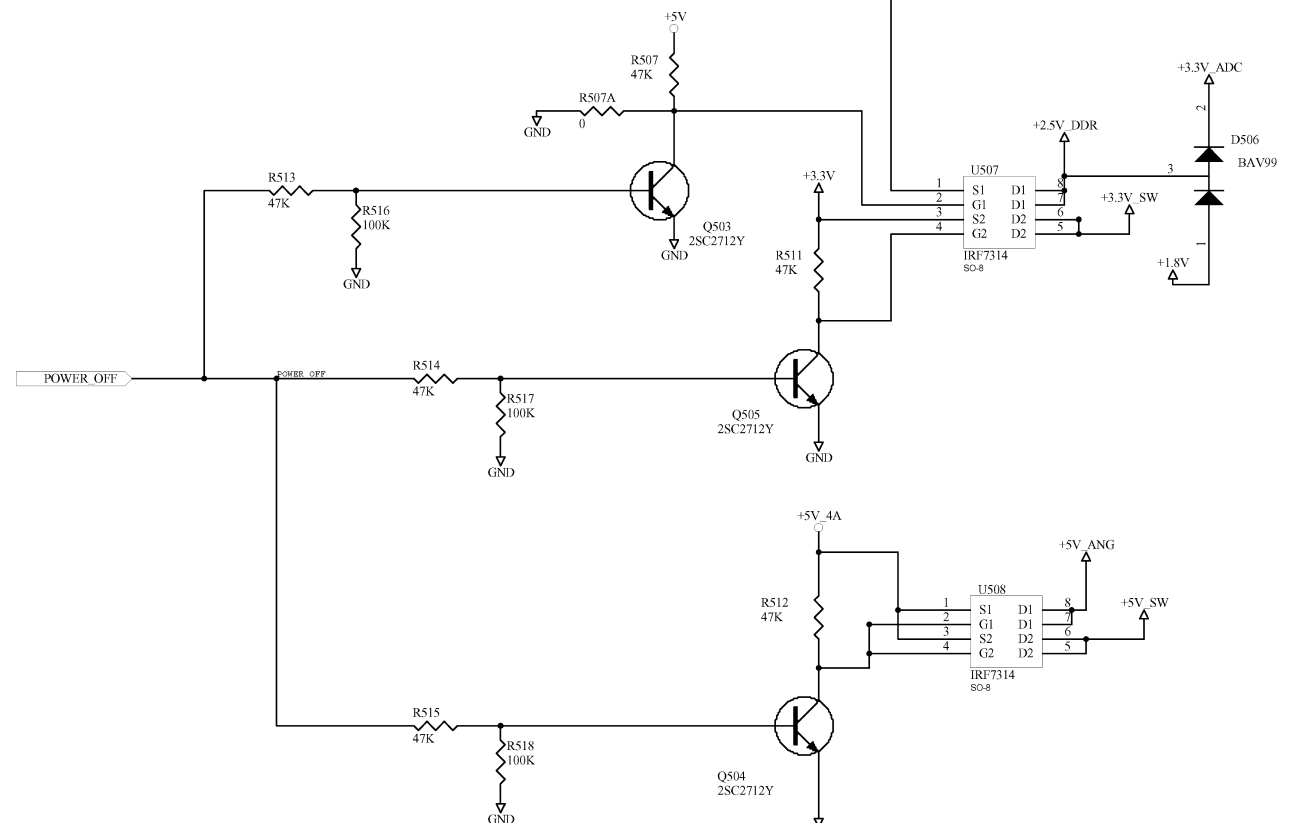
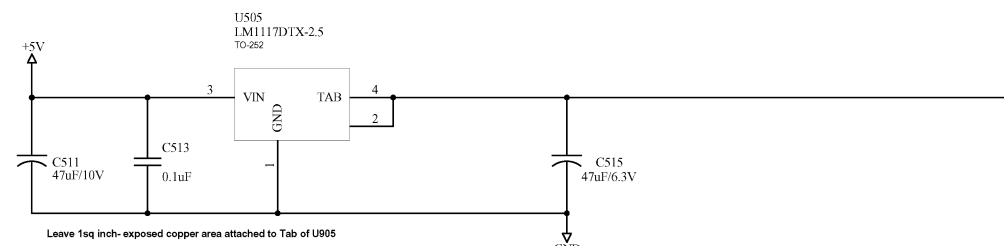
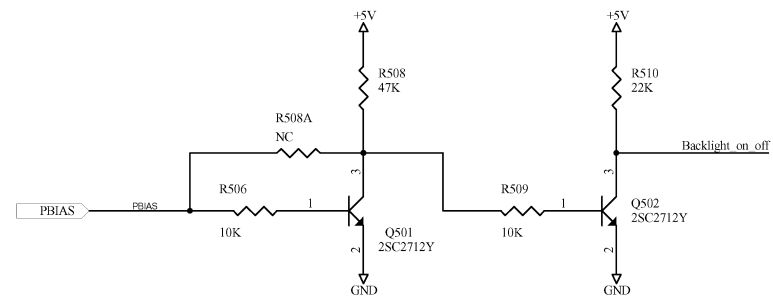
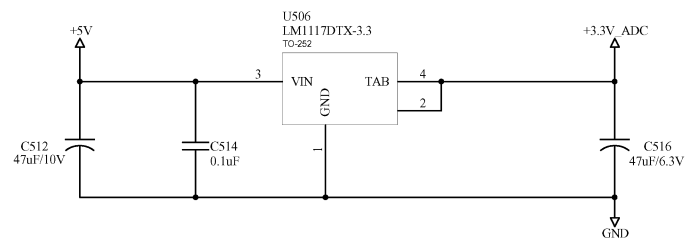
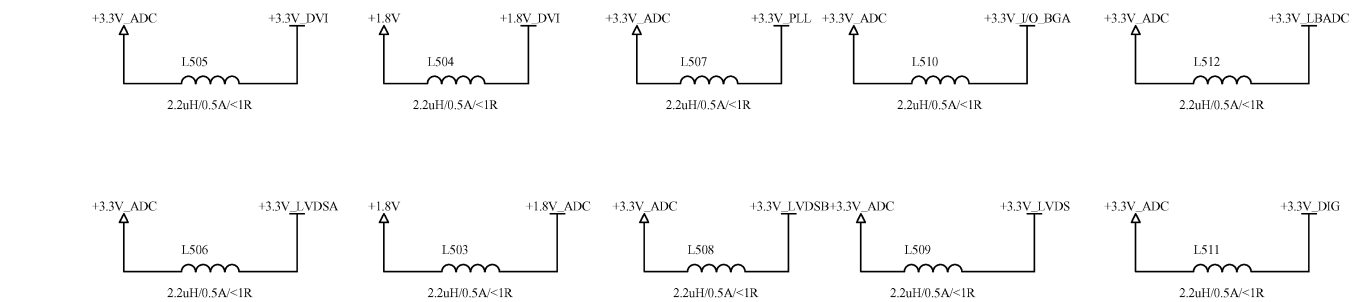
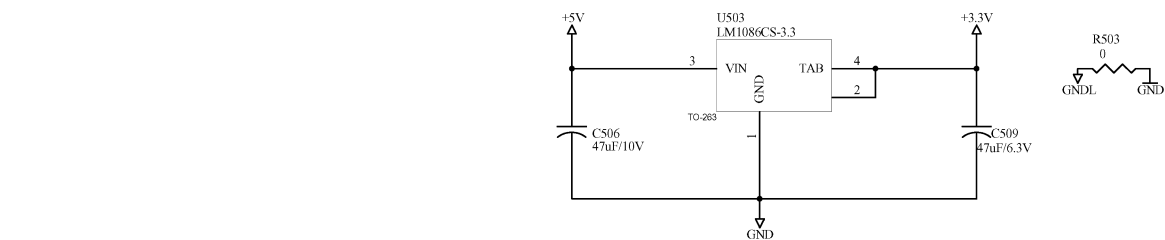


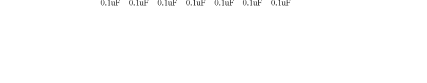
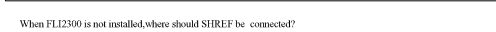
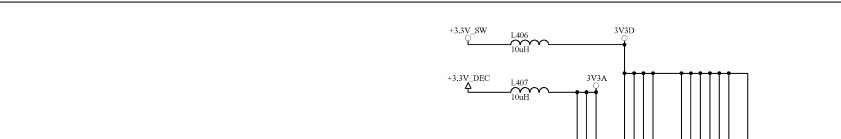
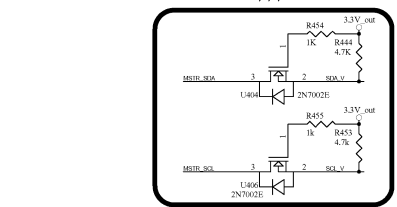


Place this parallel termination close to corresponding memory IC Pins









WHEN FLI2310 IS PRESENT	ASSEMBLE EN700 TO EN707. DO NOT ASSEMBLE EN710, EN711,EN712,EN713
WHEN FLI2310 IS NOT PRESENT	ASSEMBLE EN710, EN711,EN712,EN713 DO NOT ASSEMBLE TO EN700 TO EN707.

